

Fig. 1

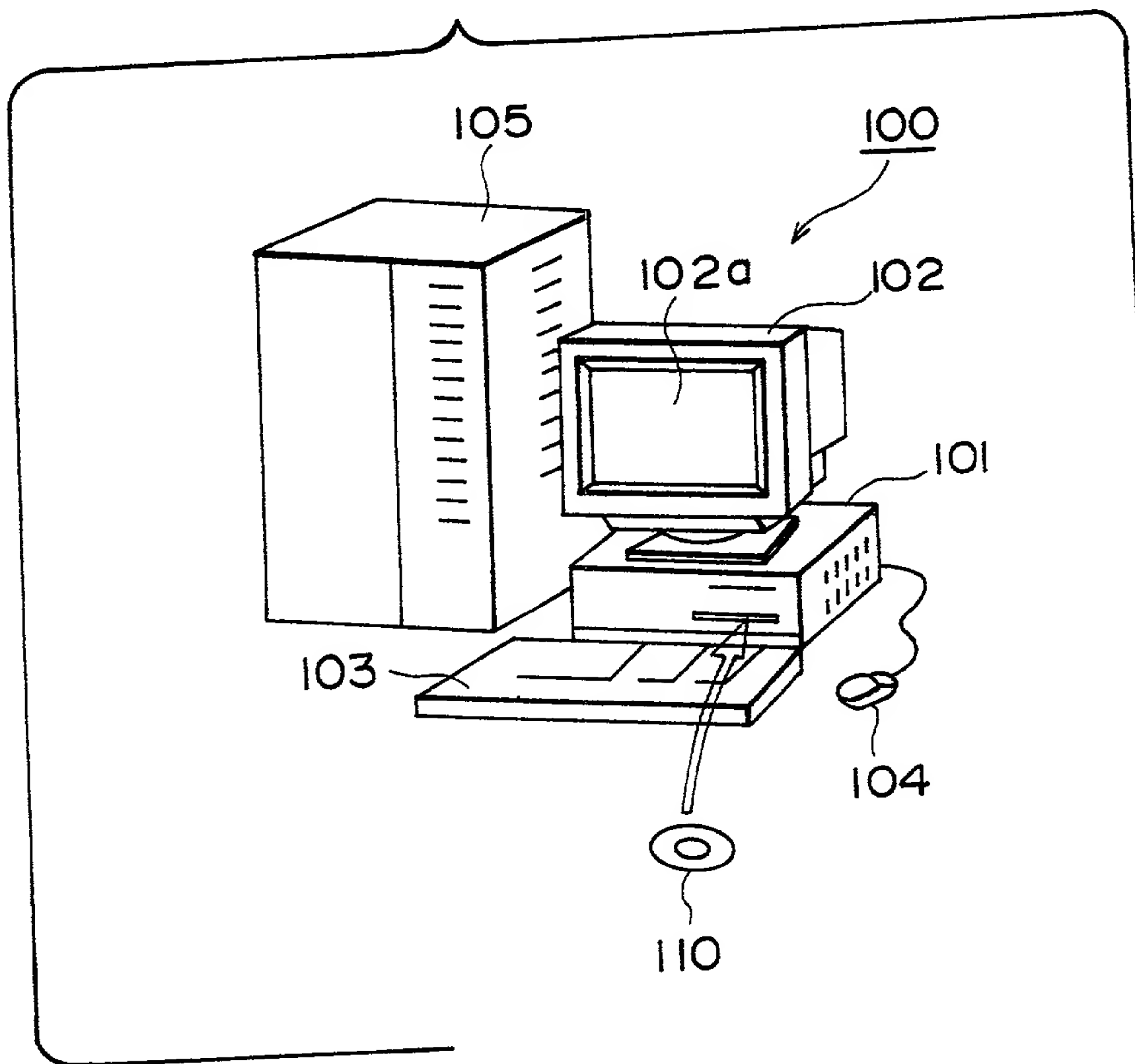


Fig. 2

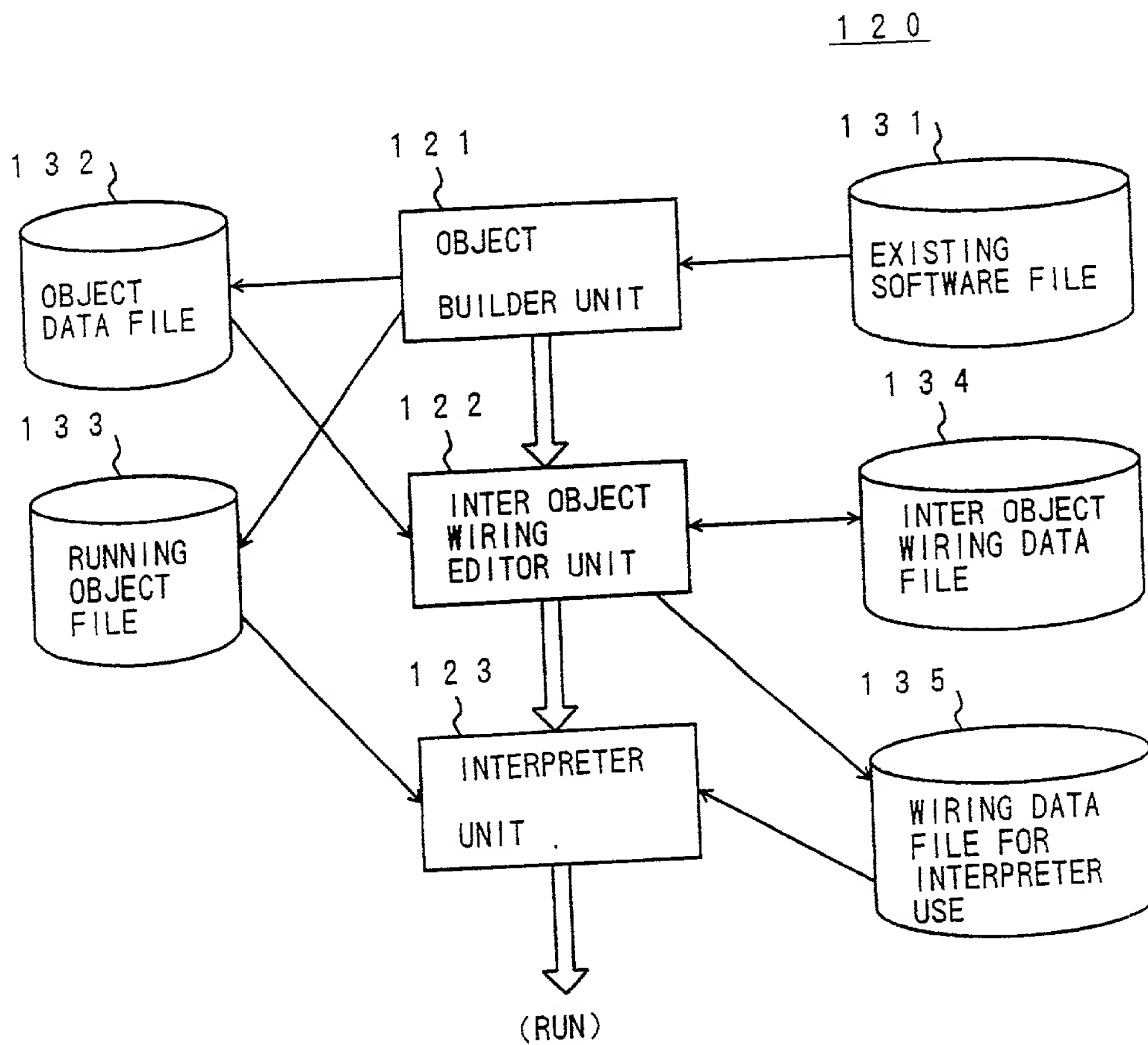


Fig. 3

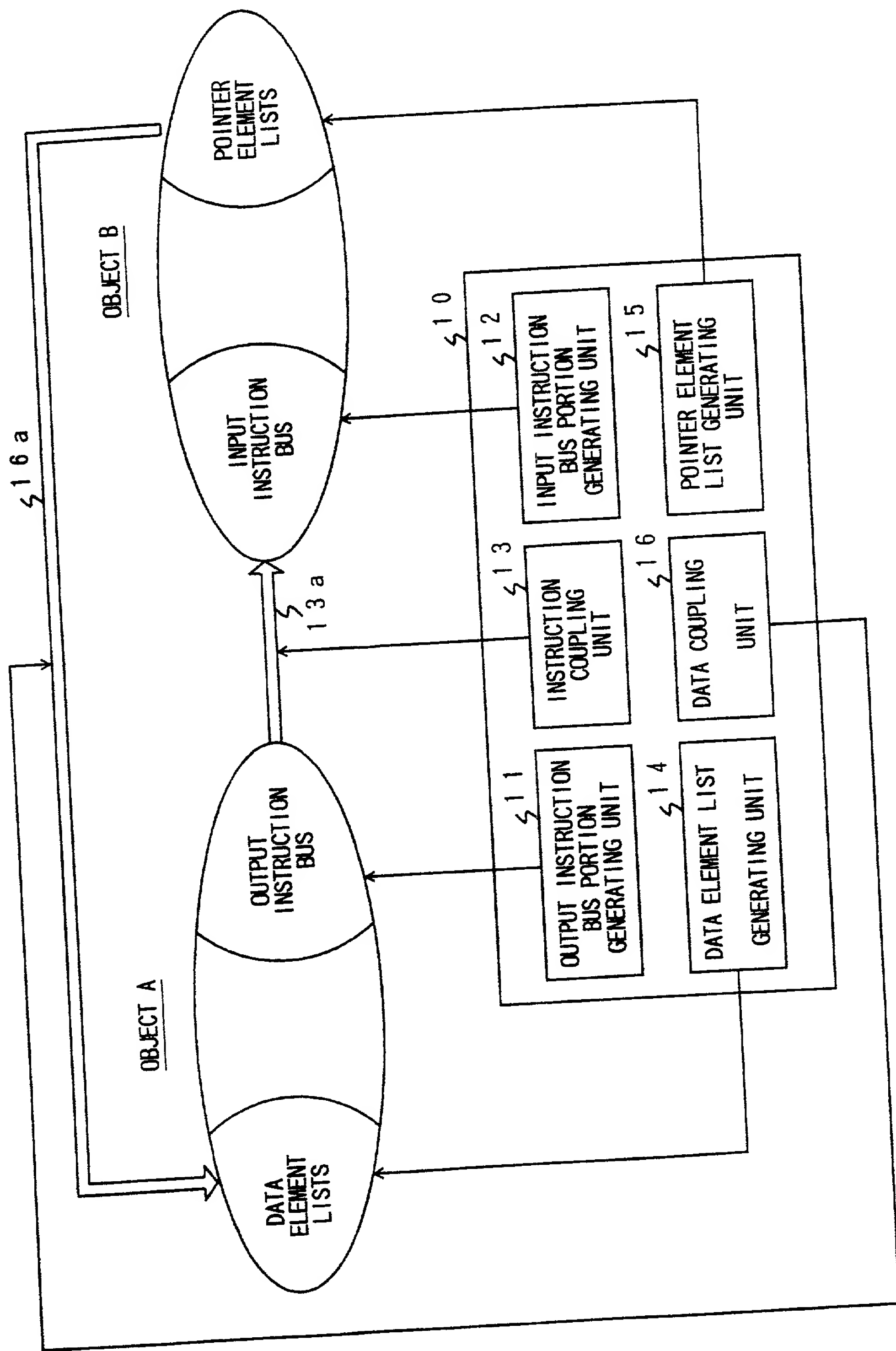


Fig. 4

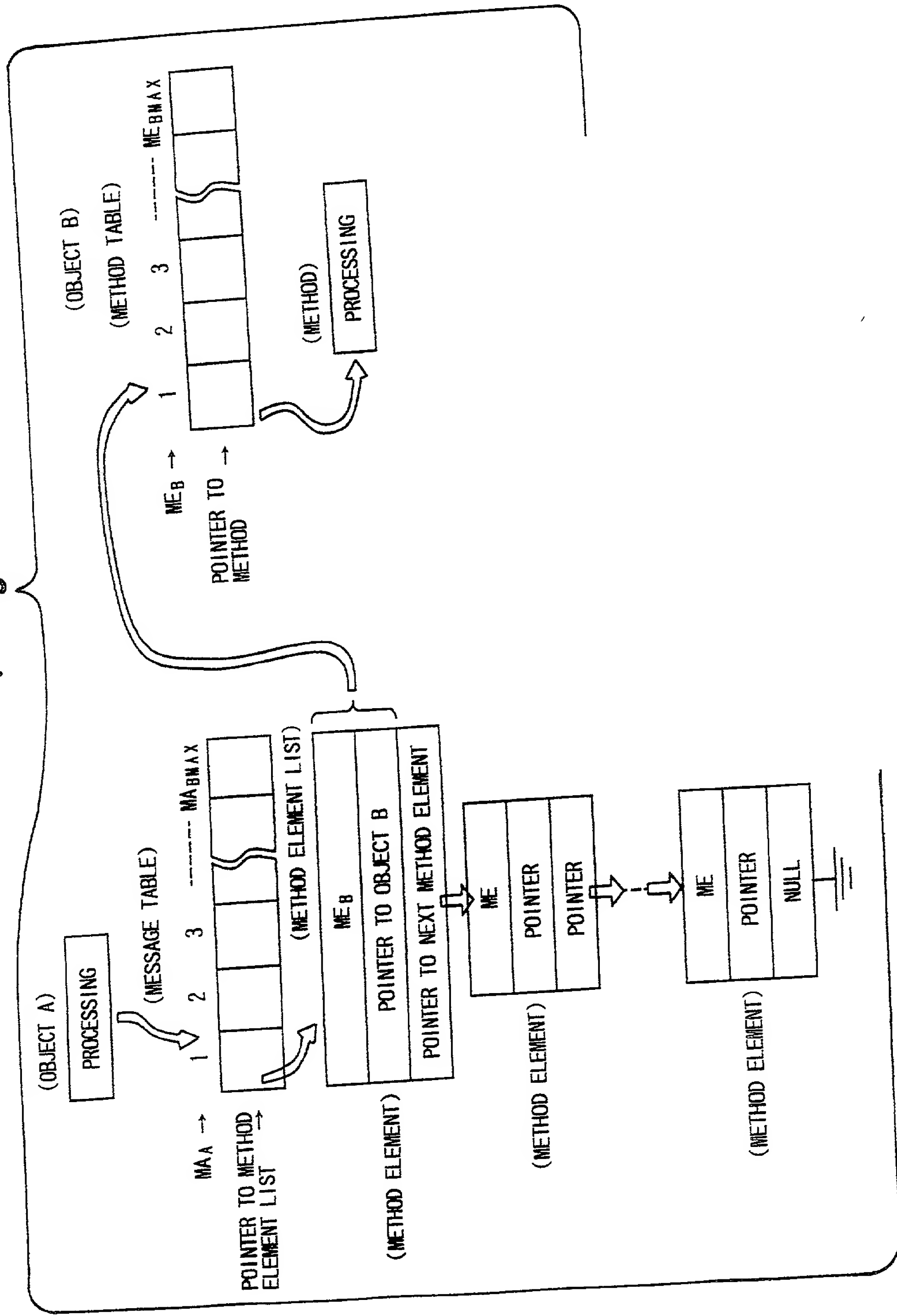


Fig. 5

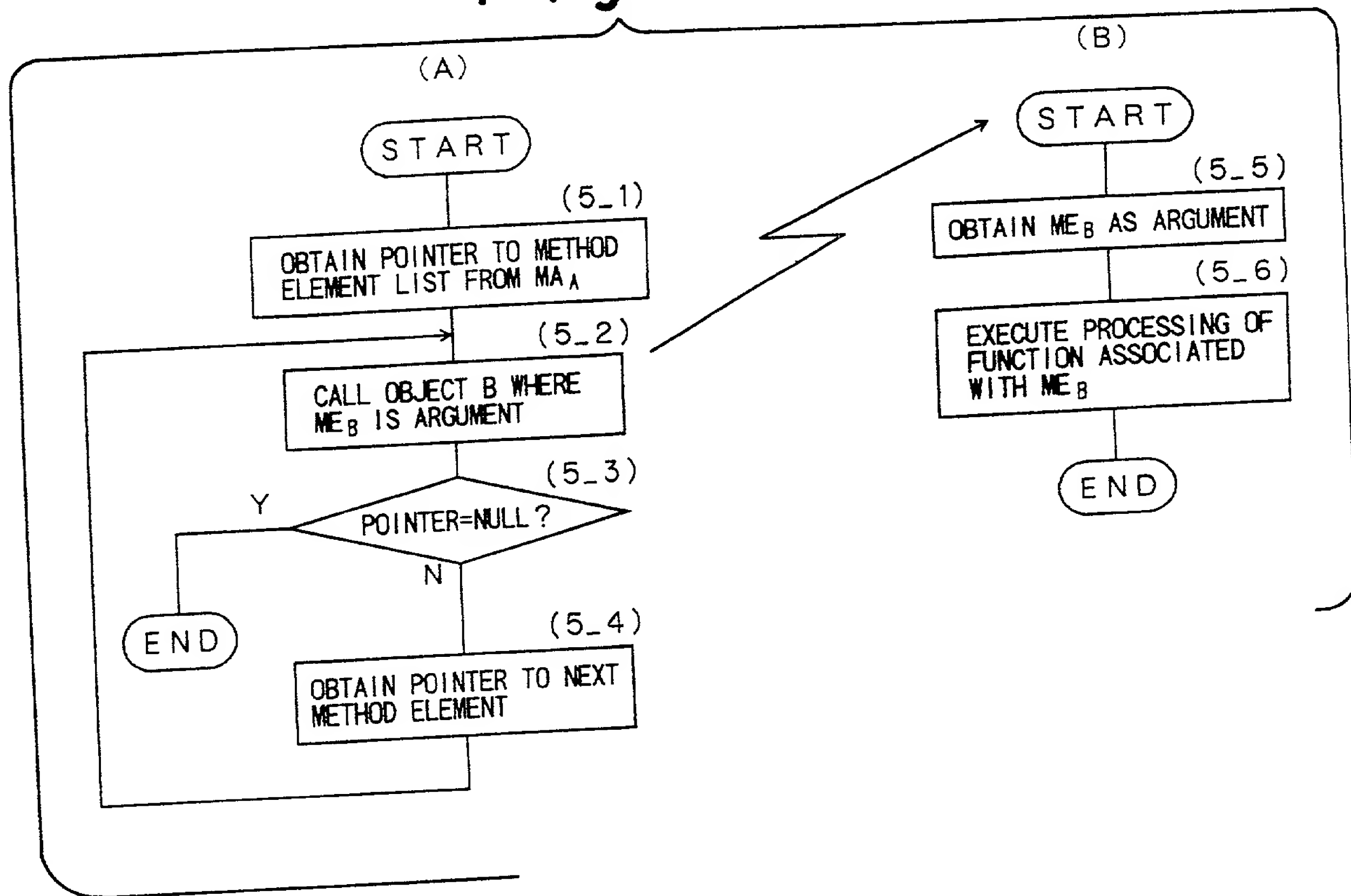


Fig. 6

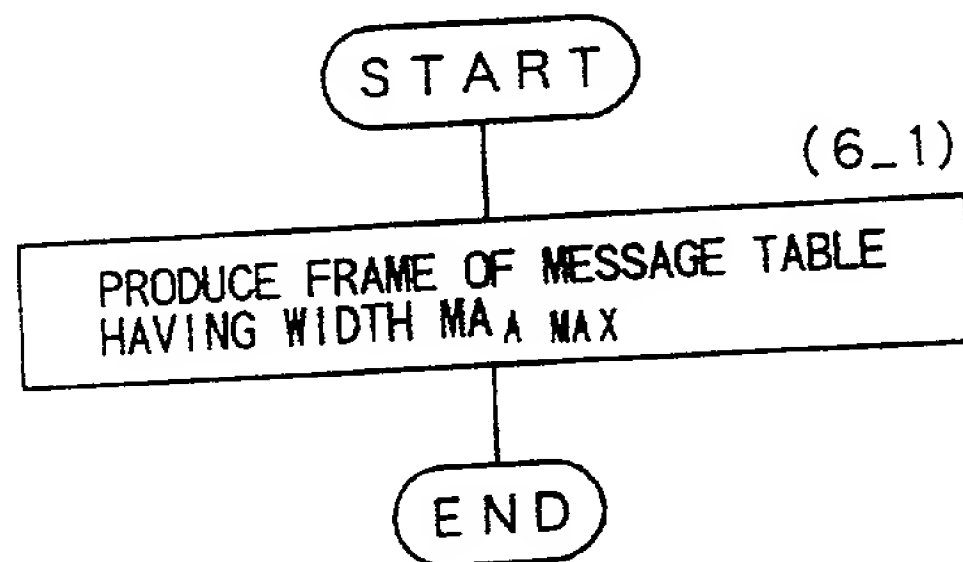


Fig. 7

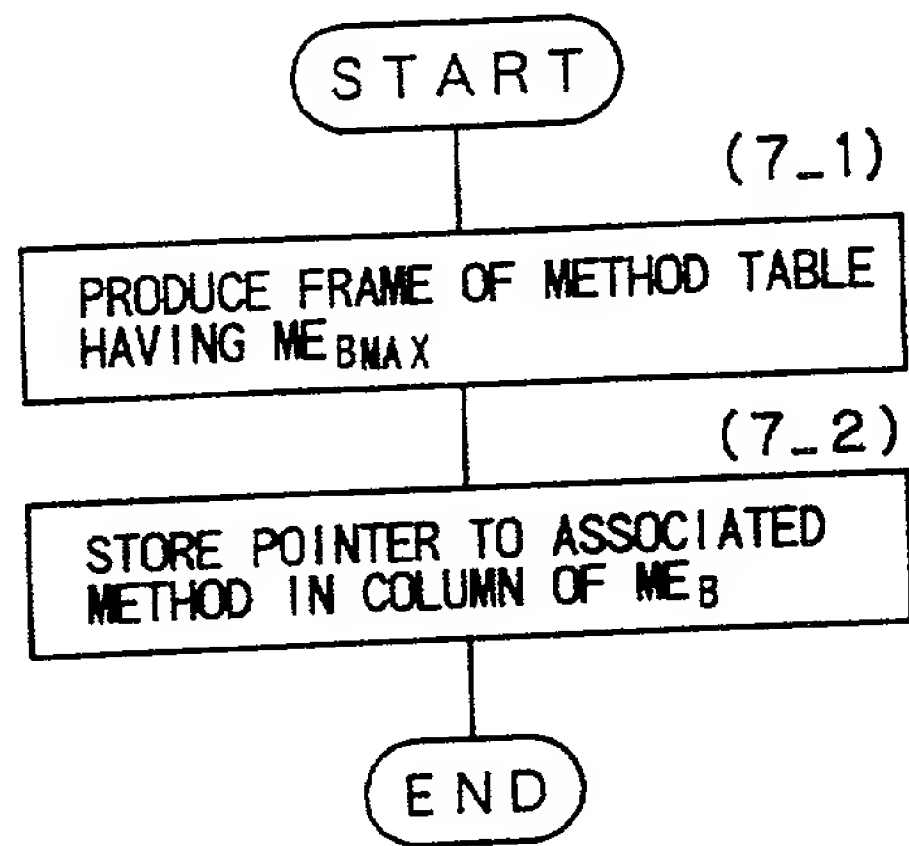


Fig. 8

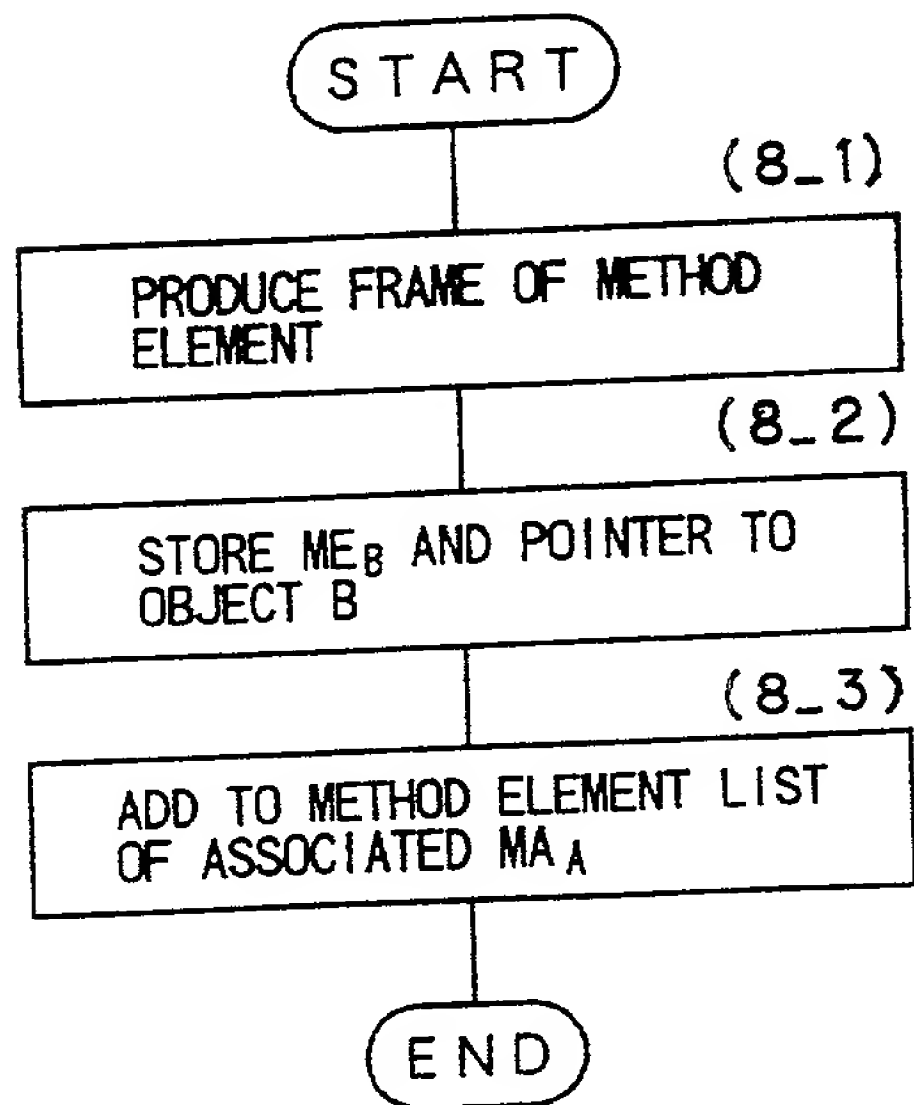


Fig. 9

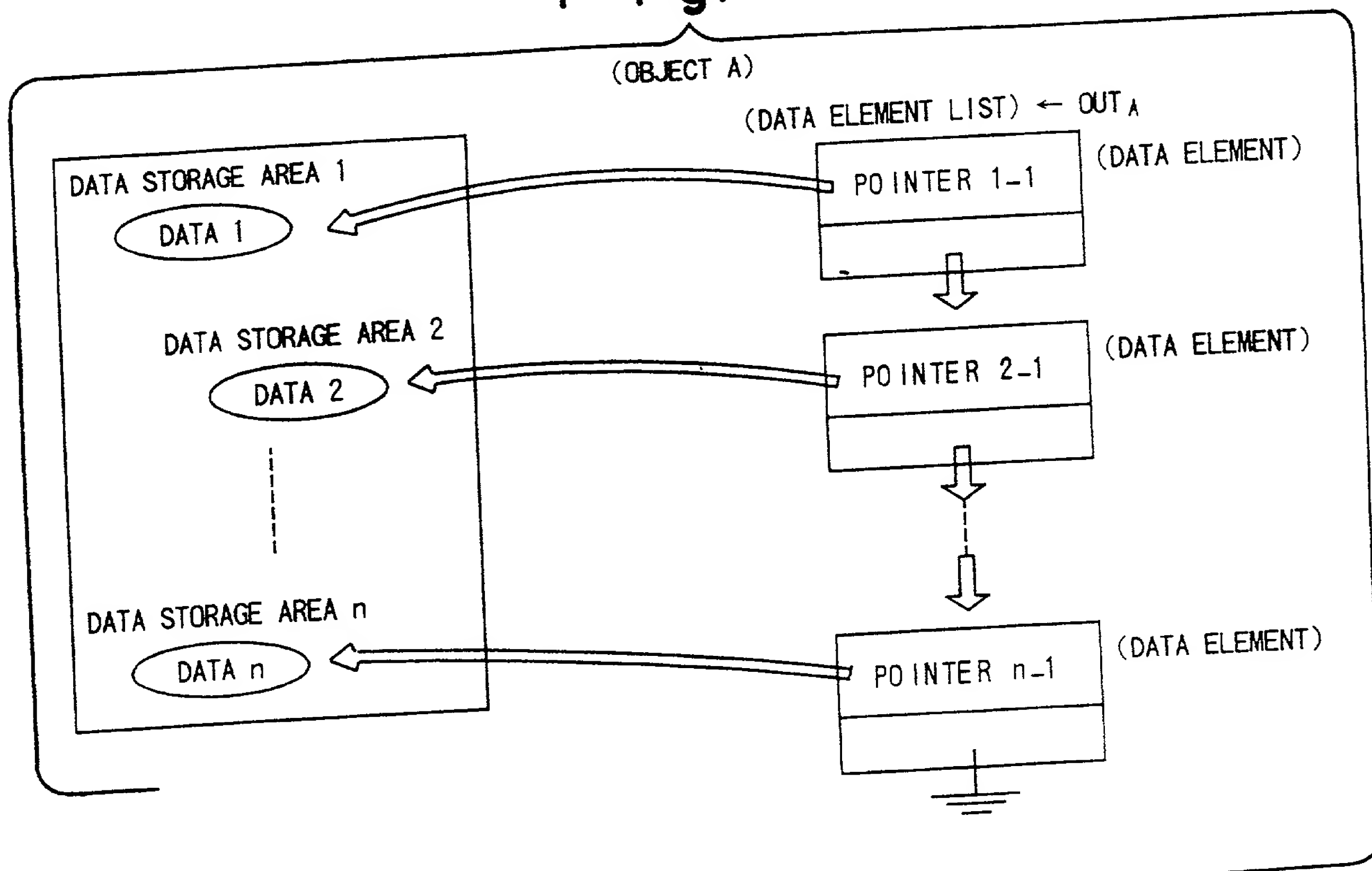


Fig. 10

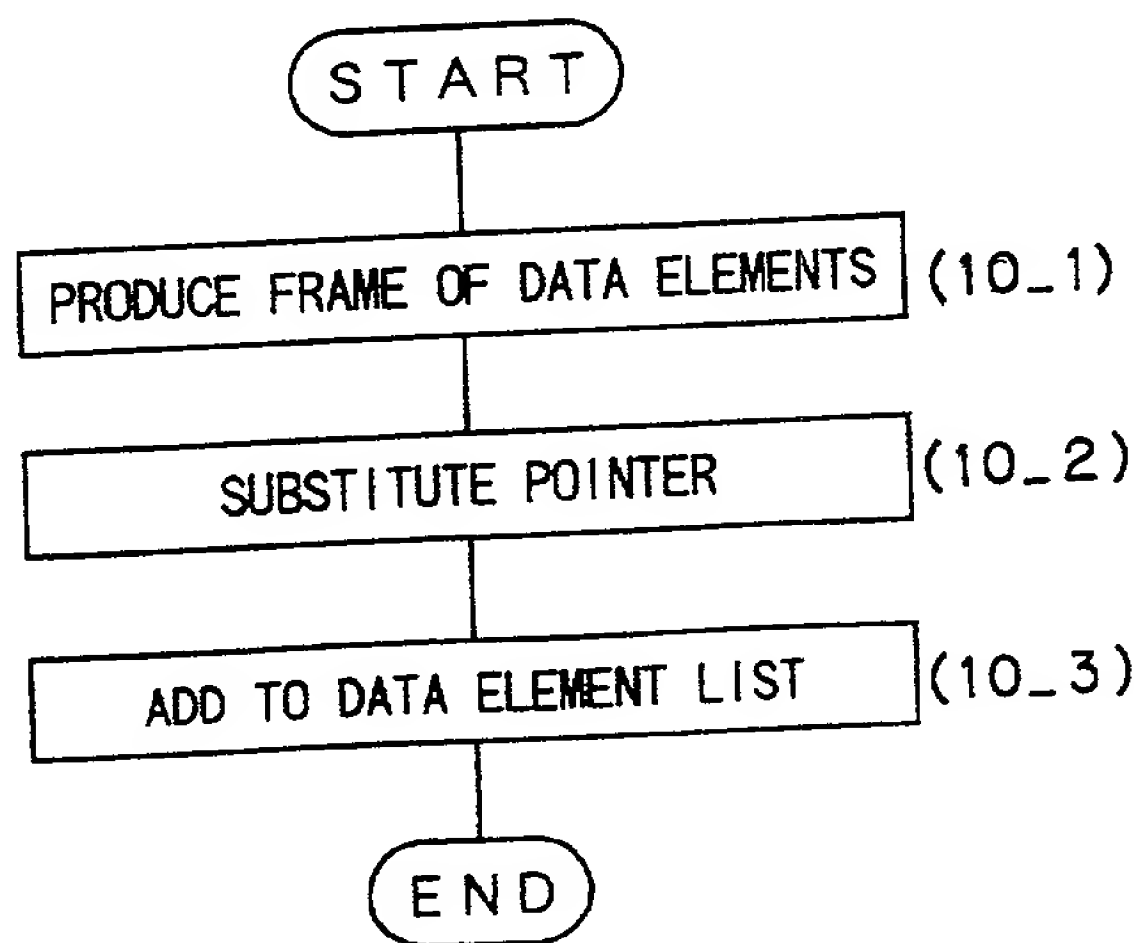


Fig. 11

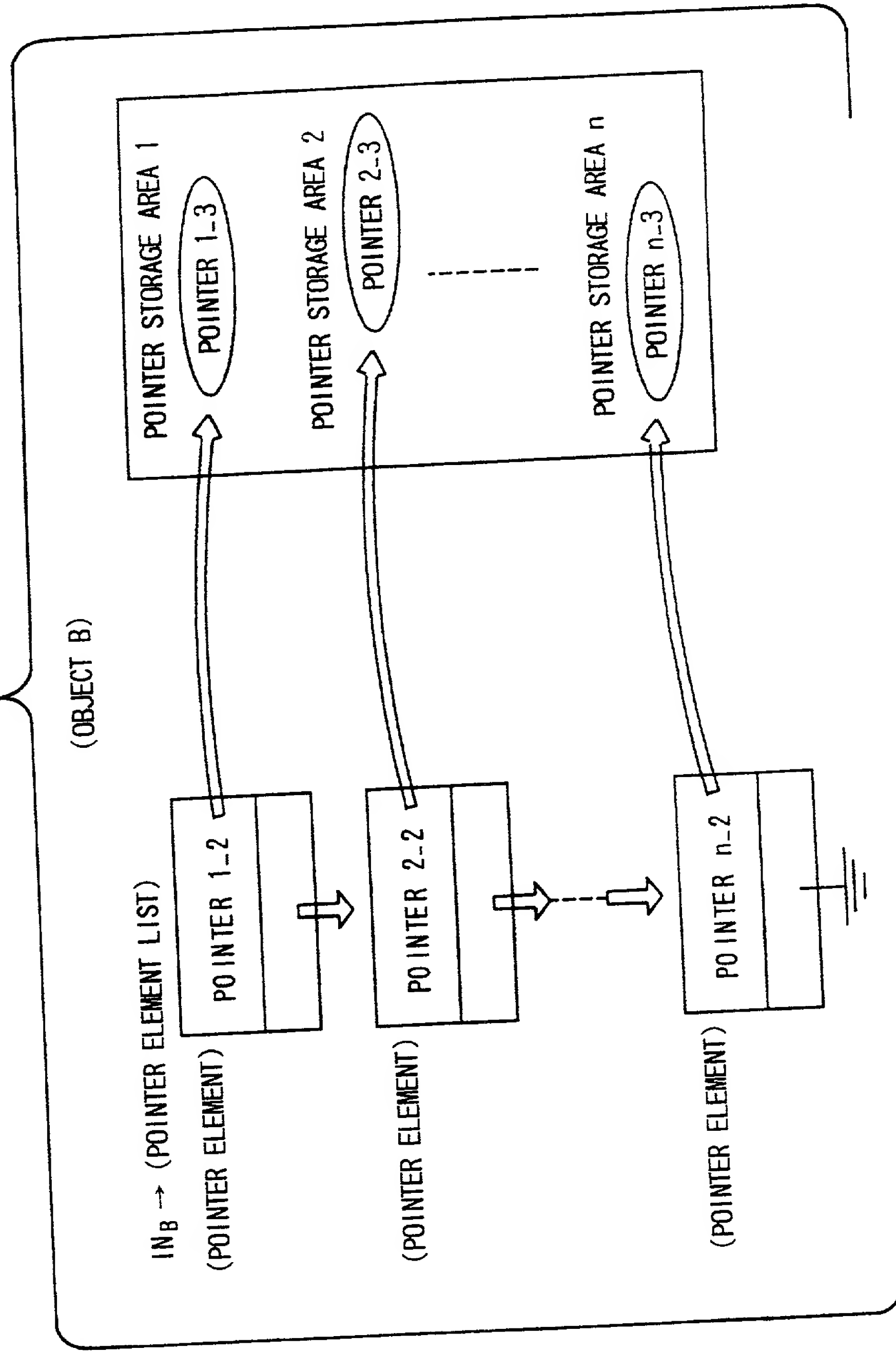


Fig. 12

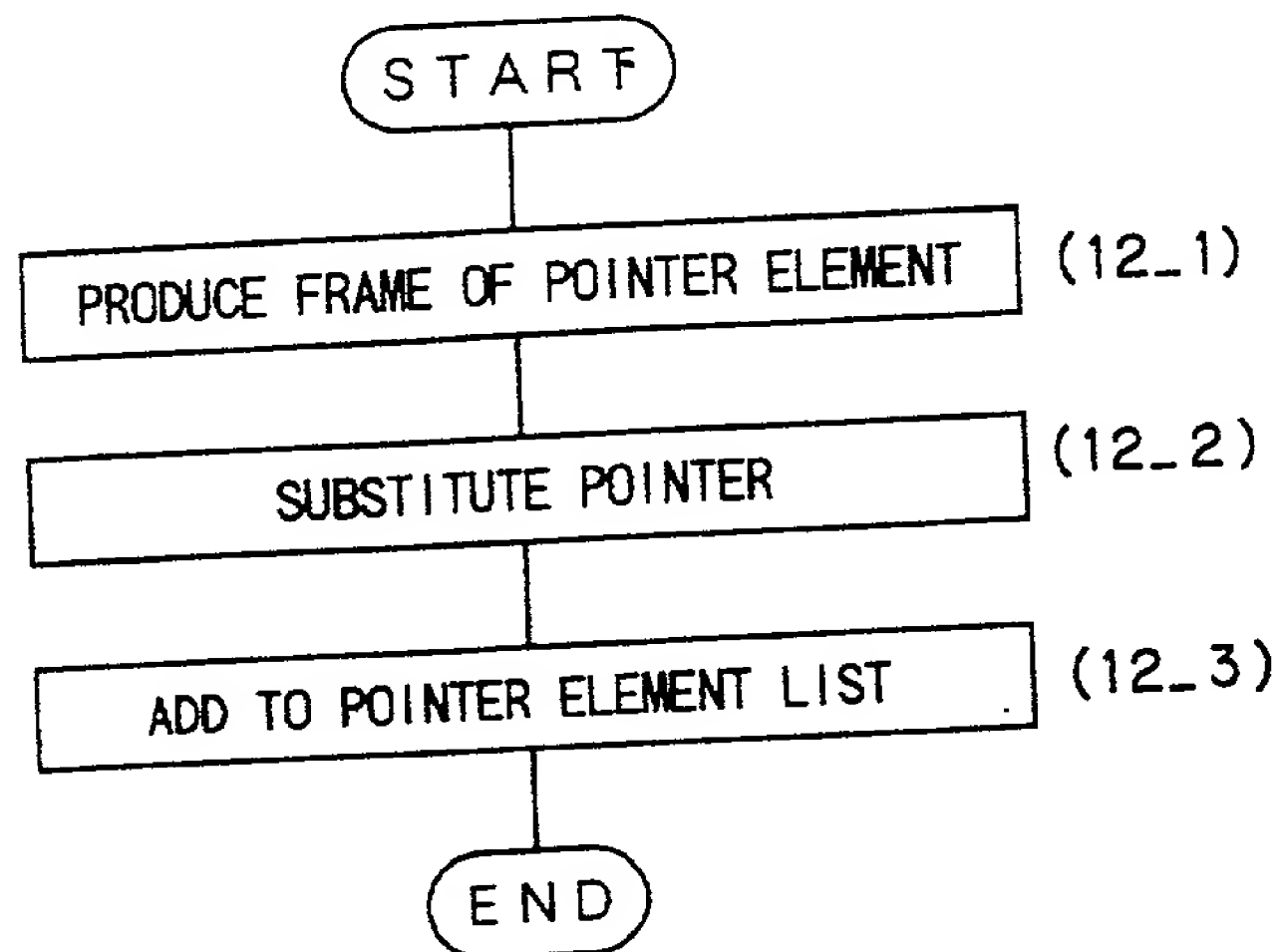


Fig. 13

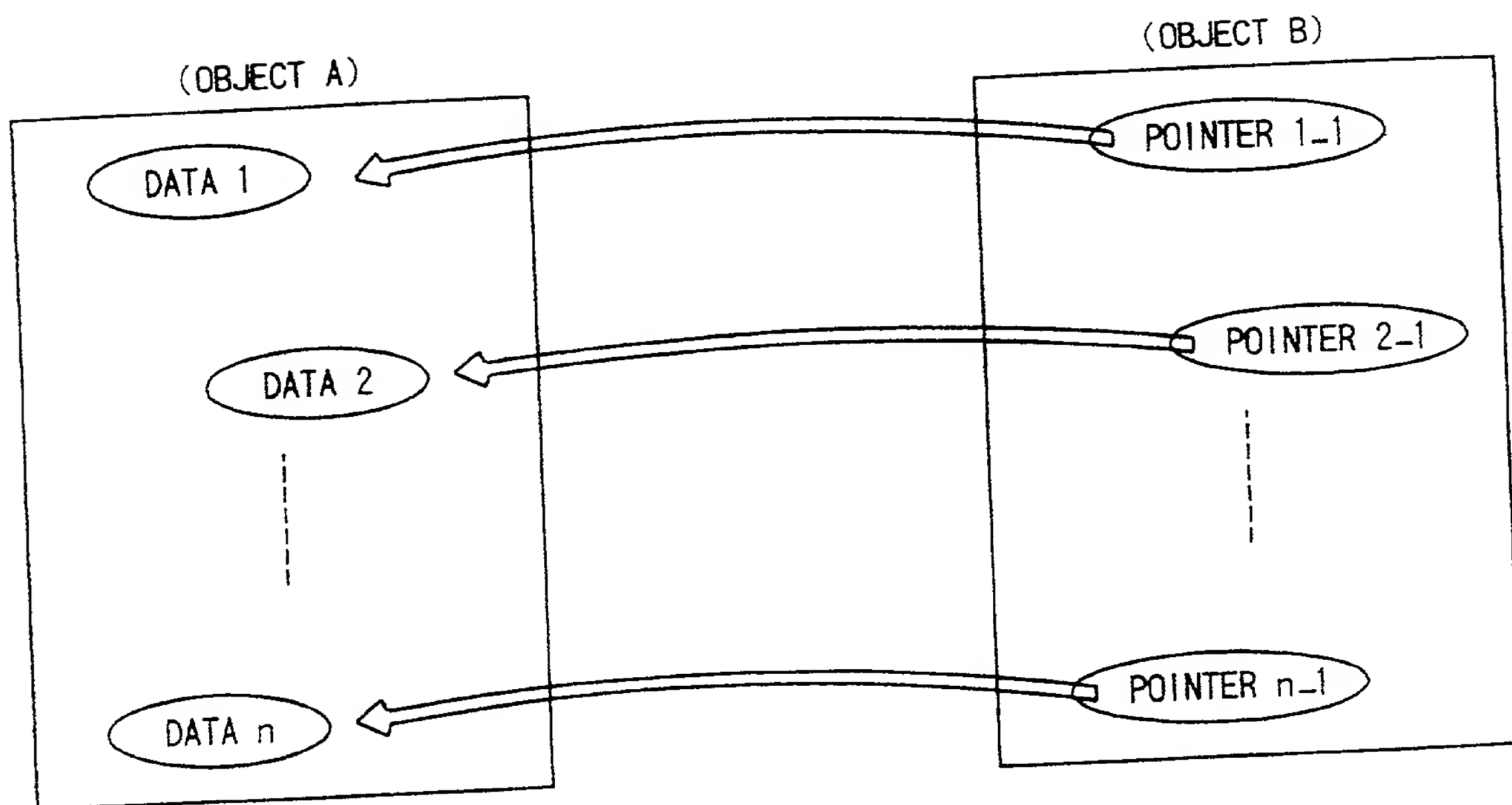


Fig. 14

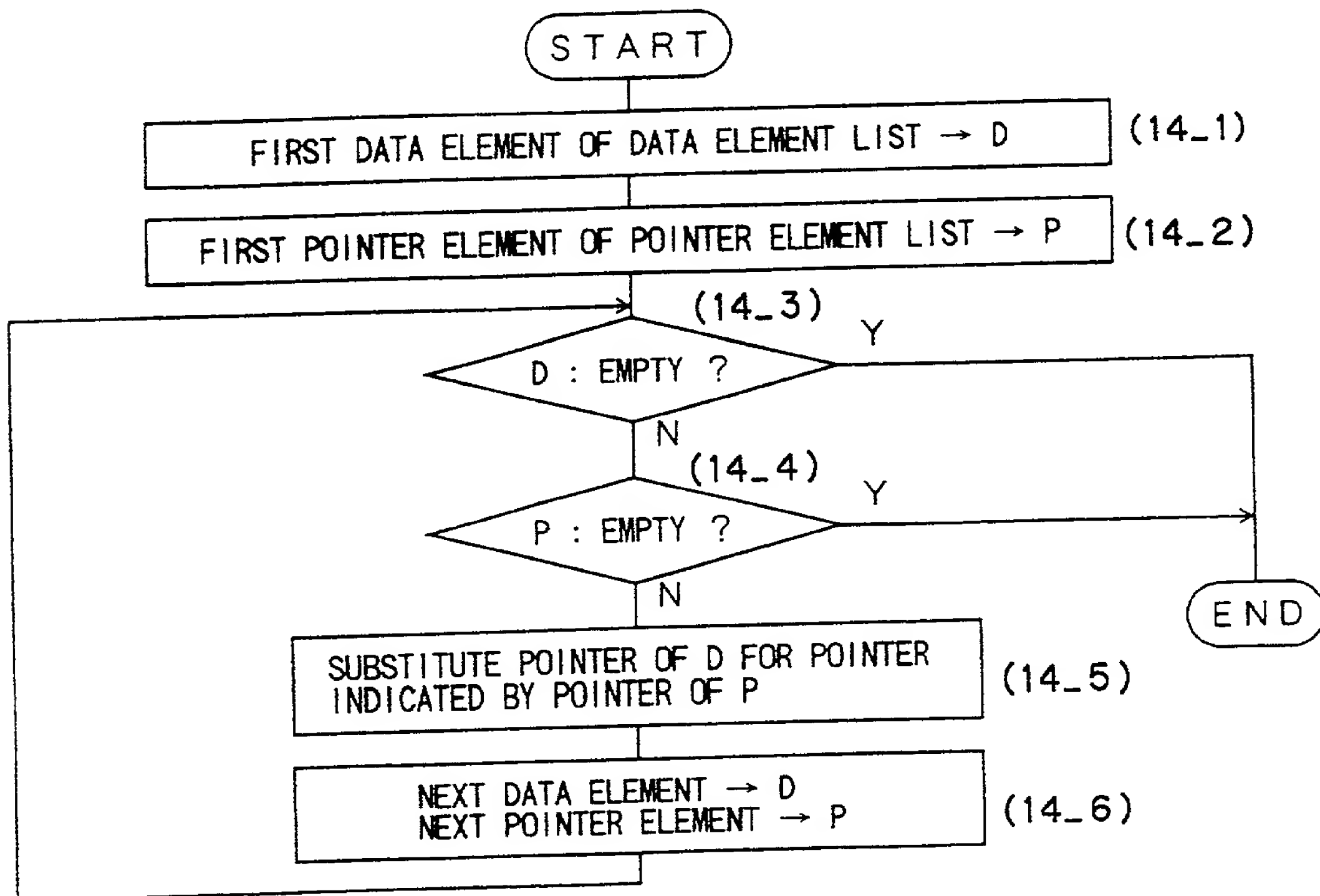


Fig. 15

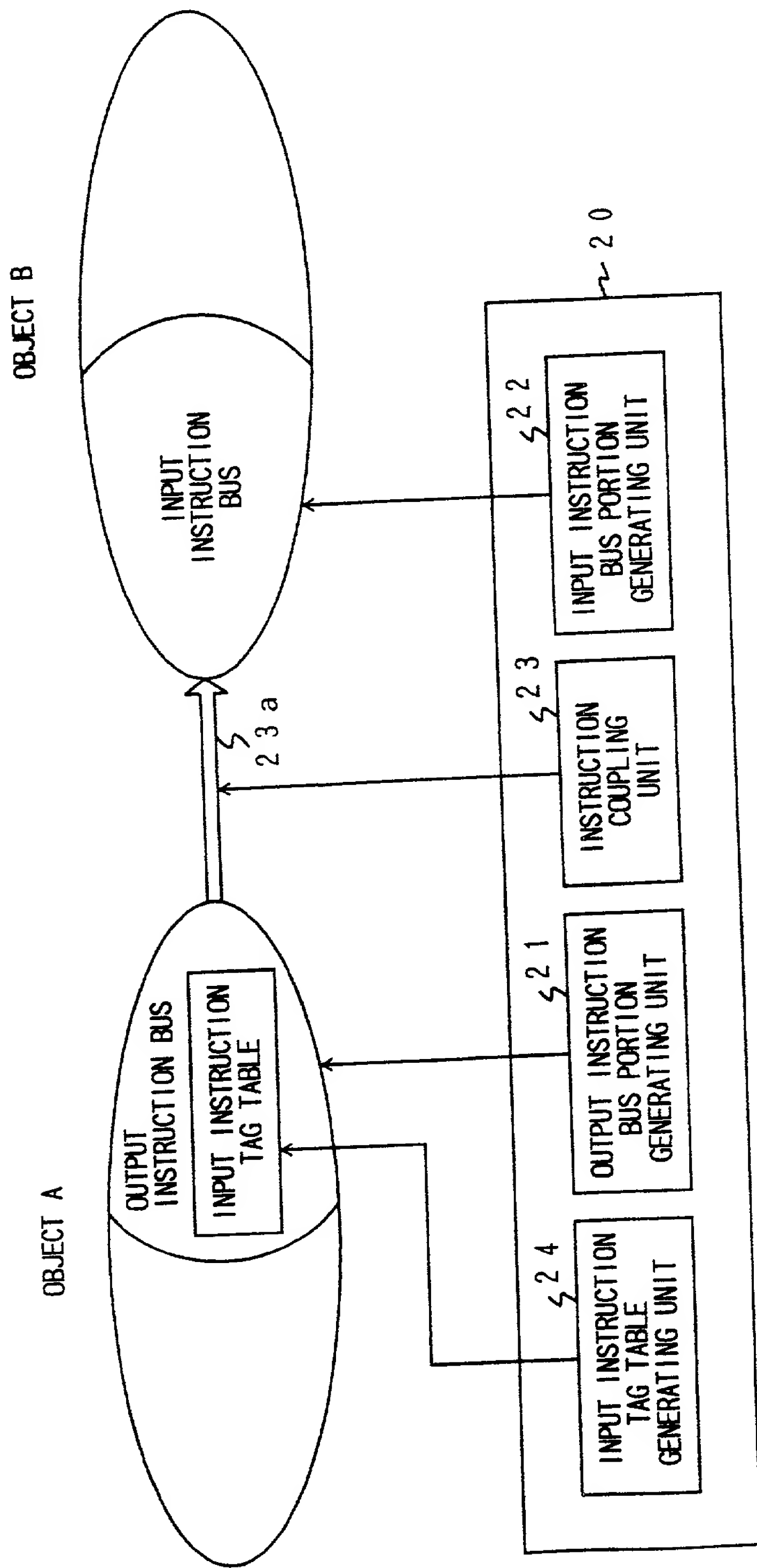


Fig. 16

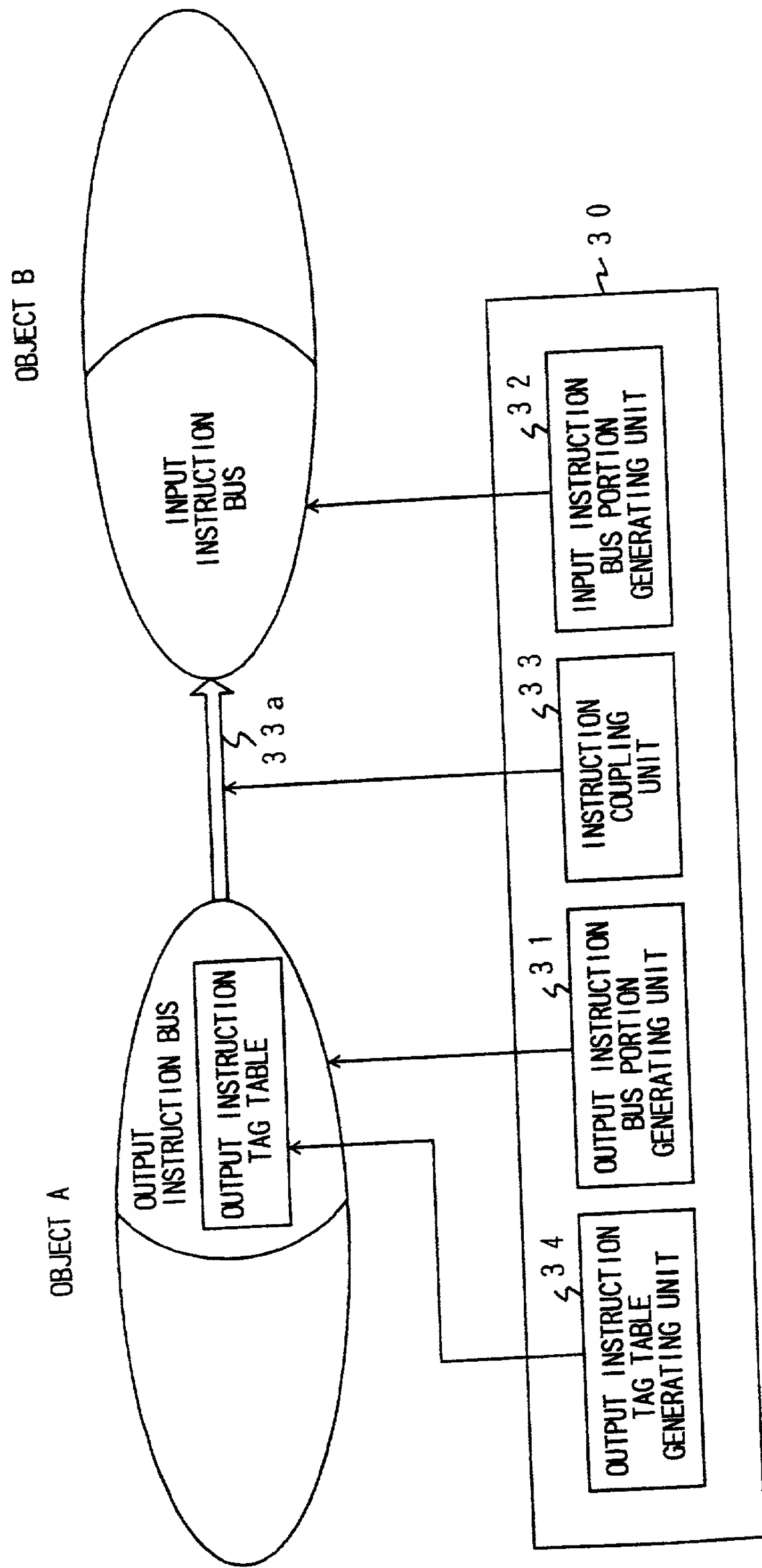


Fig. 17

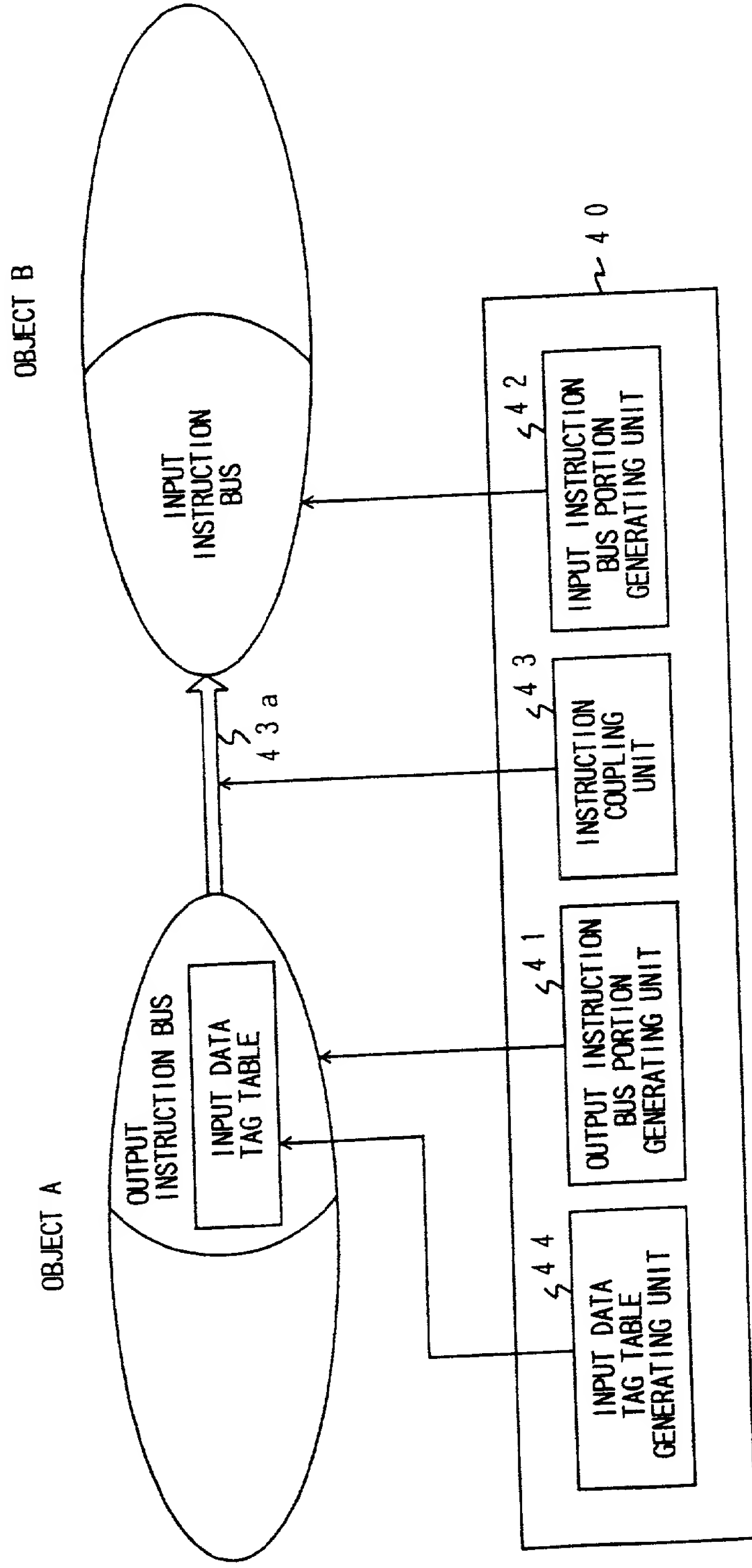


Fig. 18

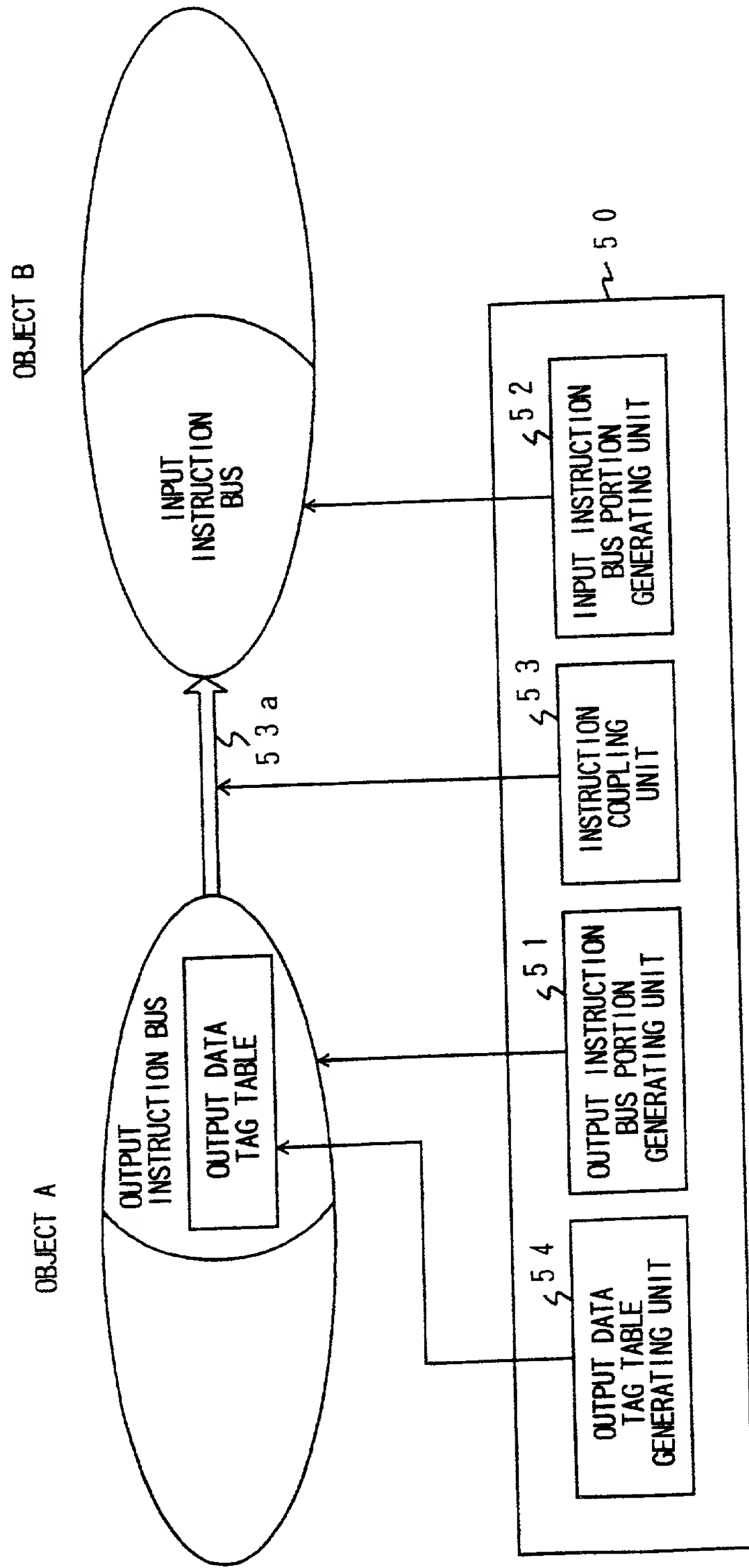


Fig. 19

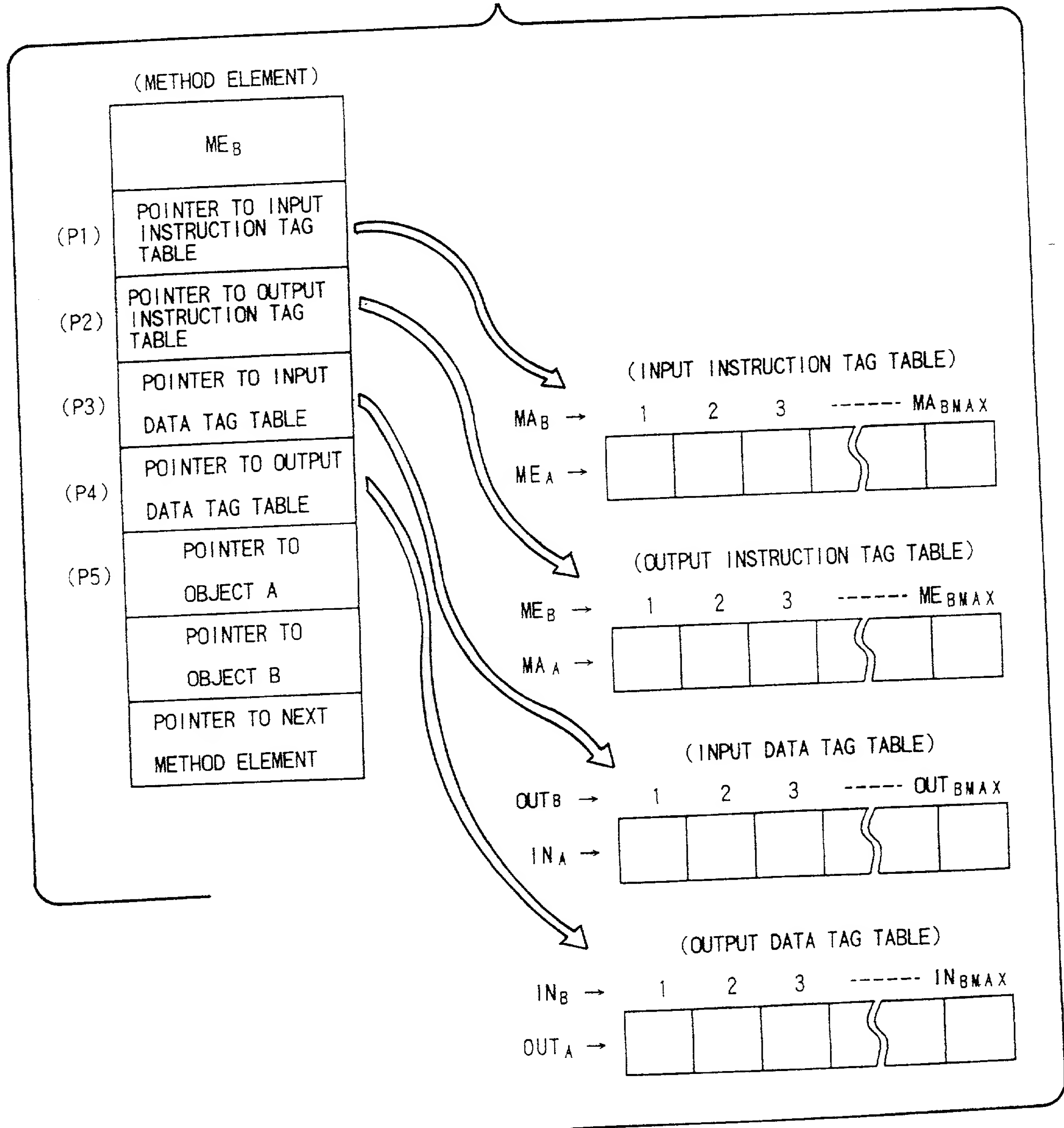


Fig. 20

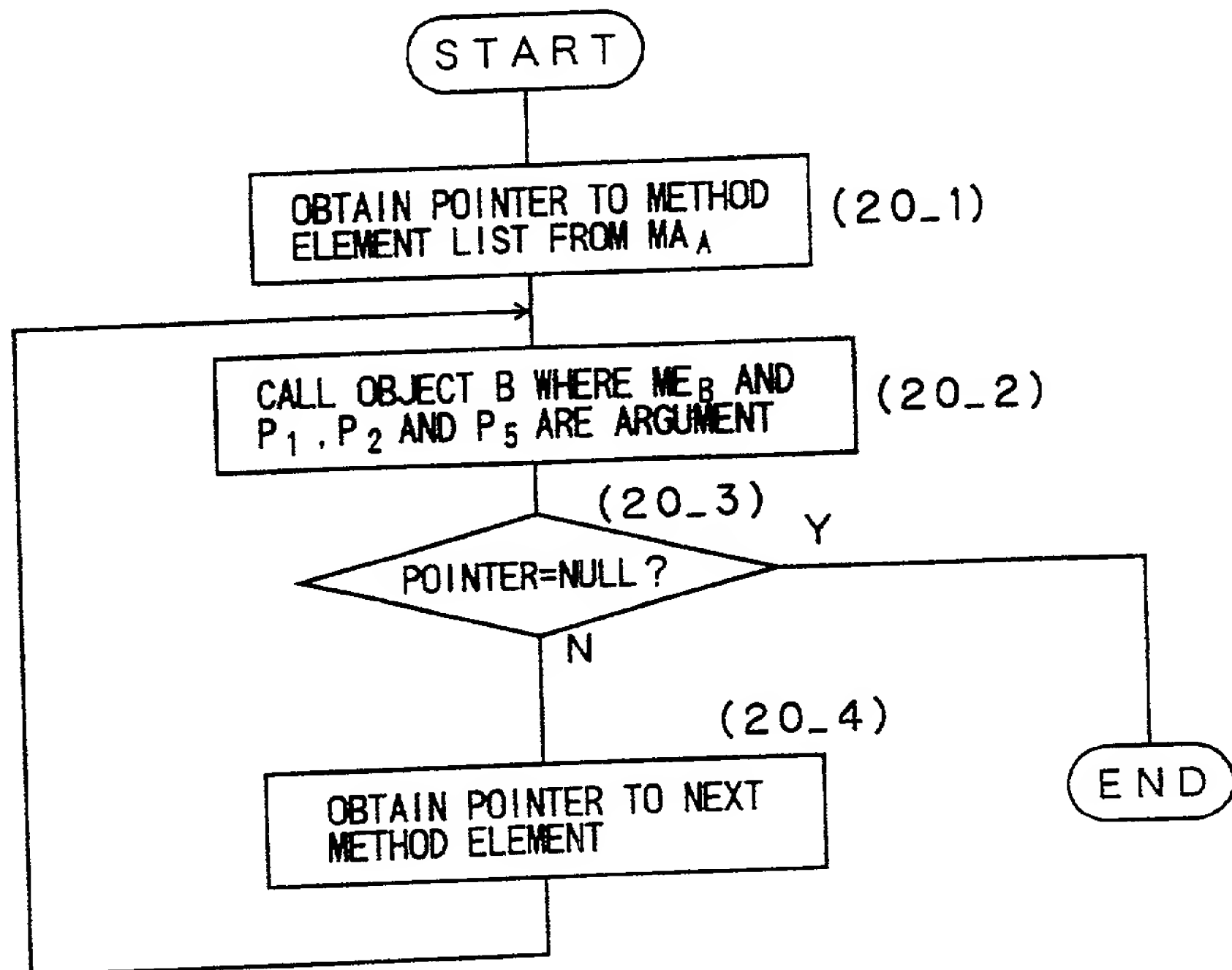


Fig. 21

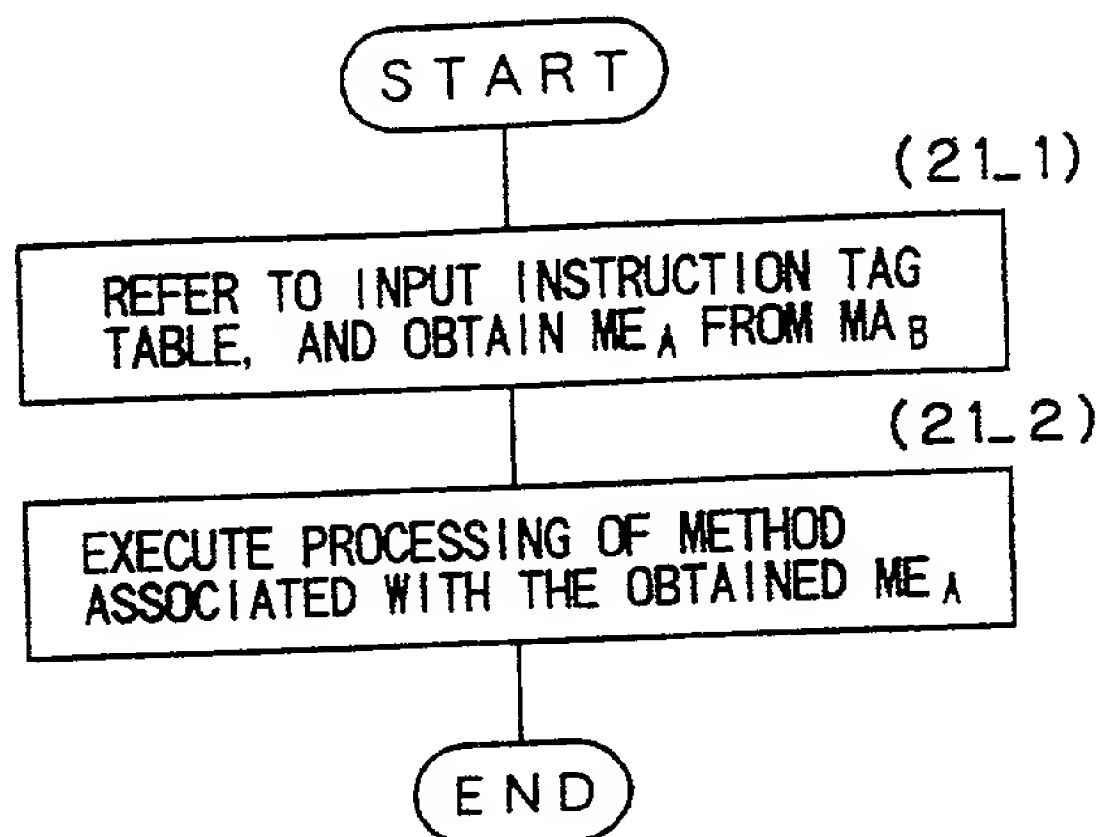


Fig.22

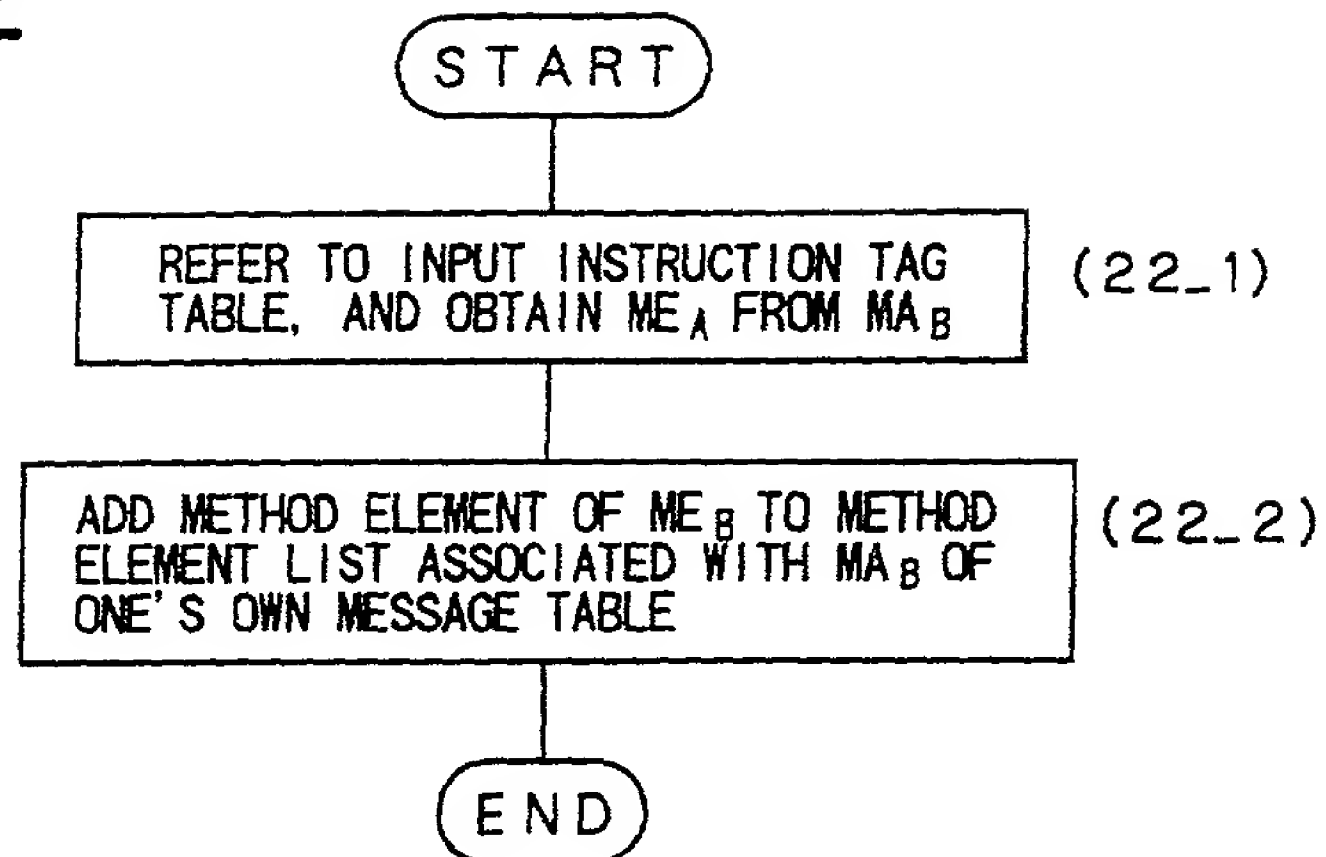


Fig.23

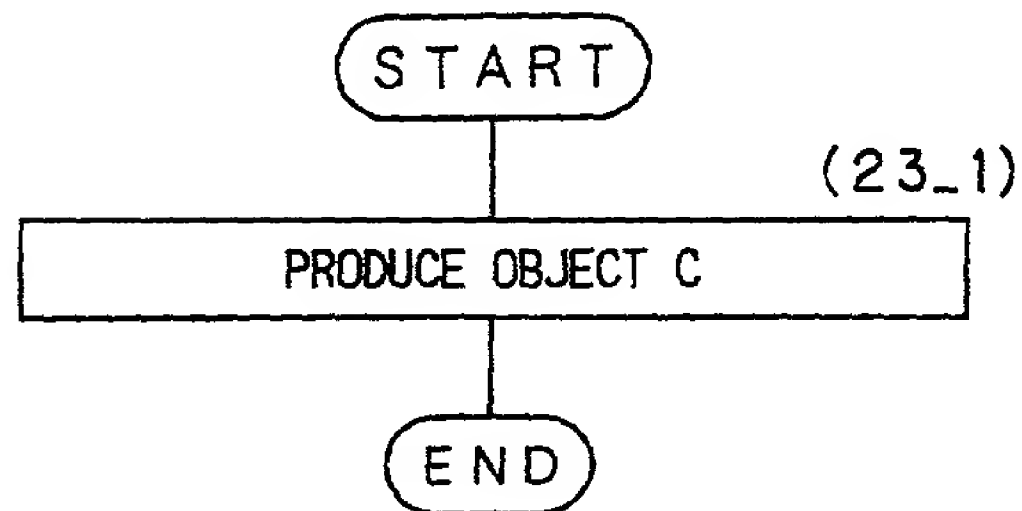


Fig. 24

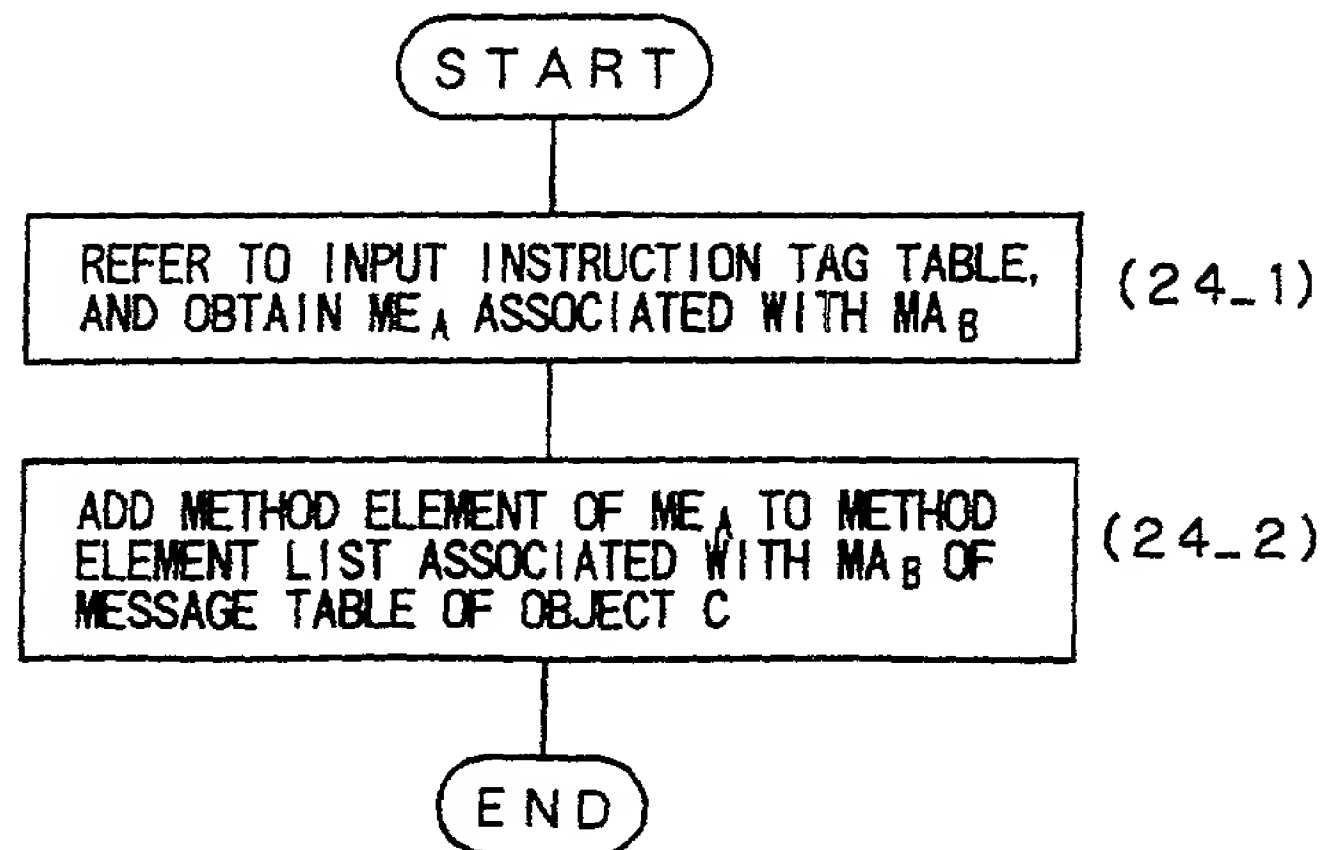


Fig. 25

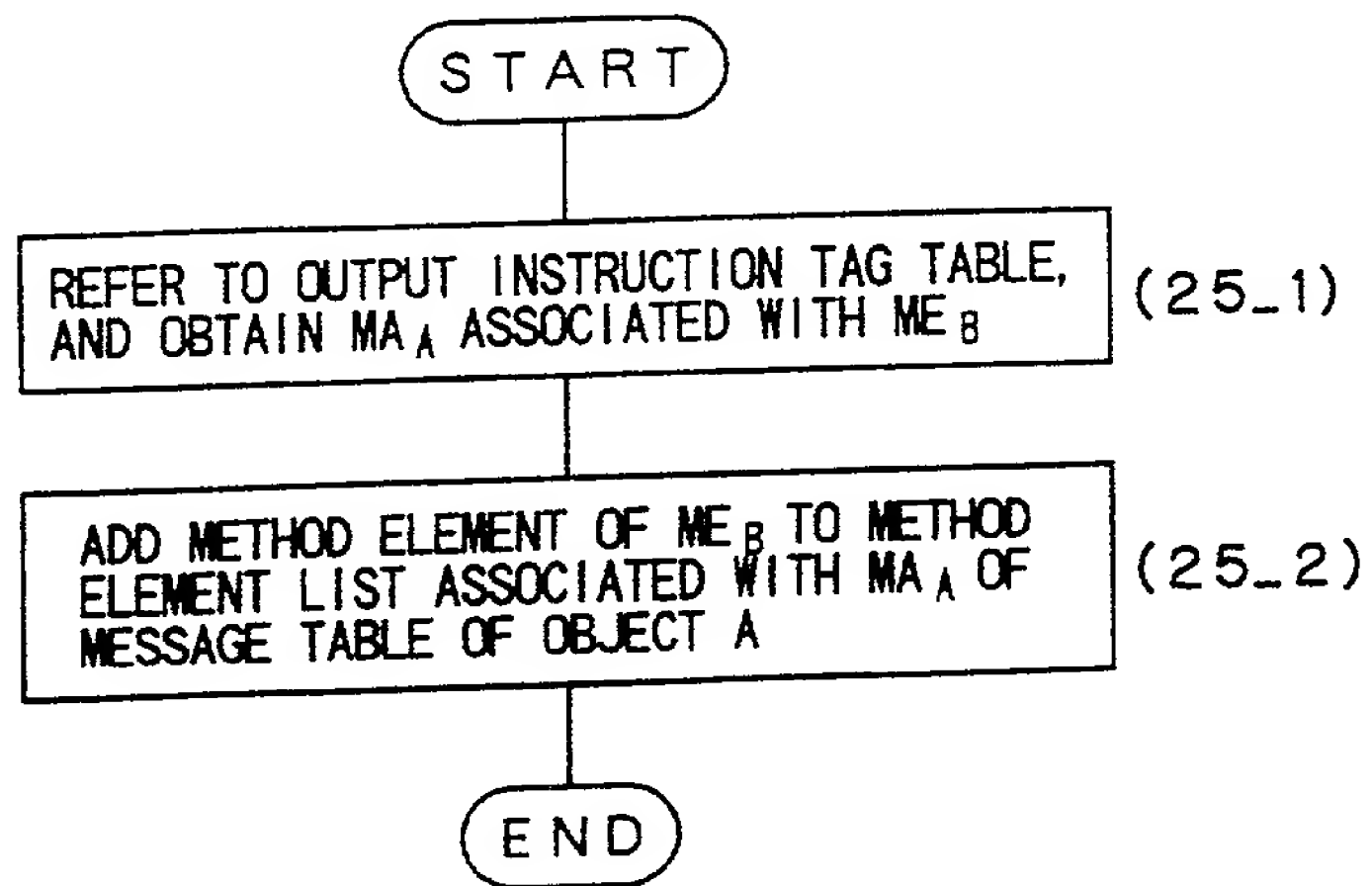


Fig. 26

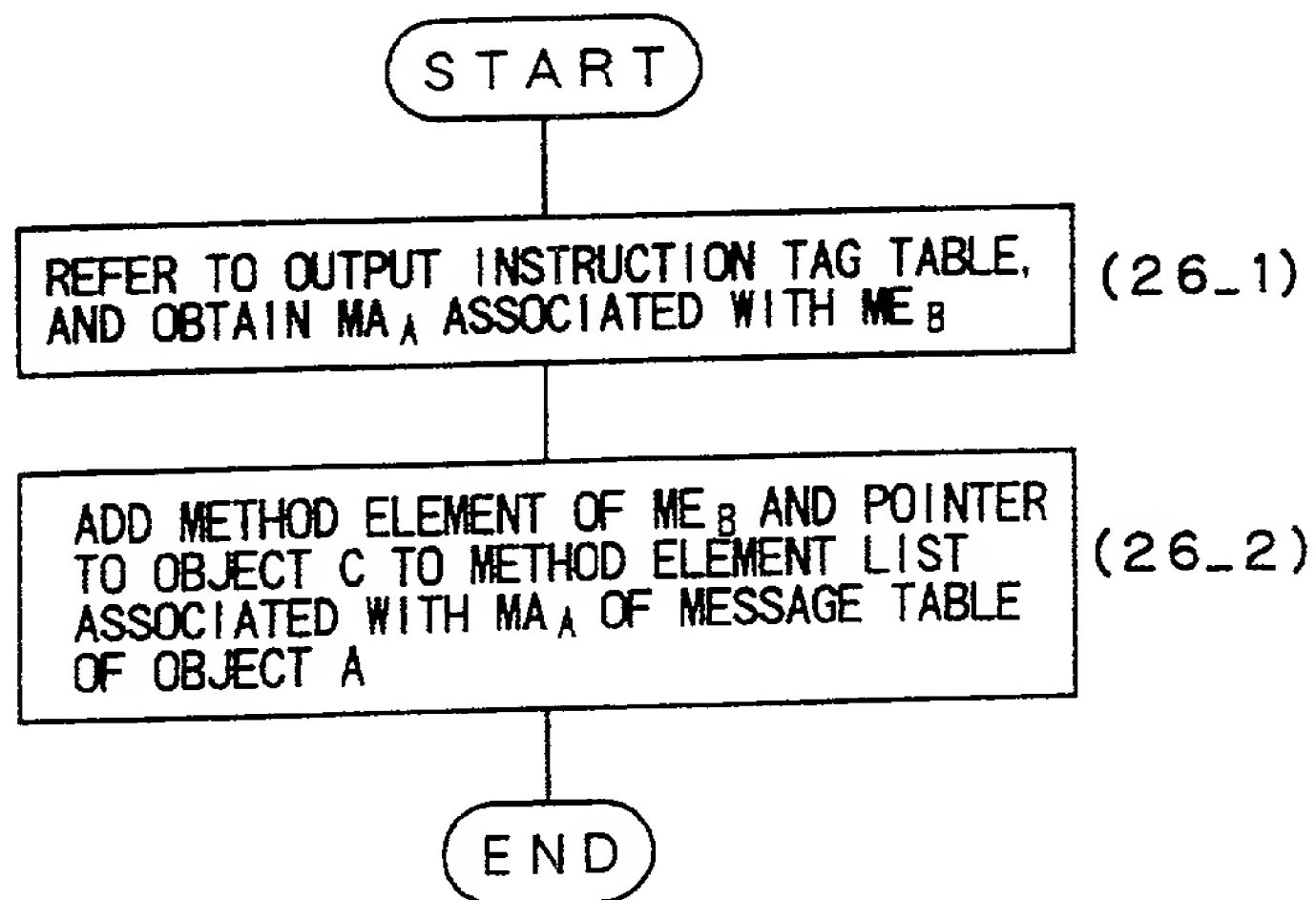


Fig. 27

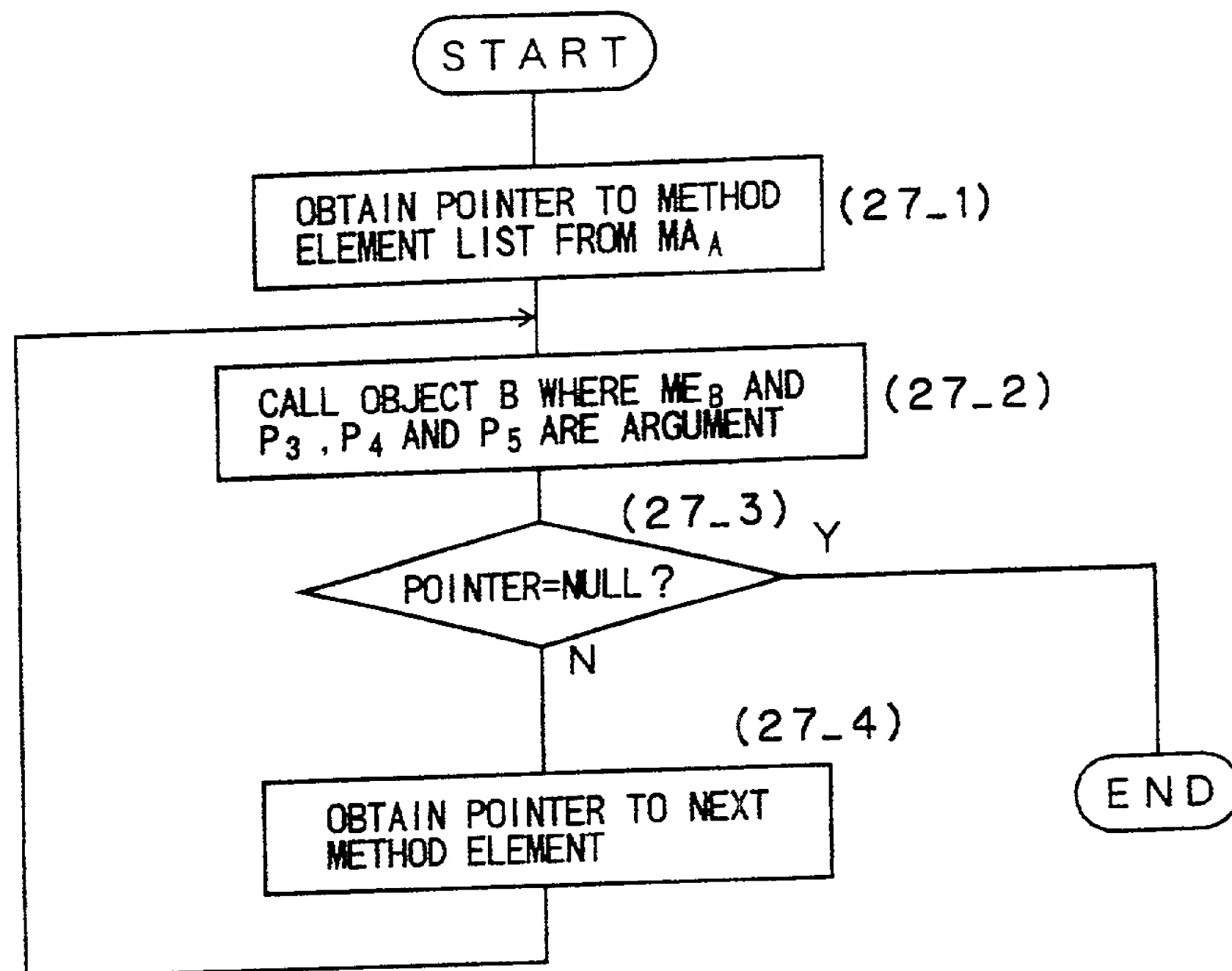


Fig. 28

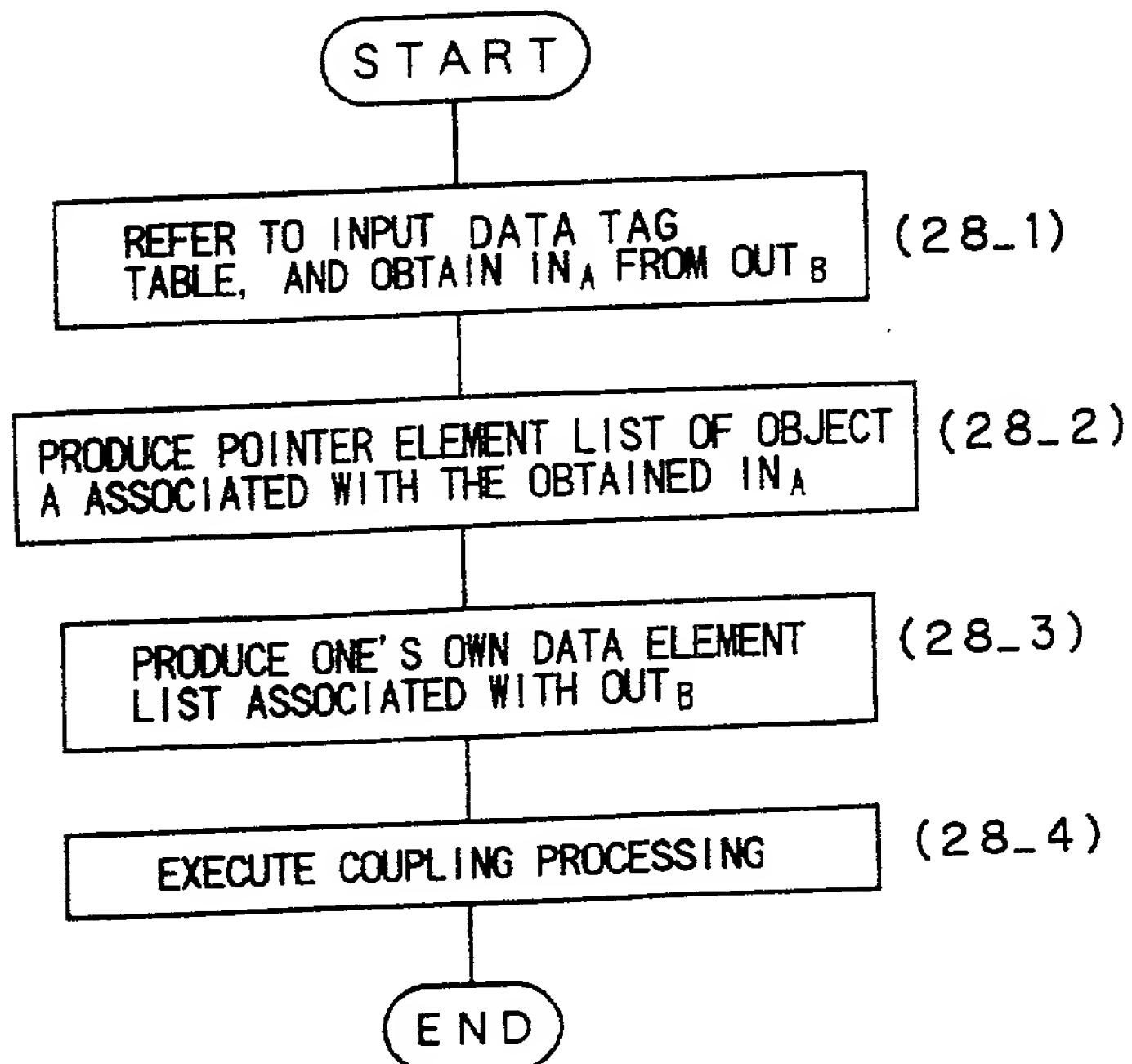


Fig. 29

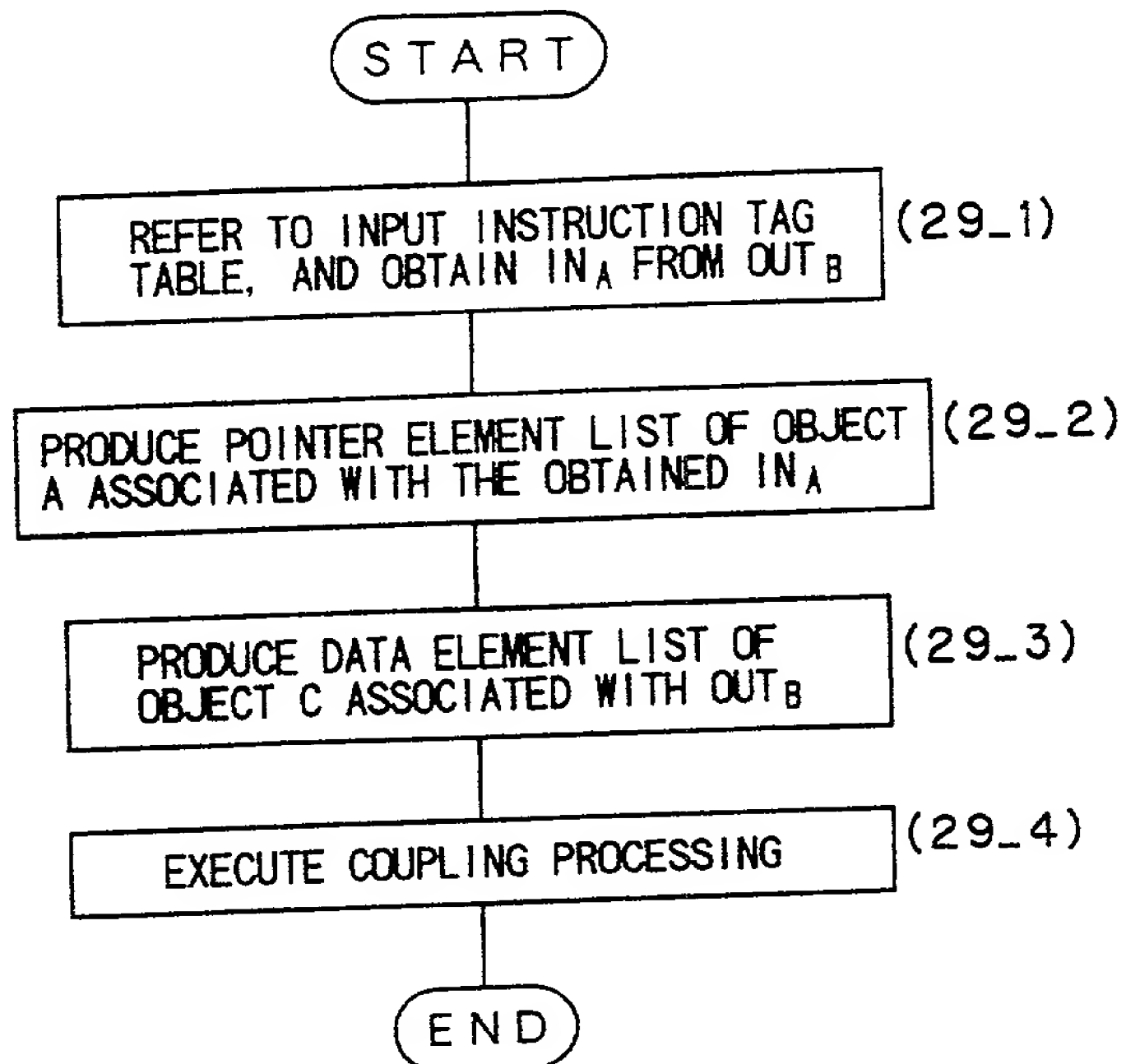


Fig. 30

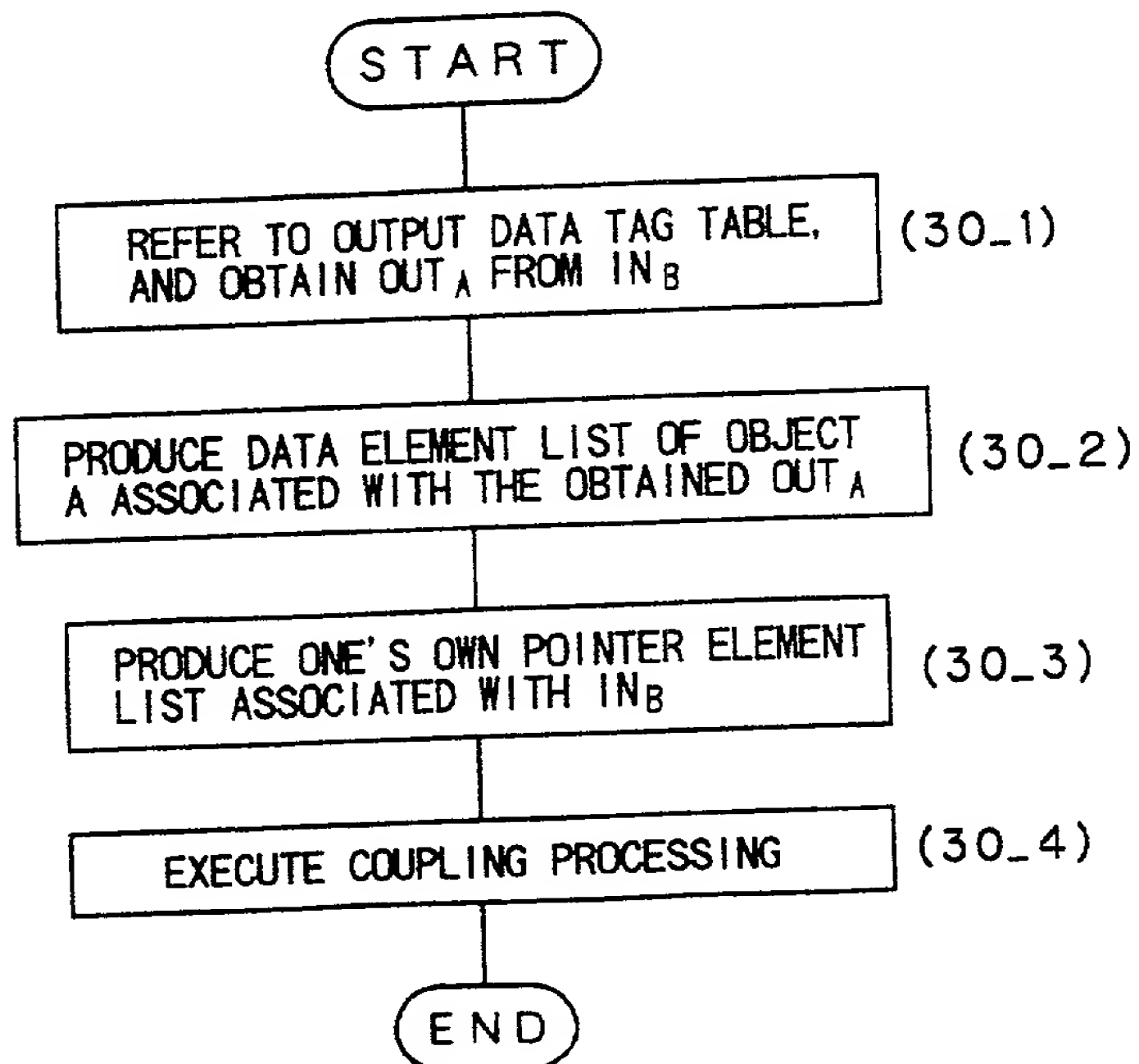


Fig. 31

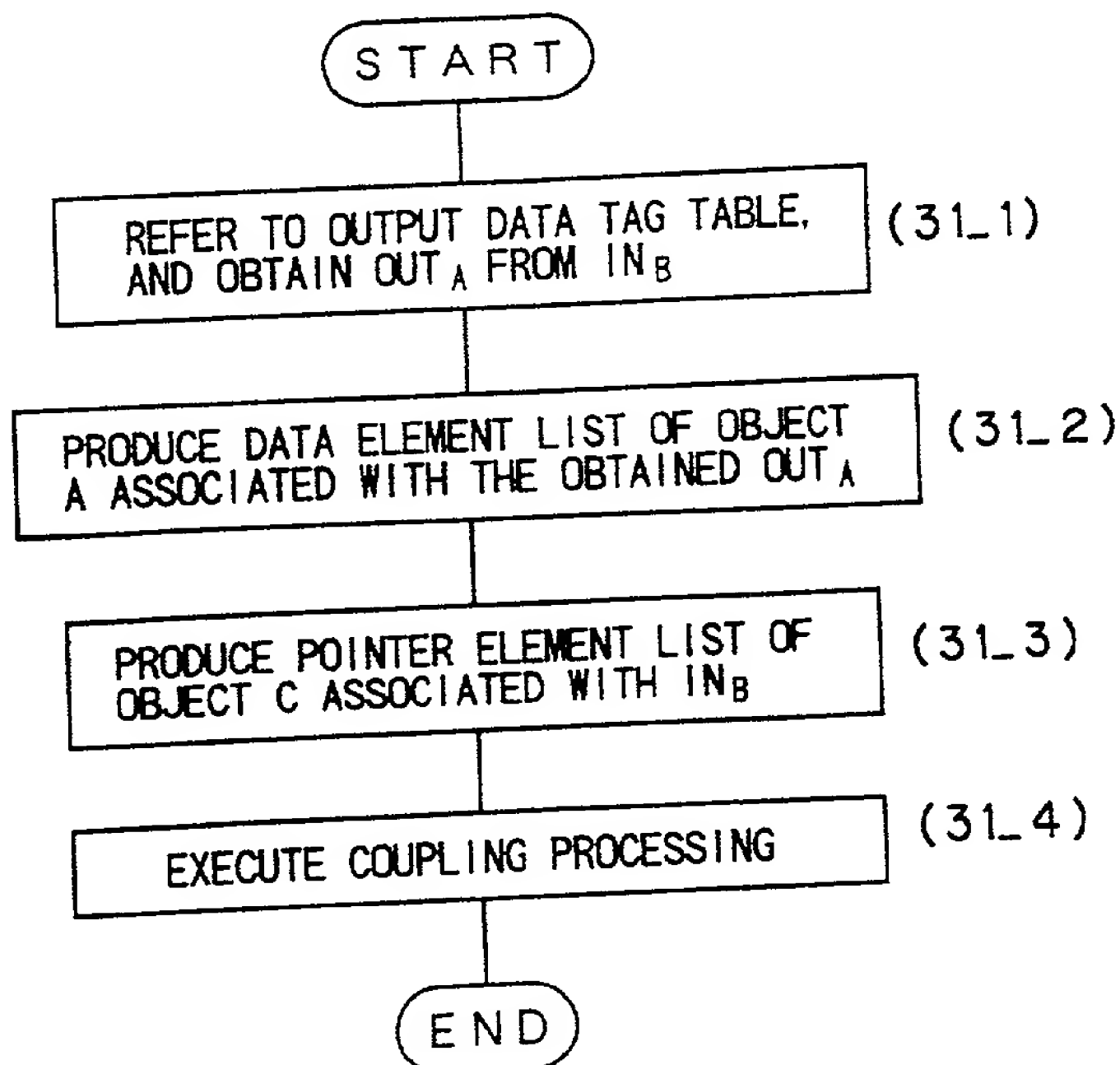


Fig. 32

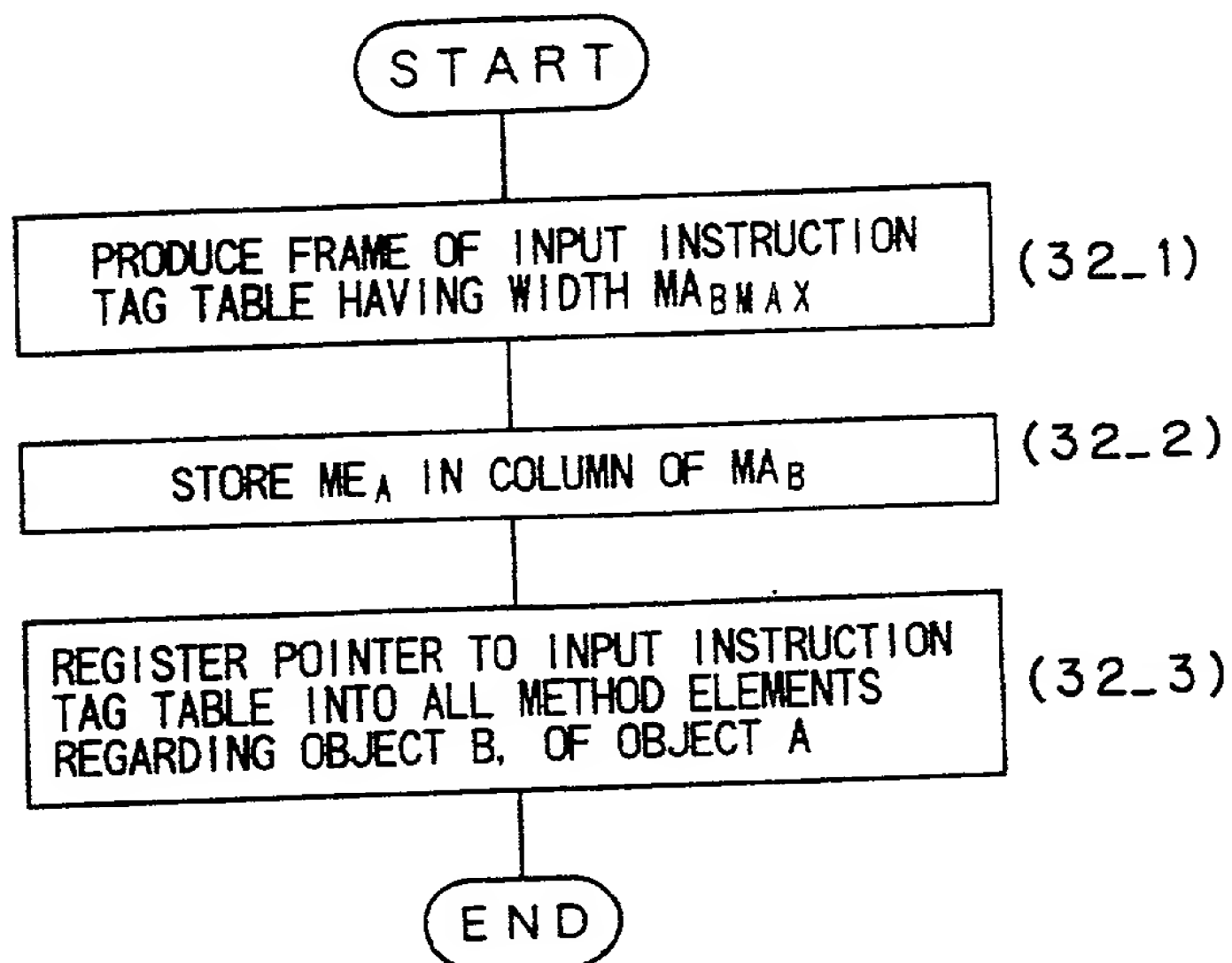


Fig. 33

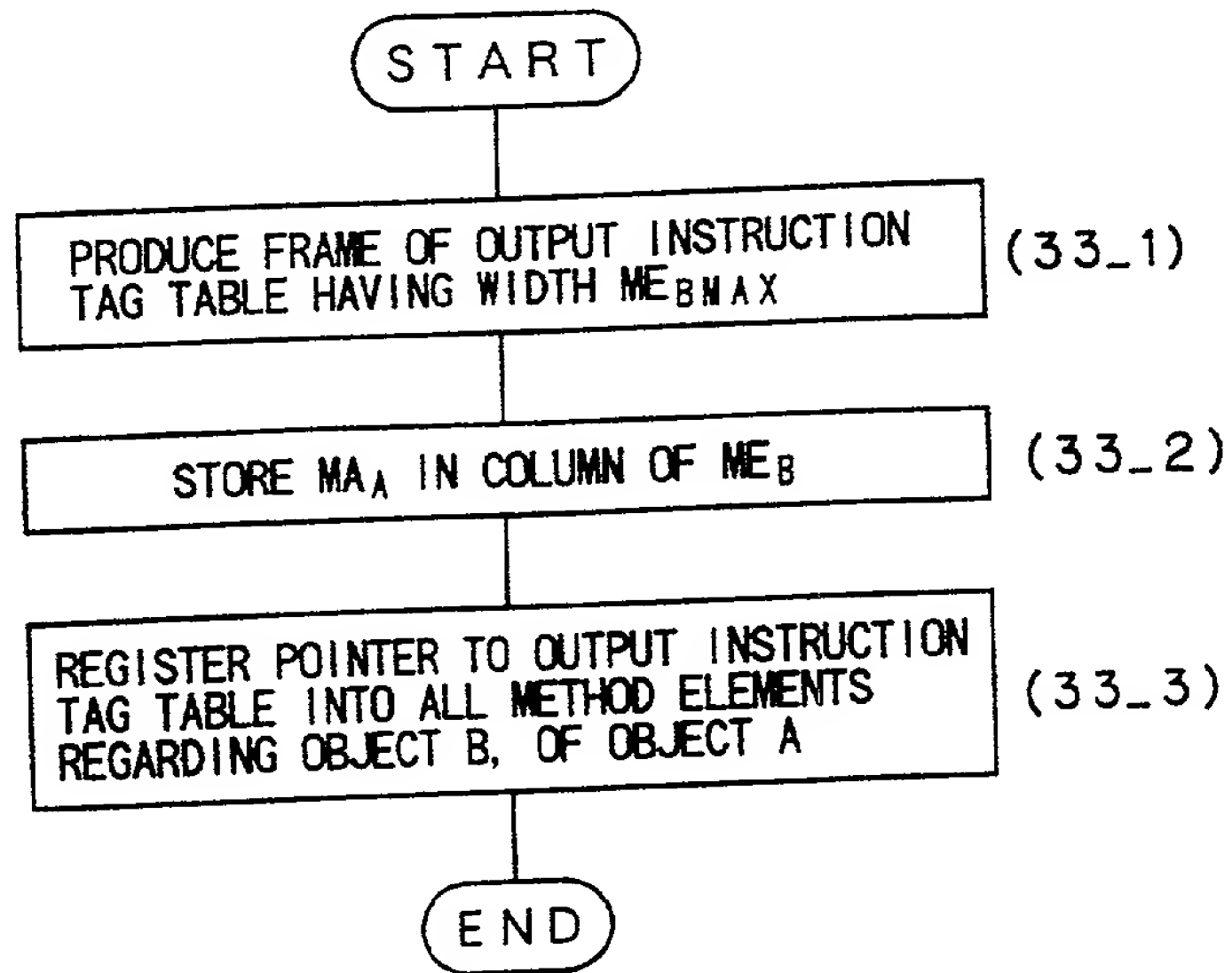


Fig. 34

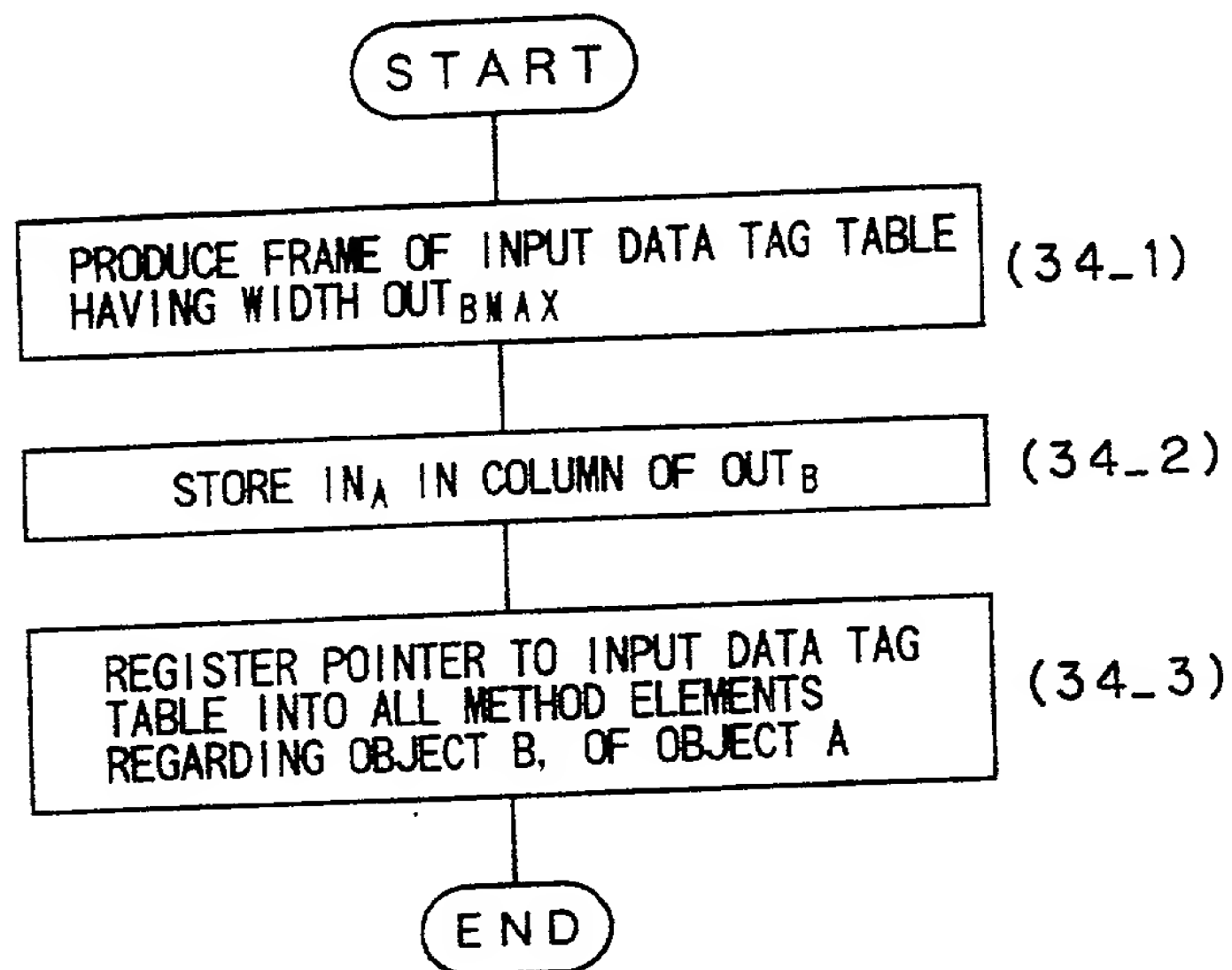


Fig. 35

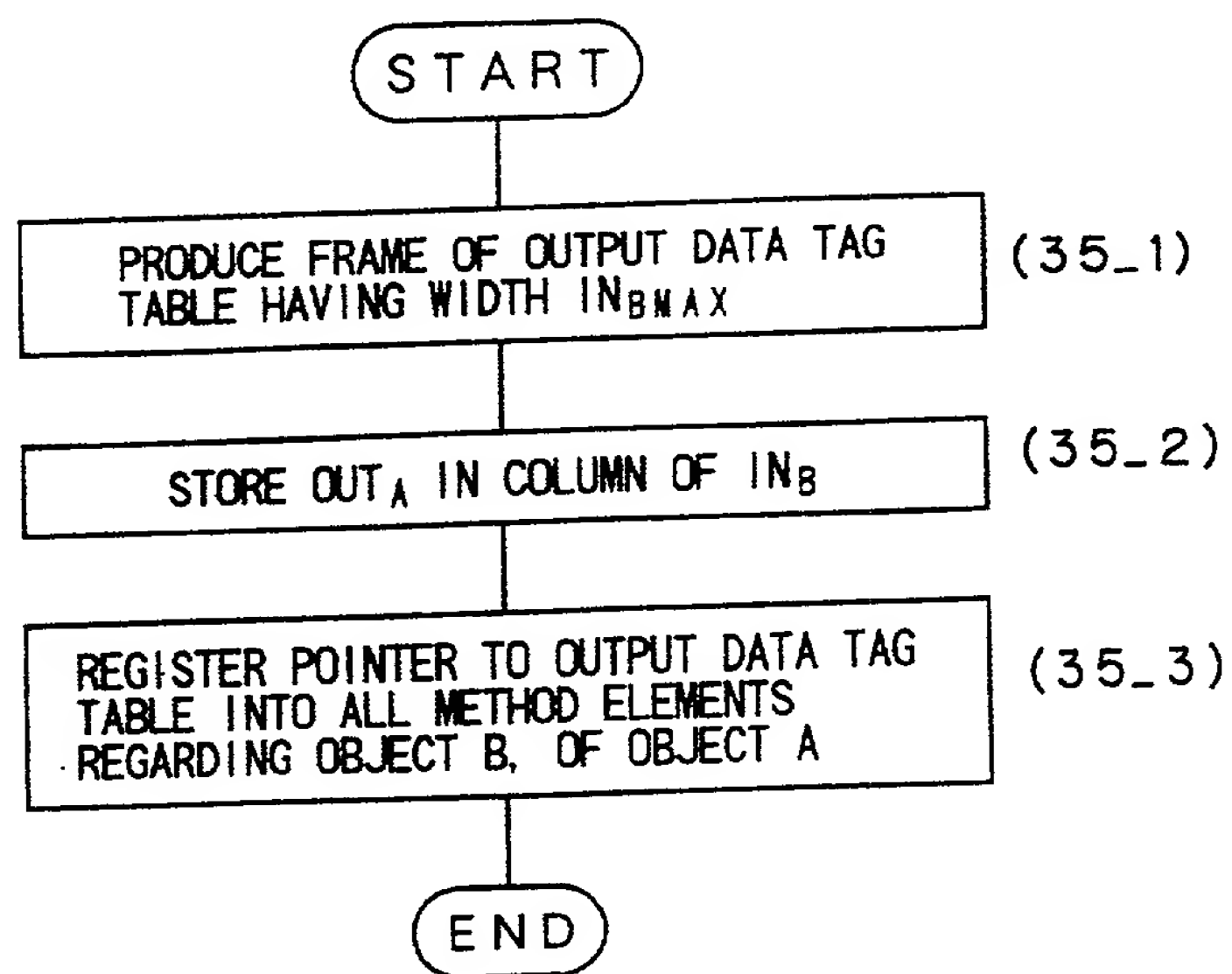


Fig. 36

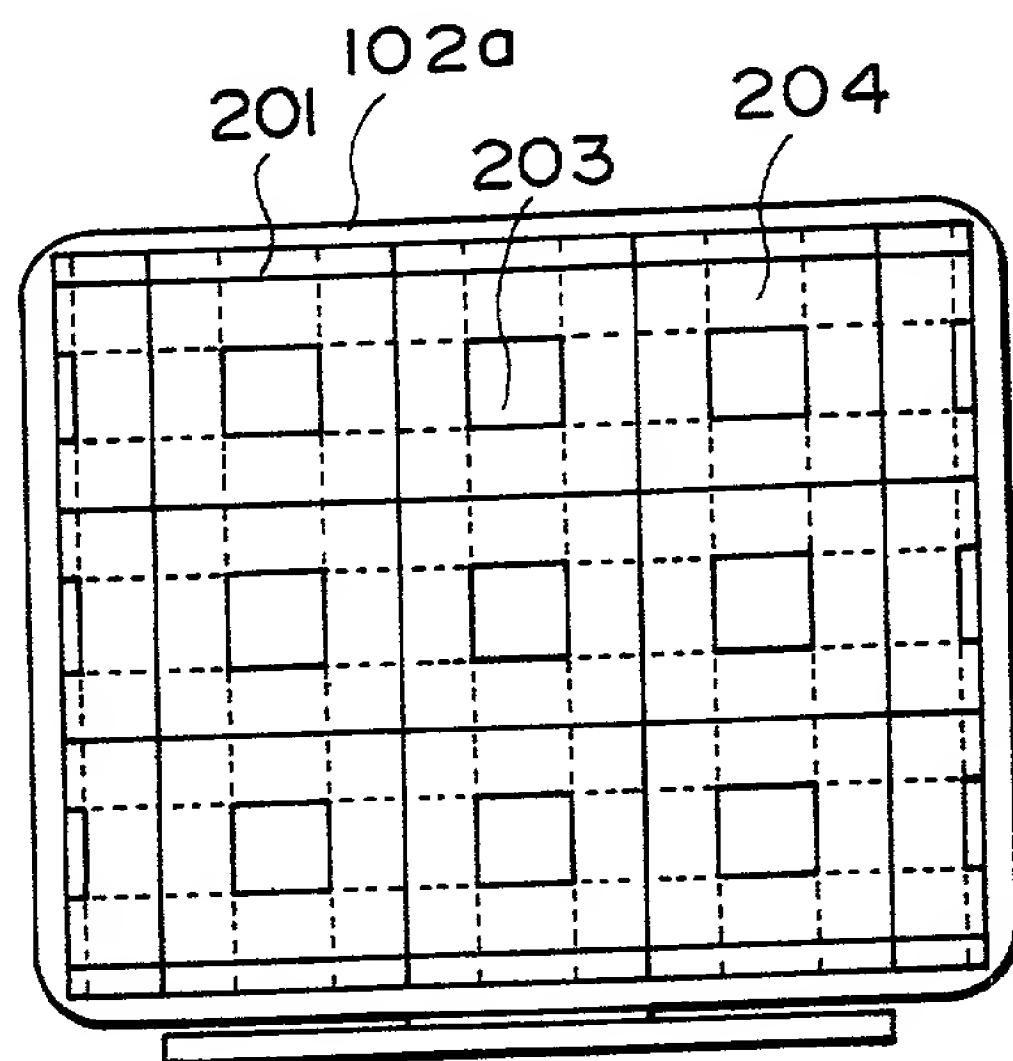


Fig. 37

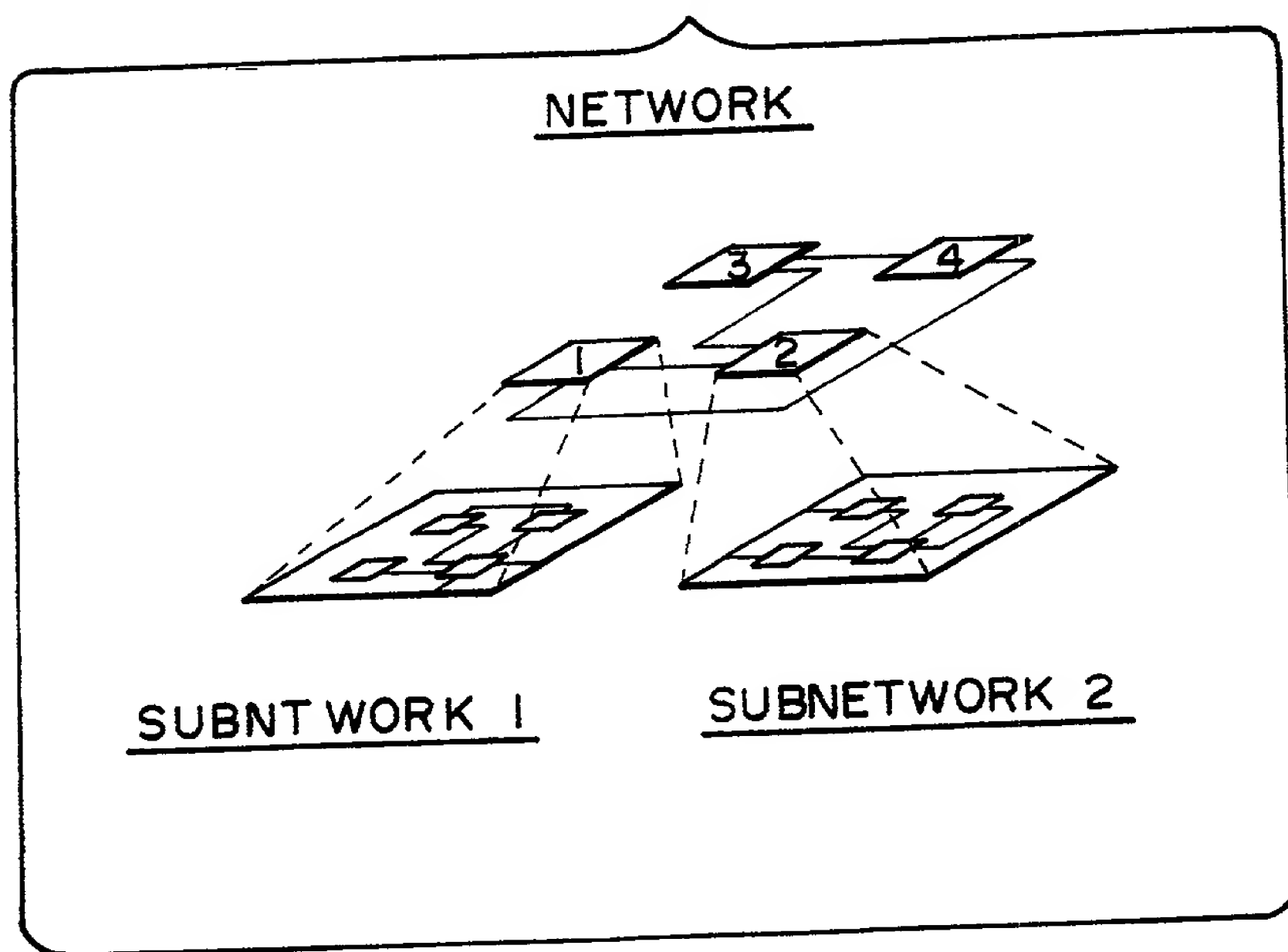


Fig. 38 (A)

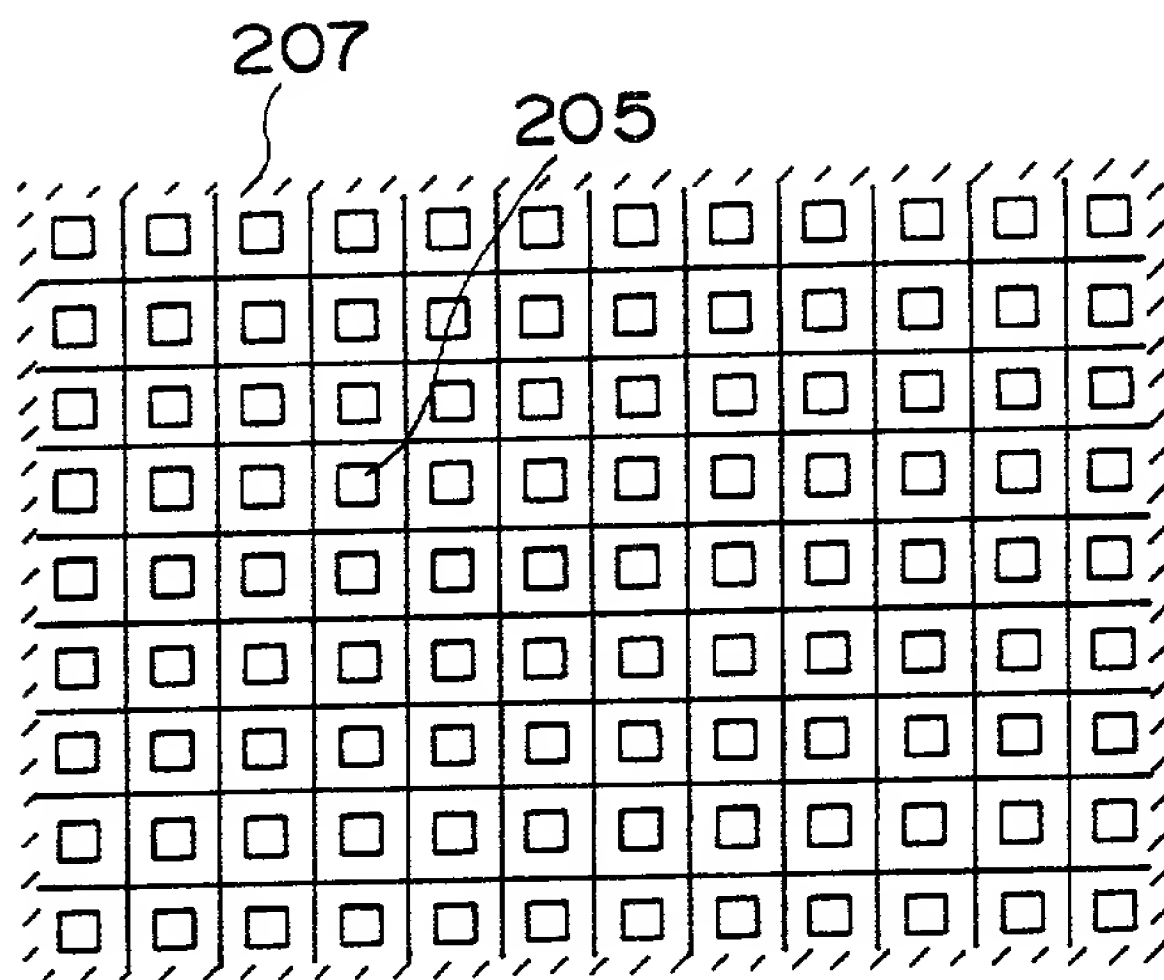


Fig. 38 (B)

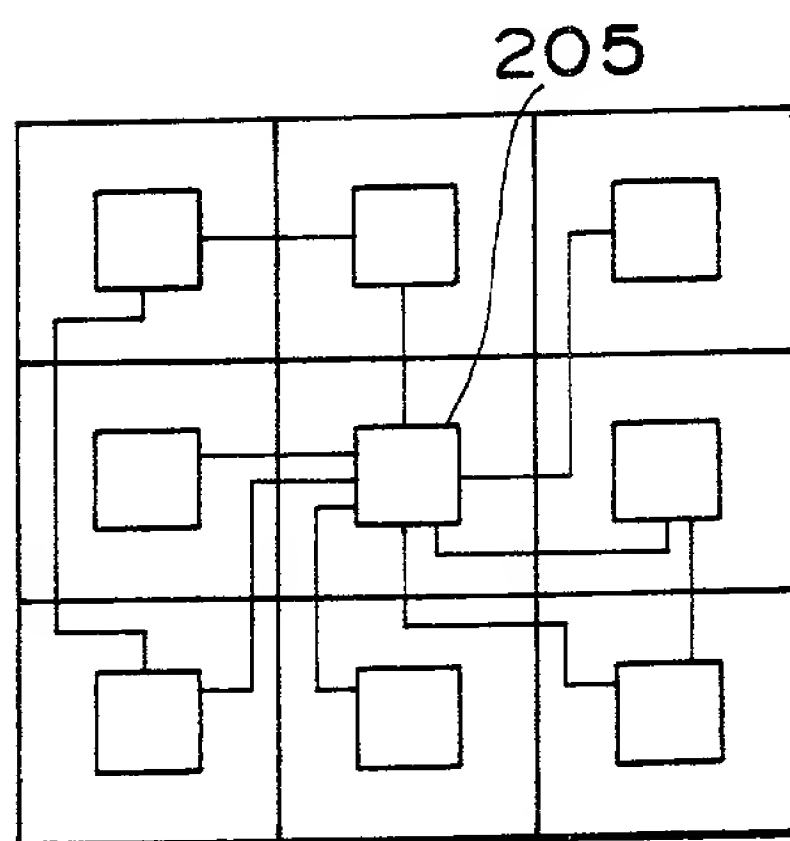


Fig. 39 (A)

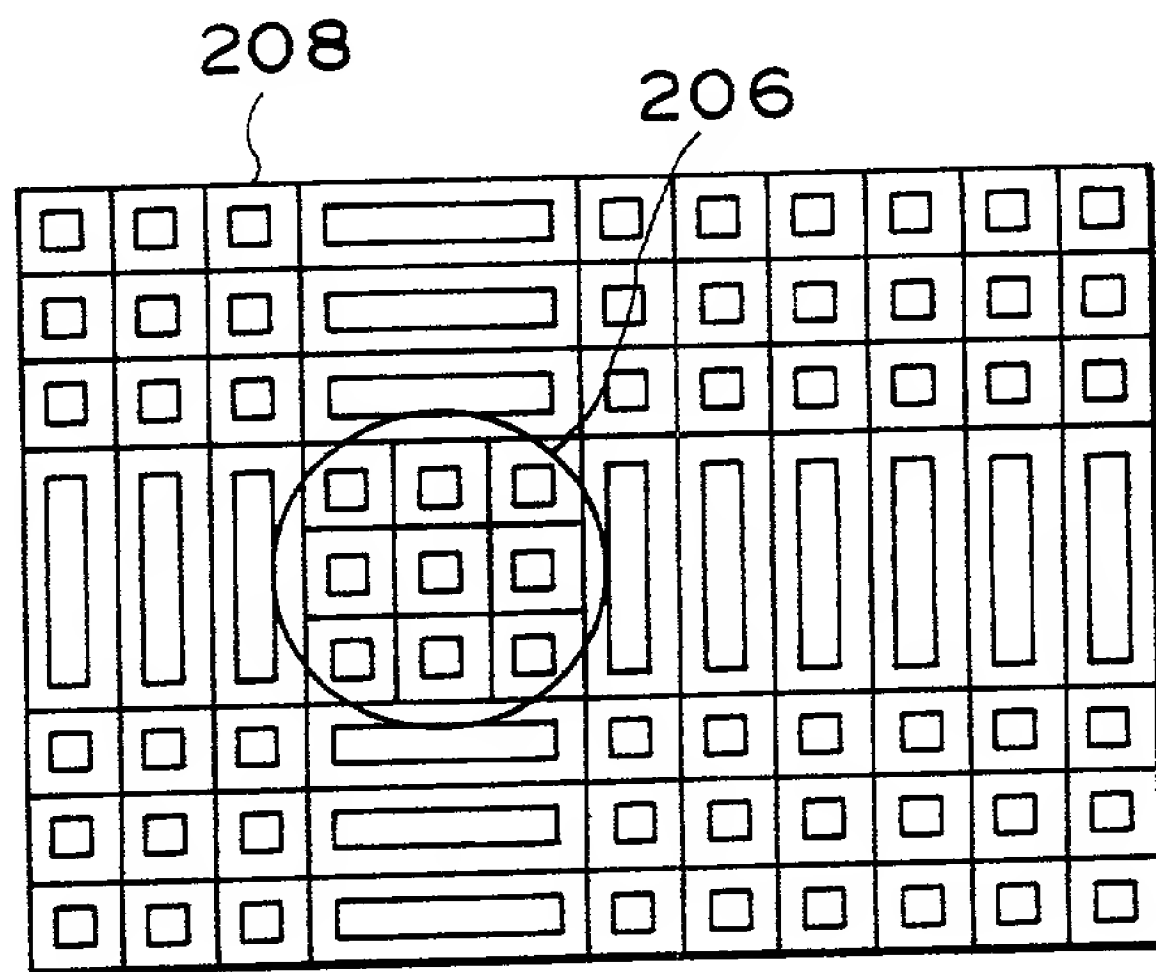


Fig. 39 (B)

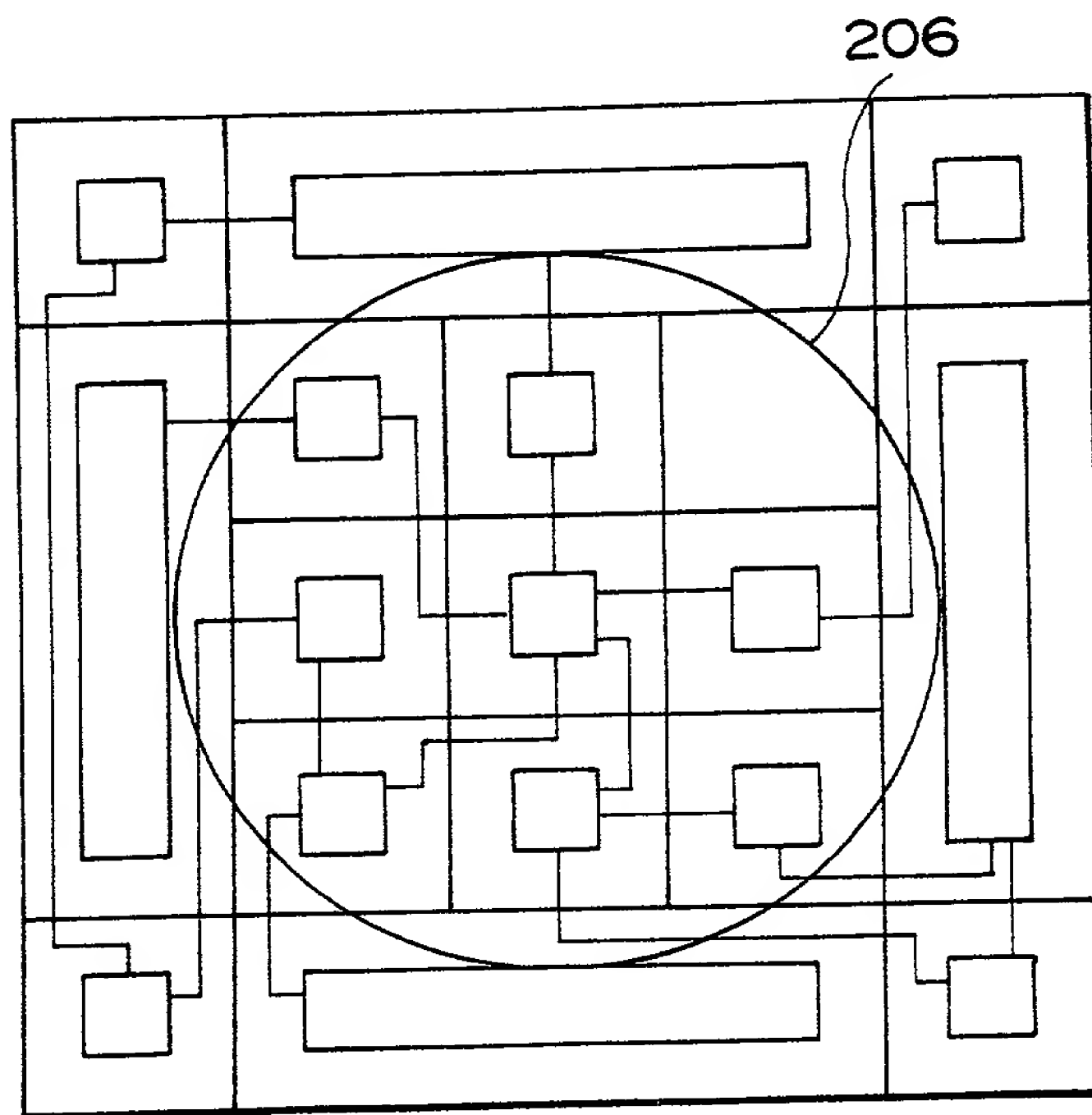


Fig. 40(A)

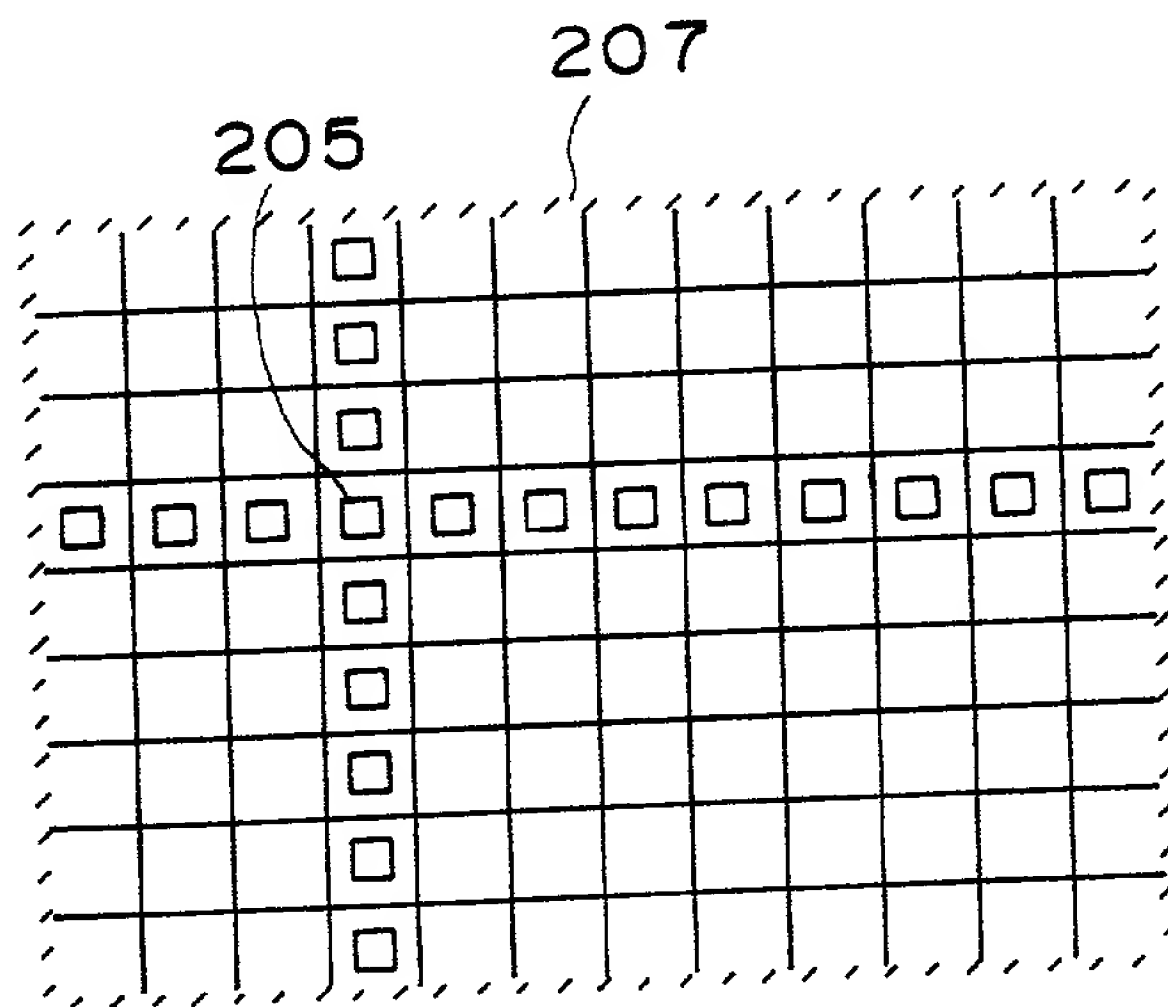


Fig. 40(B)

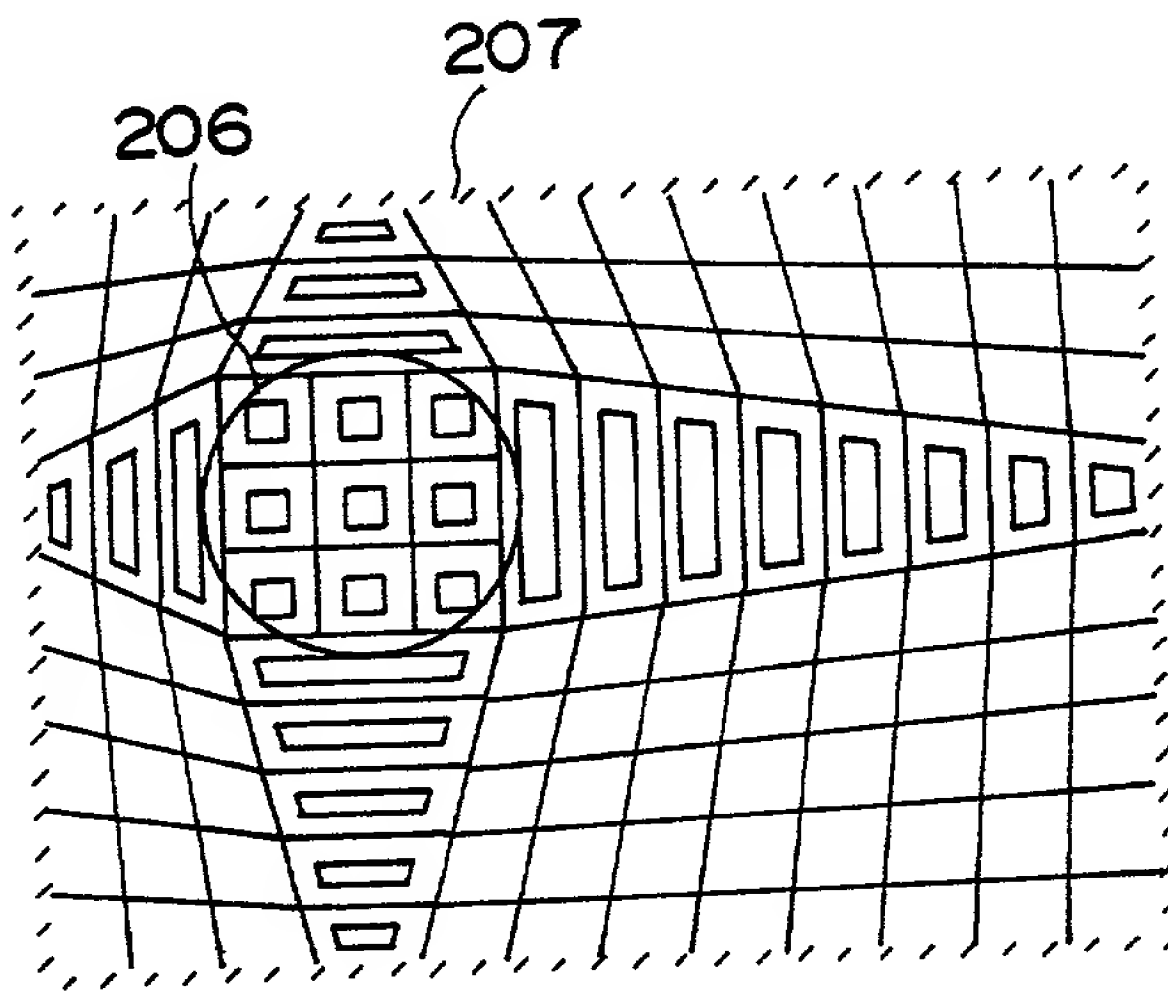


Fig. 41(A)

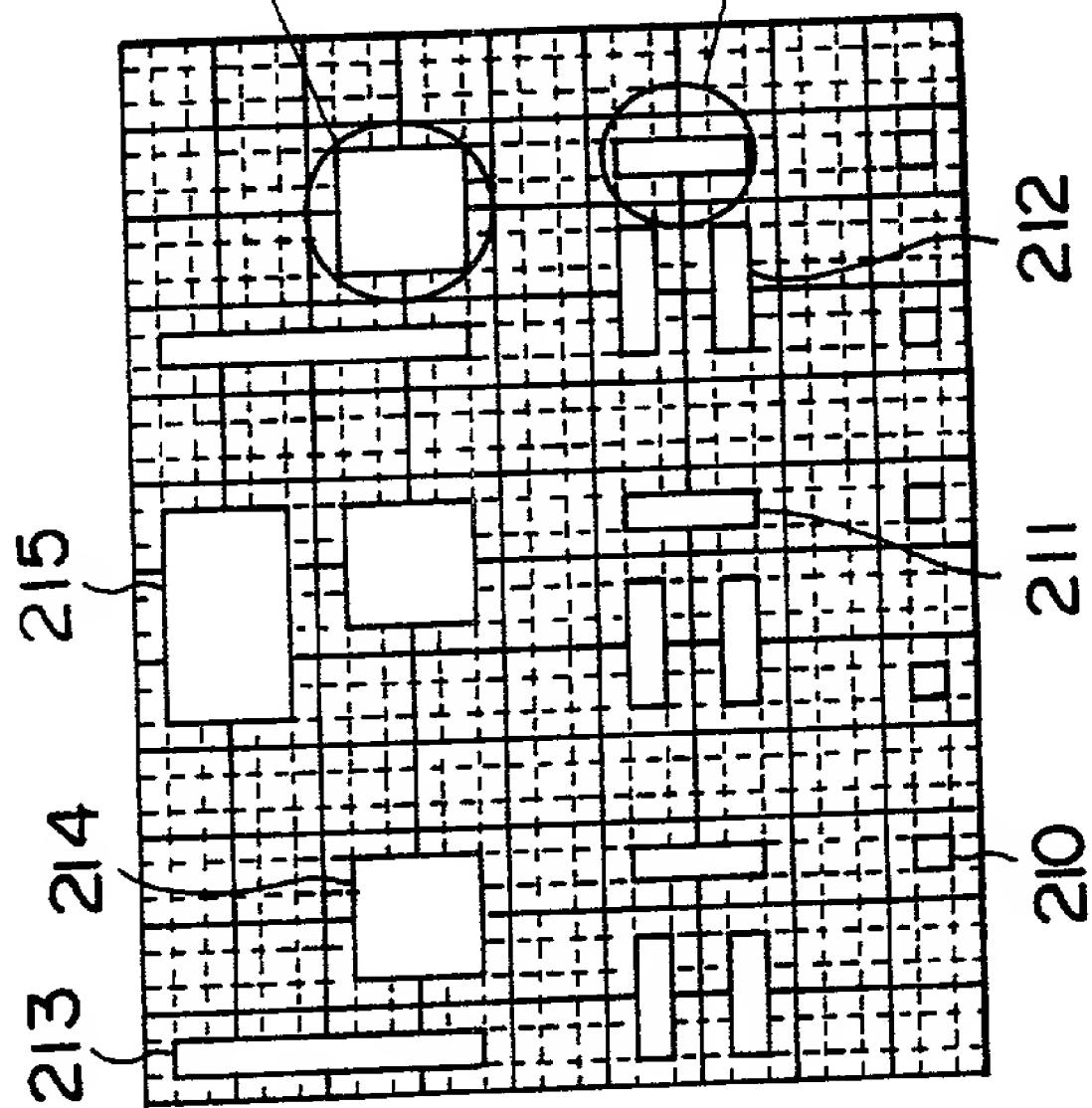


Fig. 41(B)

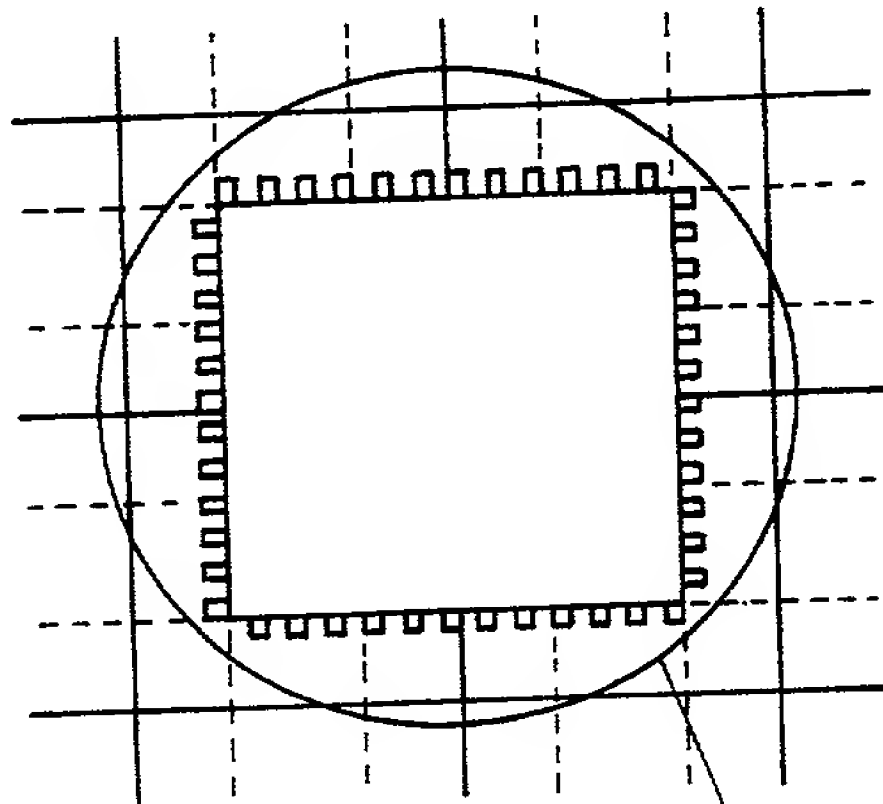


Fig. 41(C)

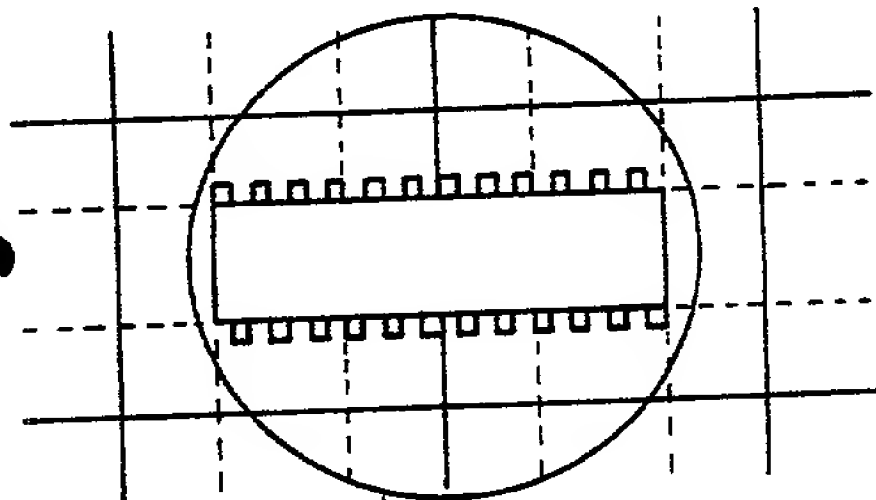


Fig. 42

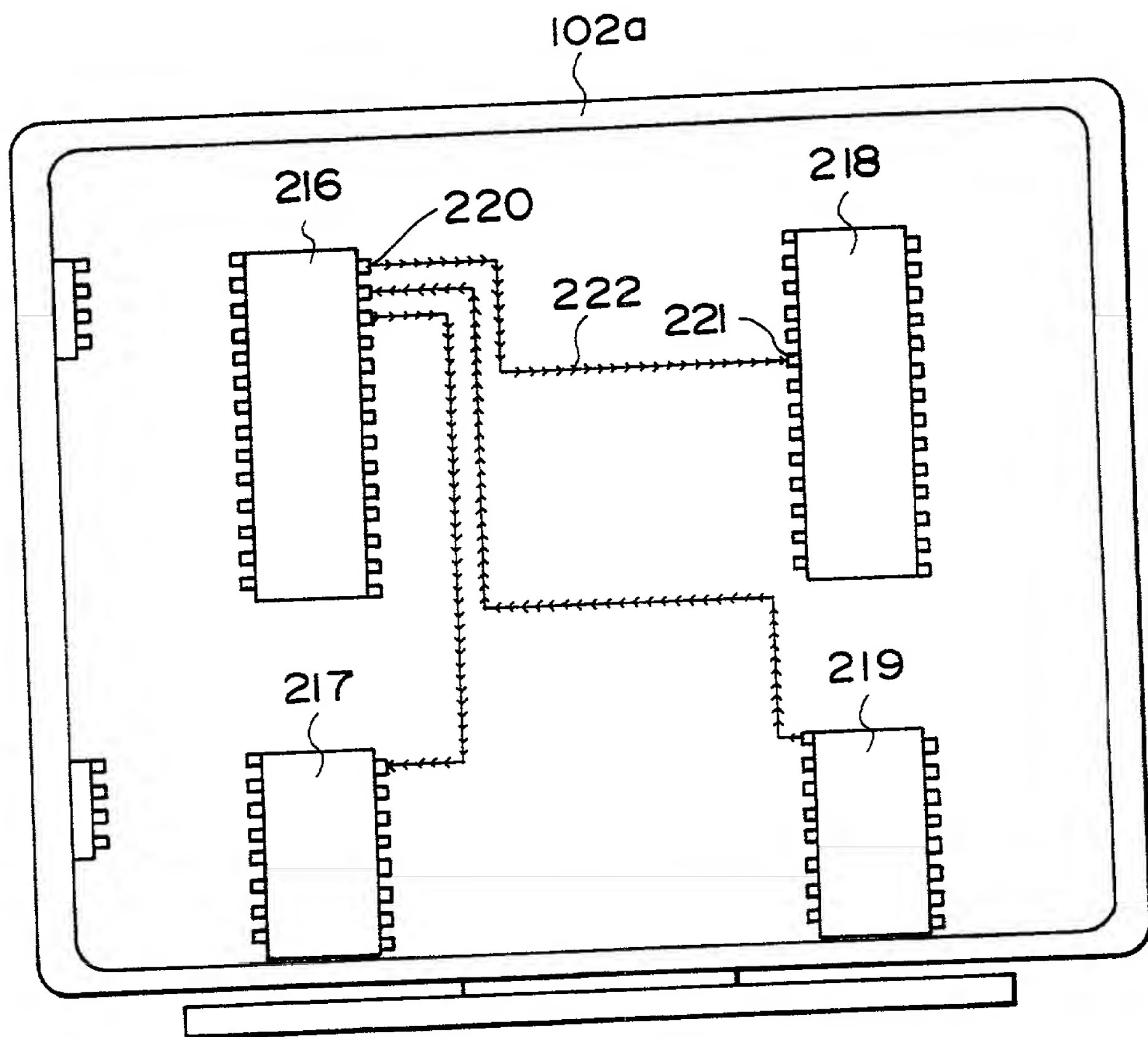


Fig. 43(A)

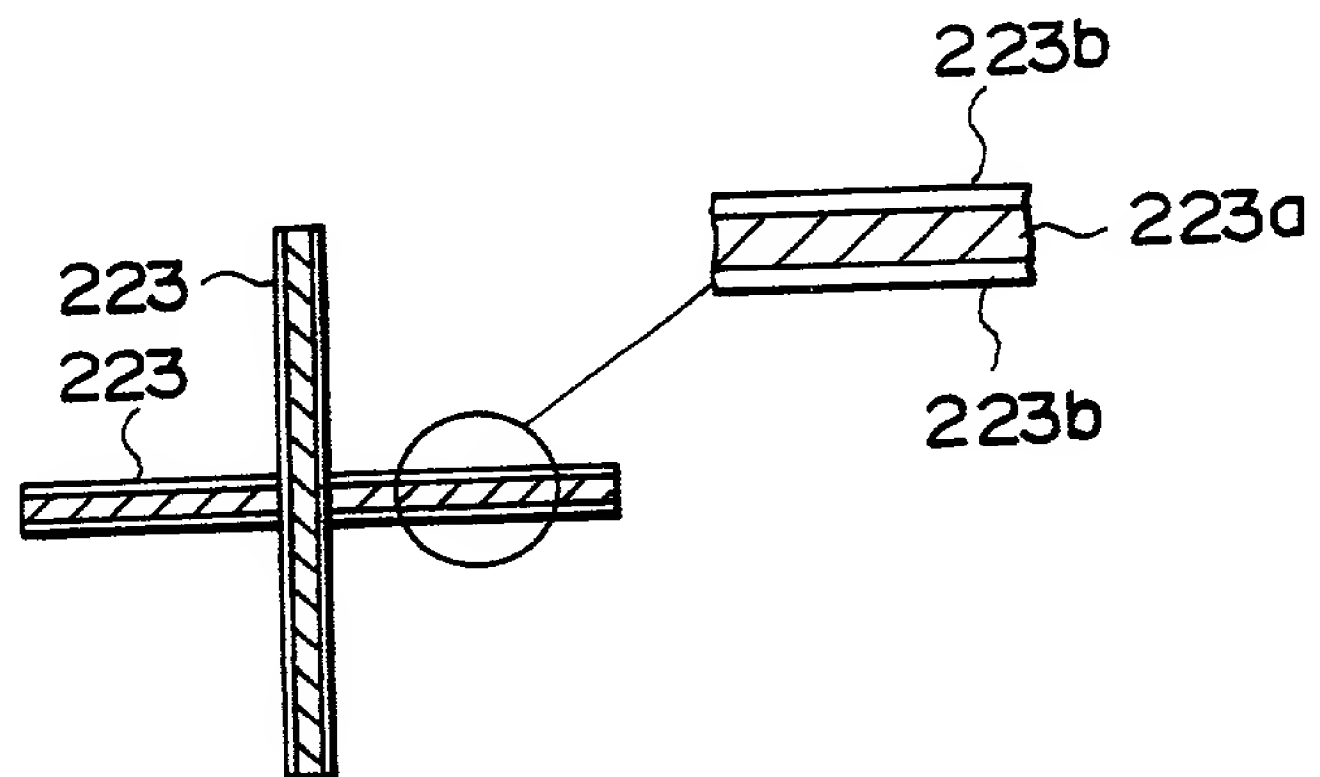


Fig. 43(B)

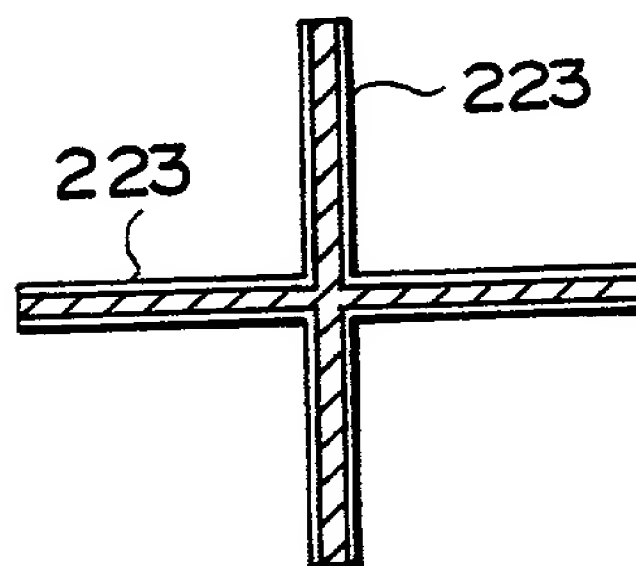


Fig. 44 (A)

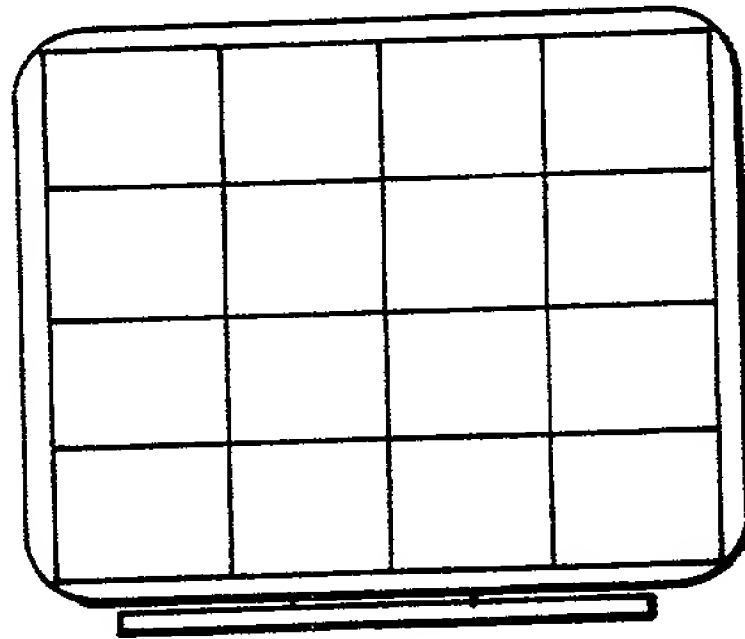


Fig. 44 (B)

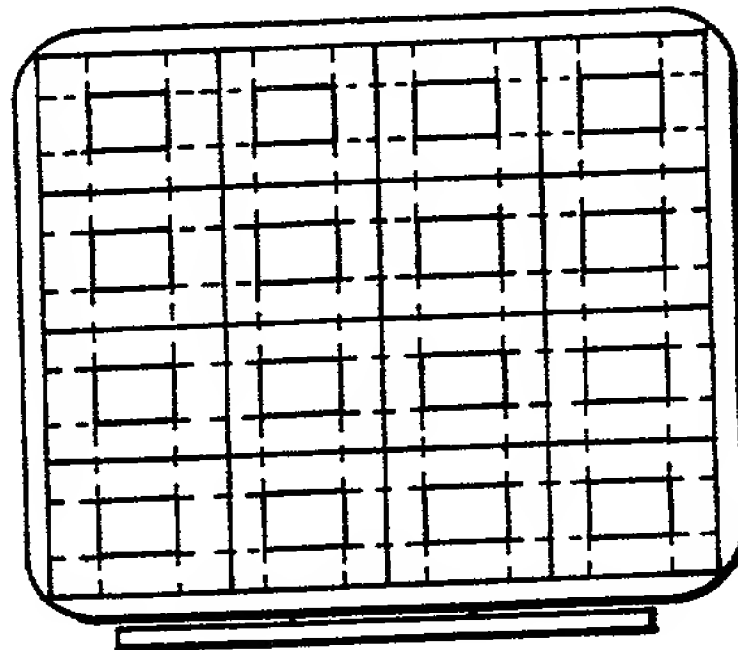


Fig. 44 (C)

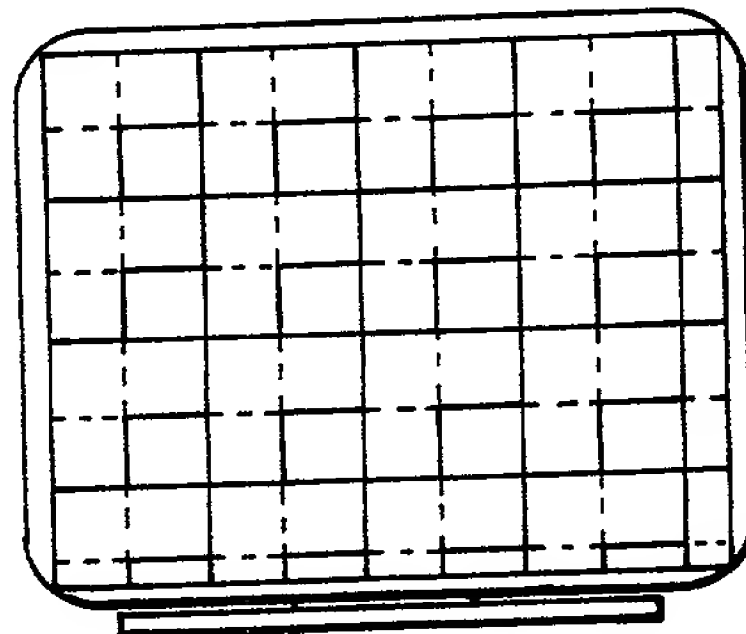


Fig. 45

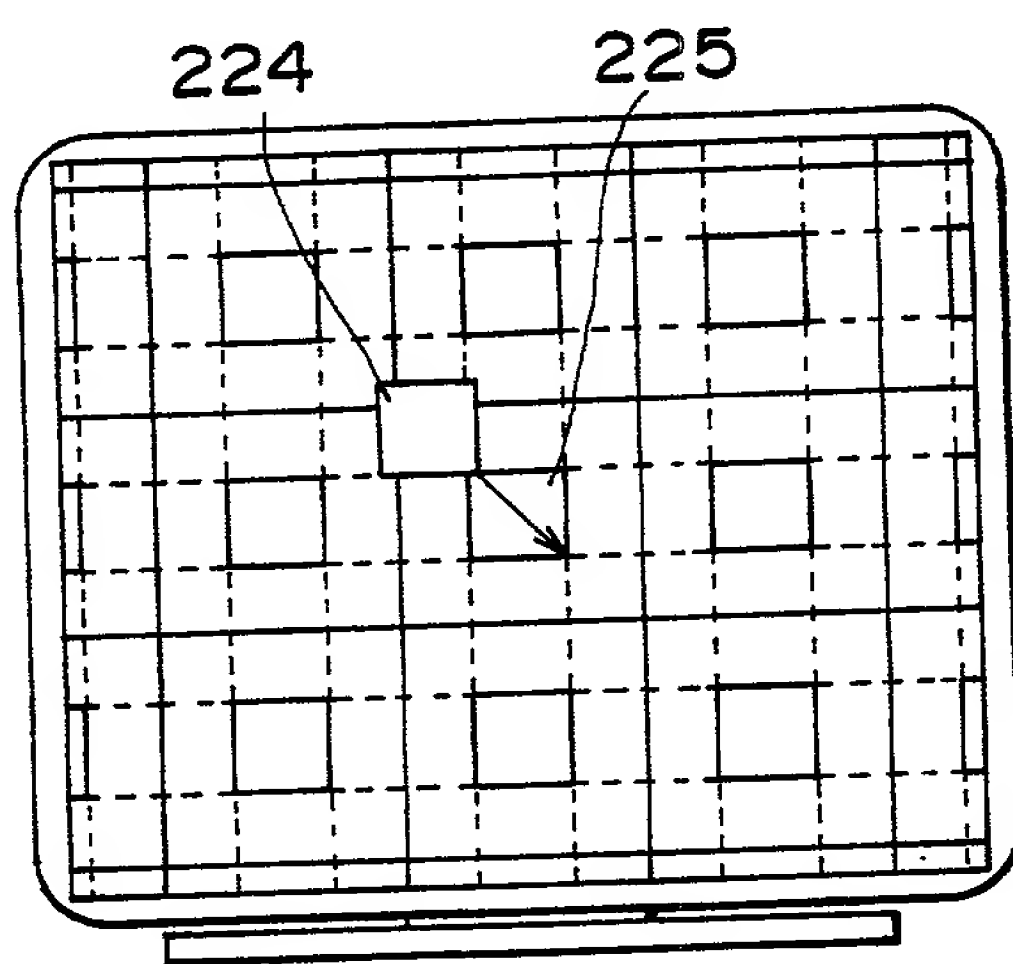


Fig.46(A)

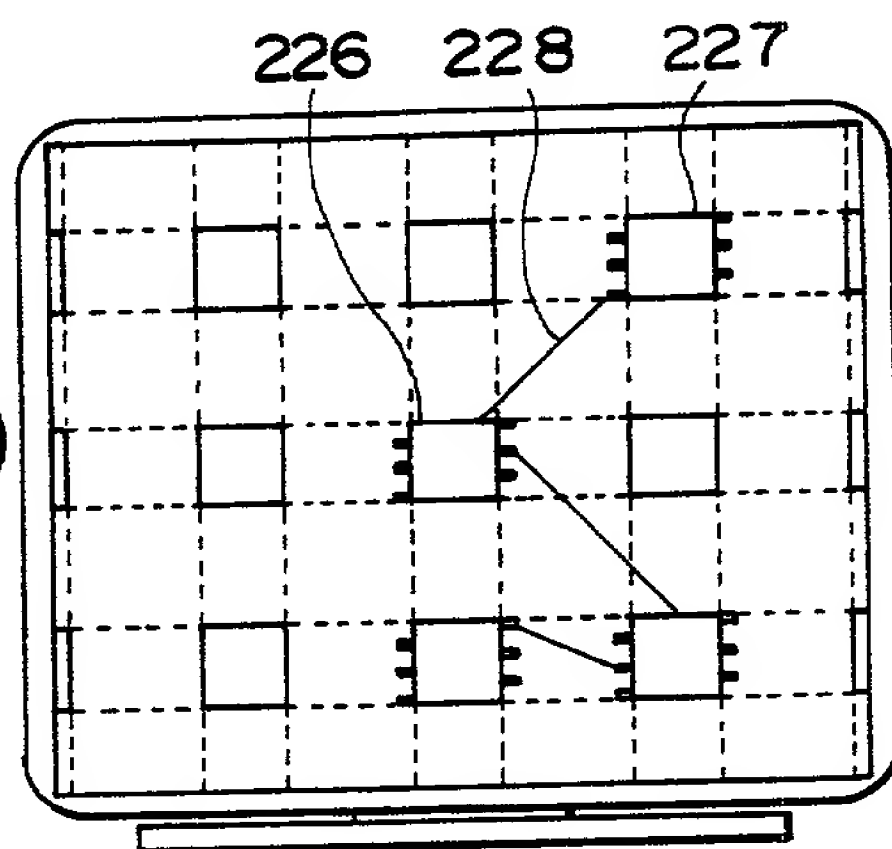


Fig.46(B)

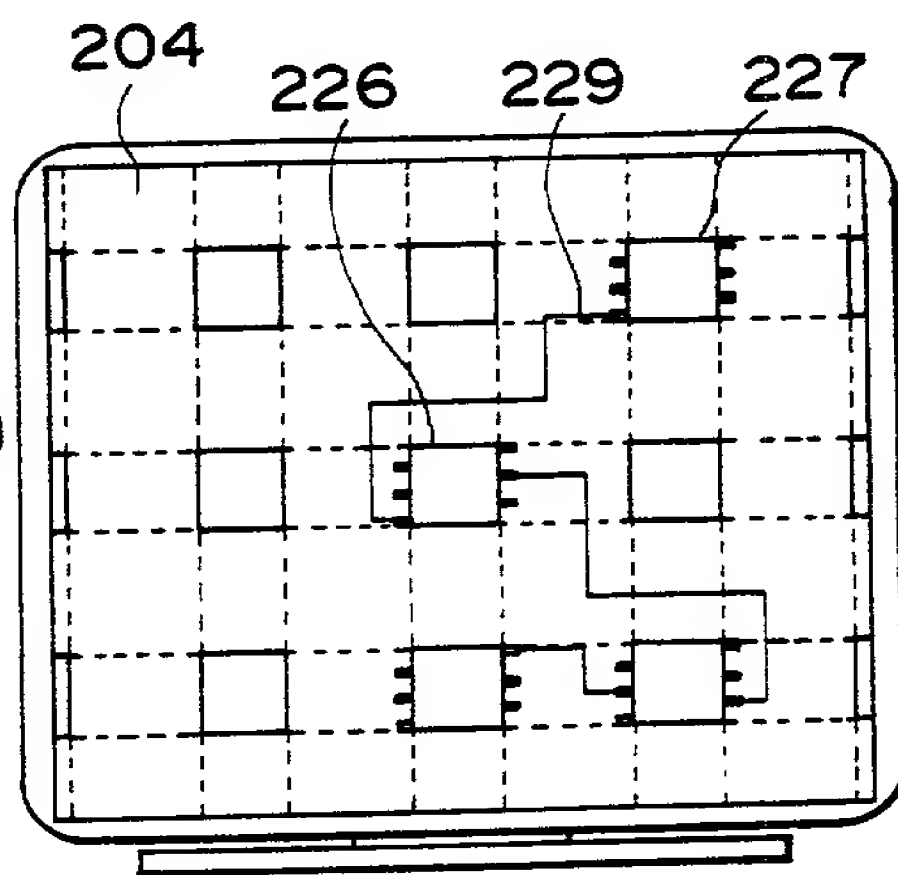


Fig. 47 (A)

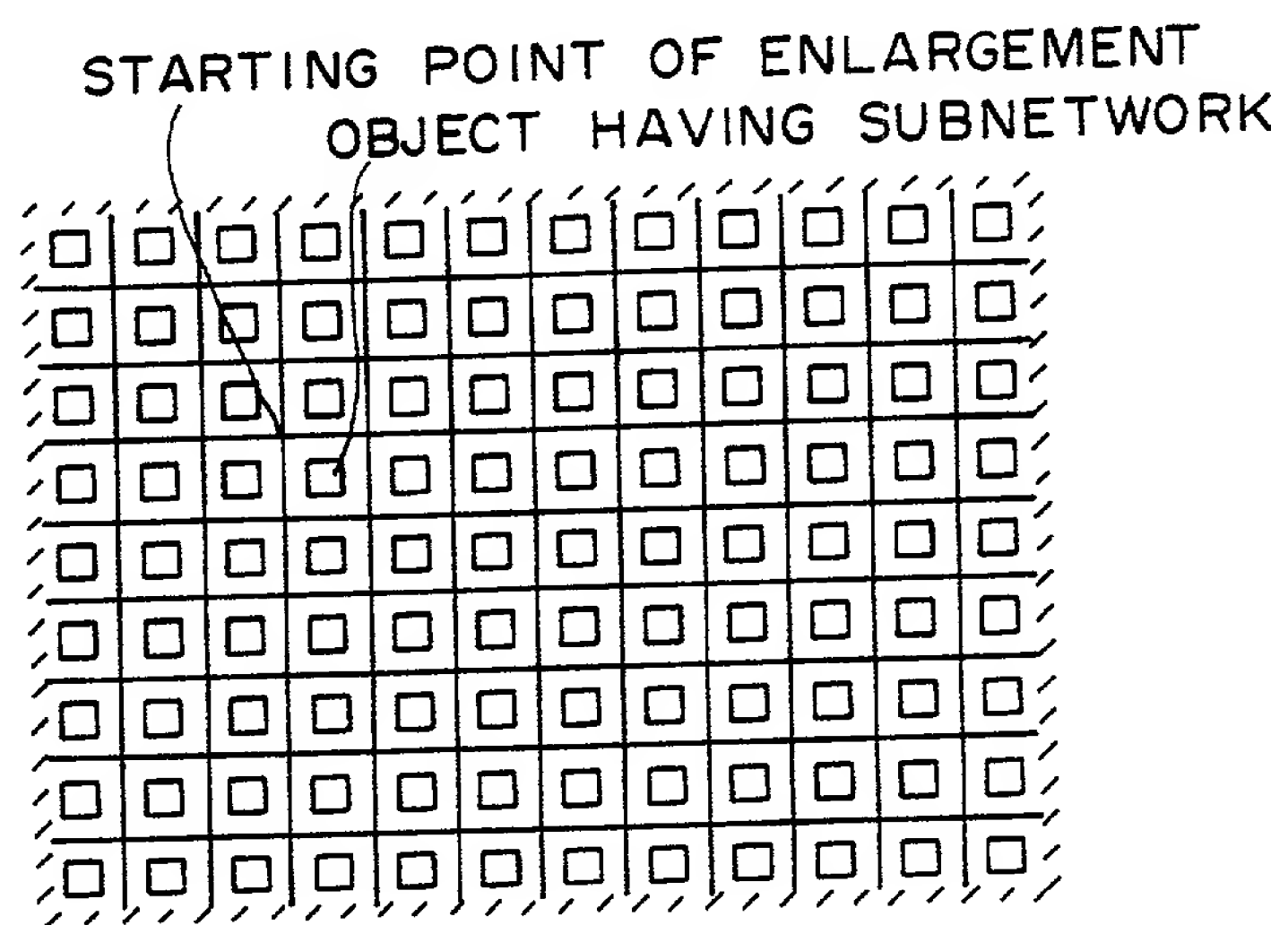


Fig. 47 (B)

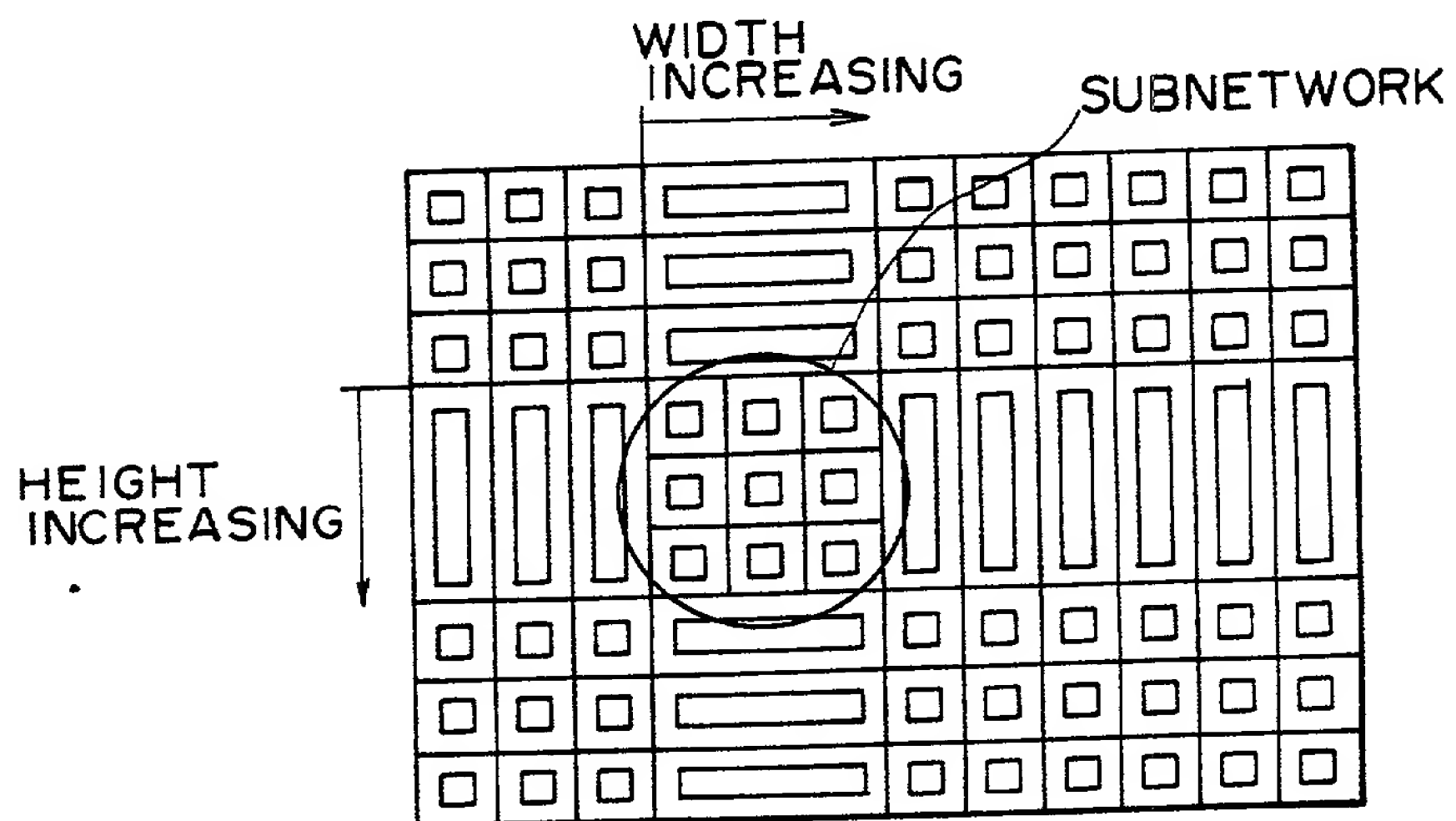


Fig. 48

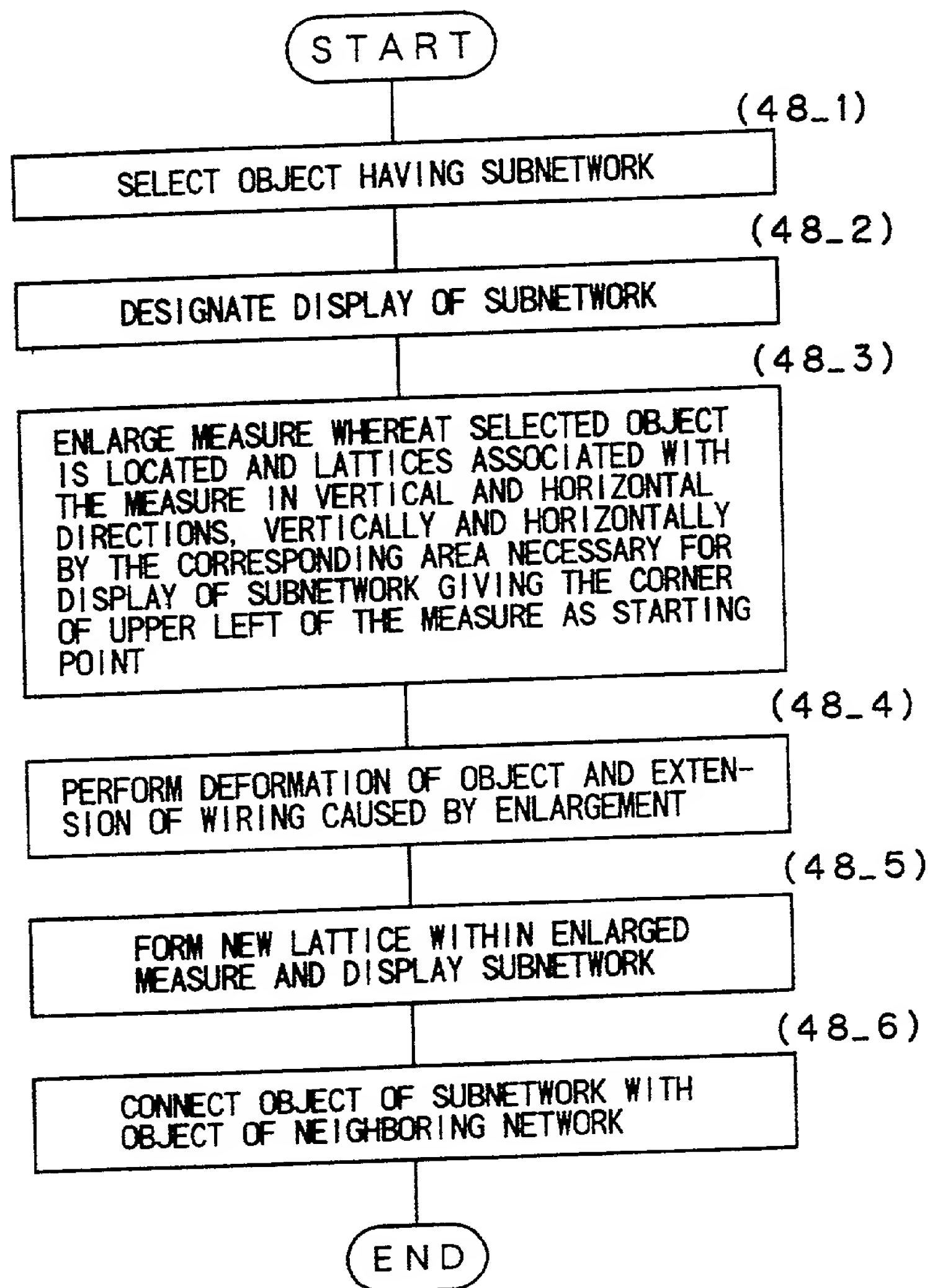


Fig.49(A)

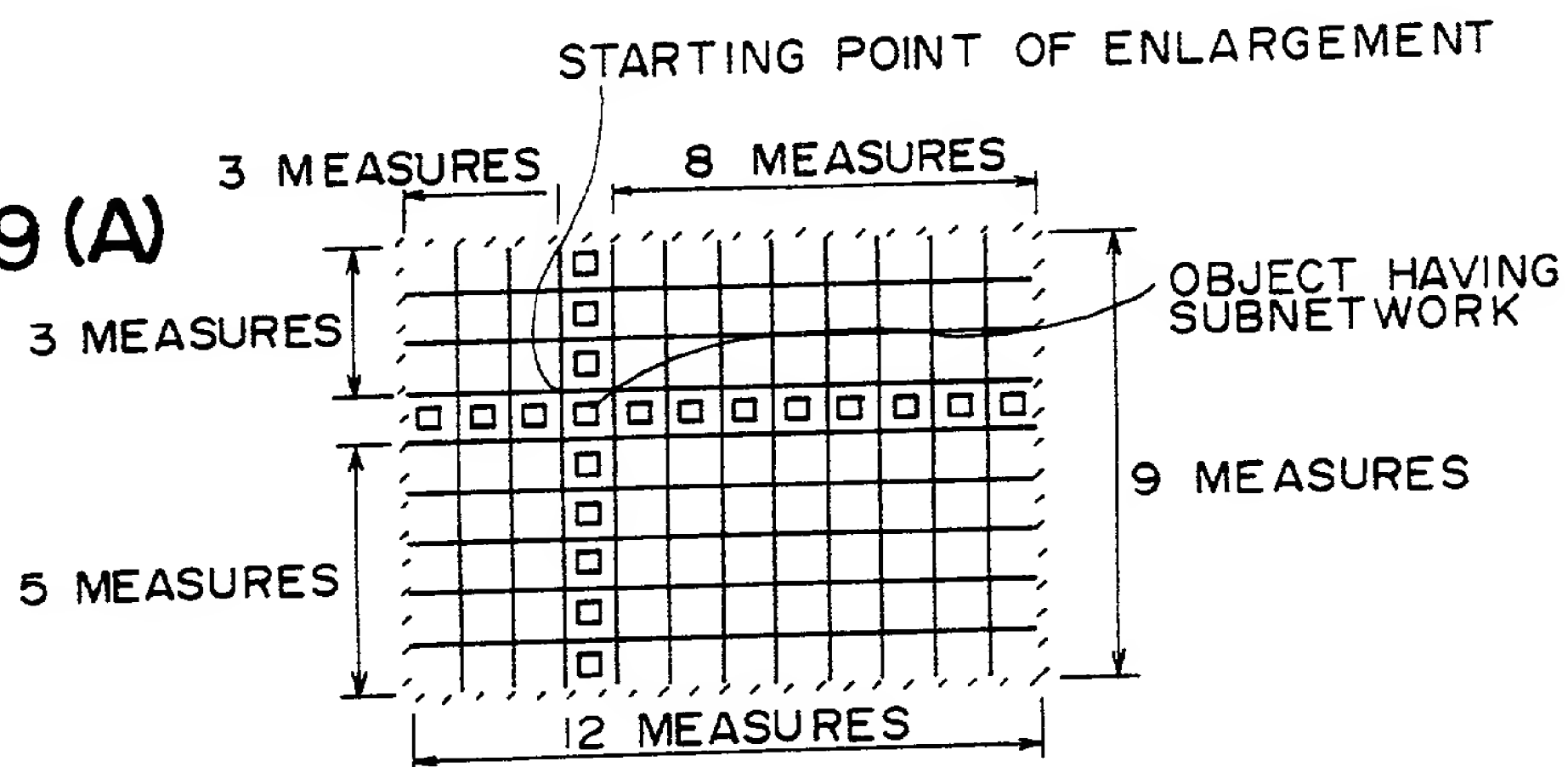


Fig.49(B)

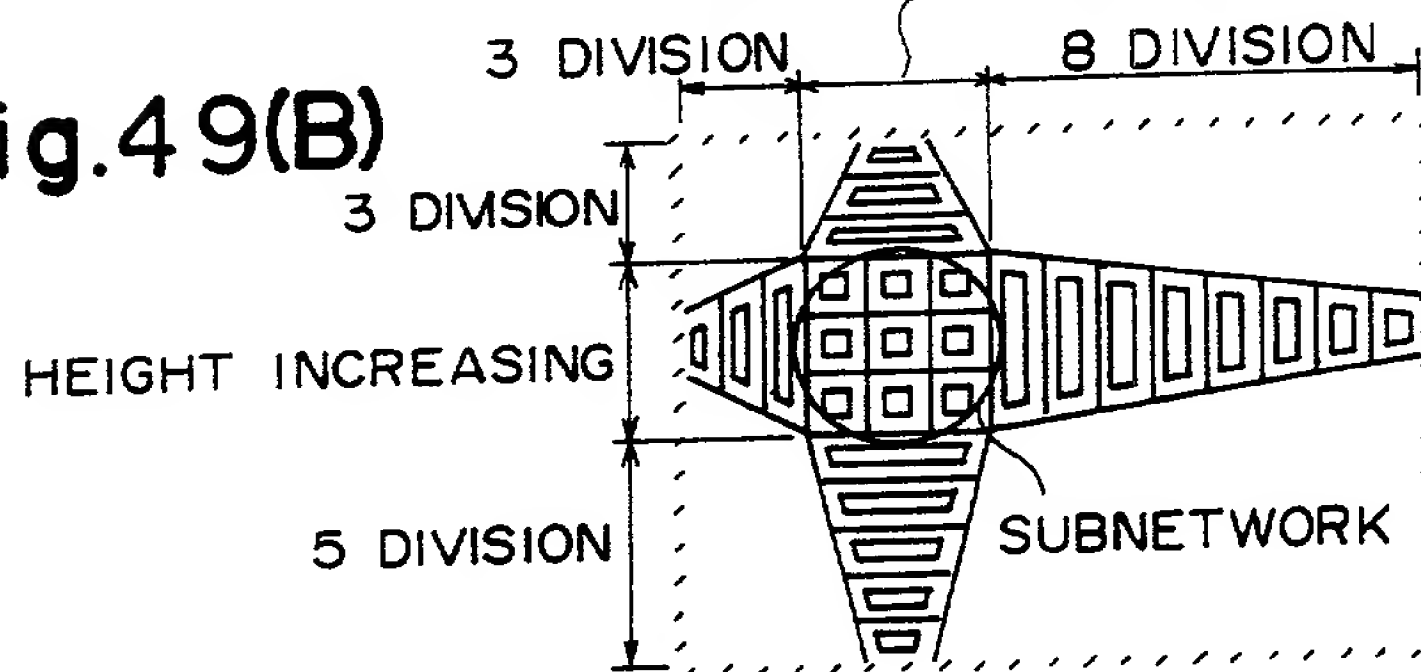


Fig.49(C)

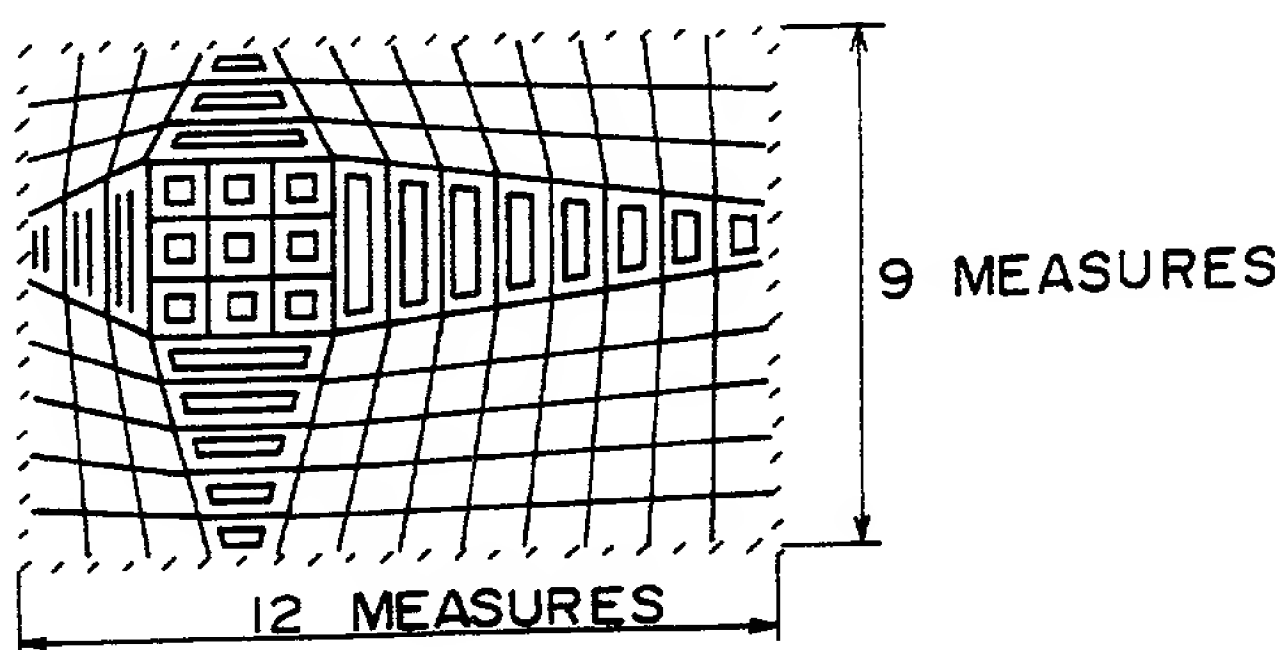


Fig. 50

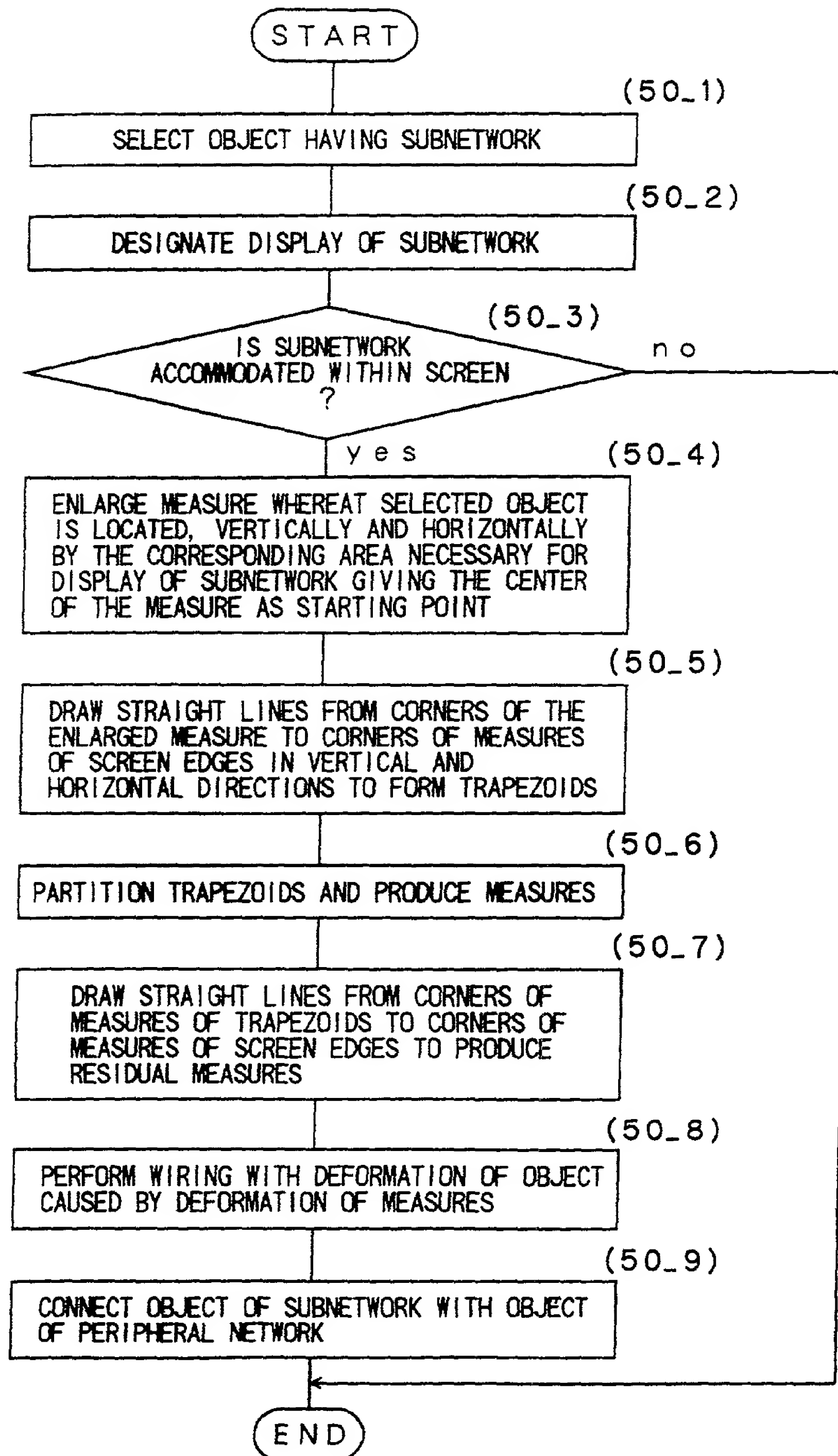


Fig. 50

Fig.51(A)

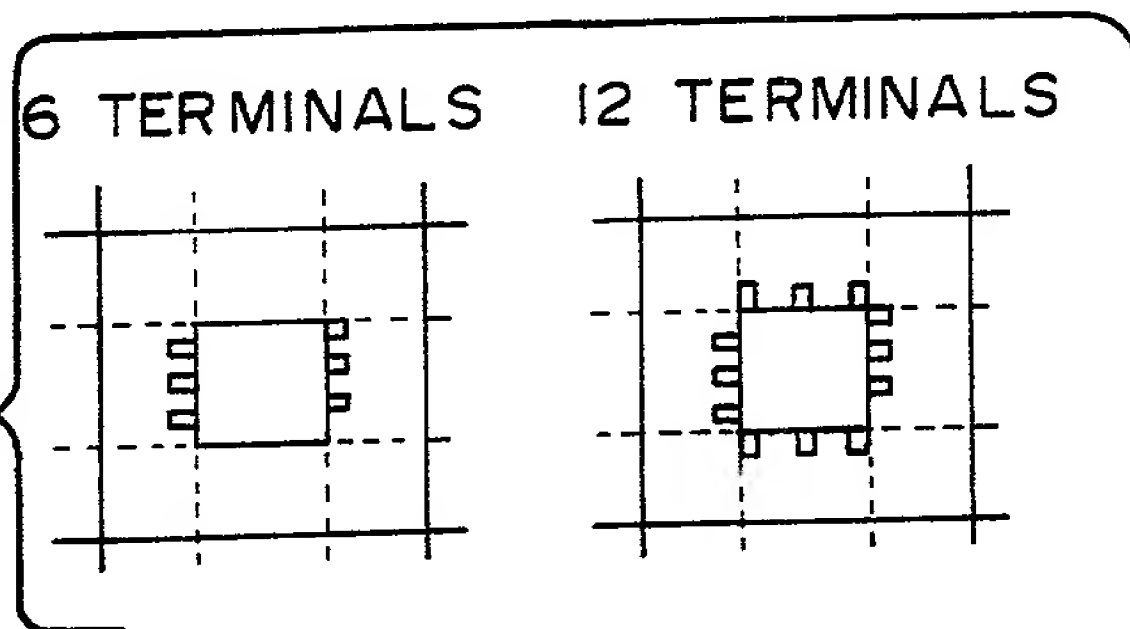


Fig.51(B)

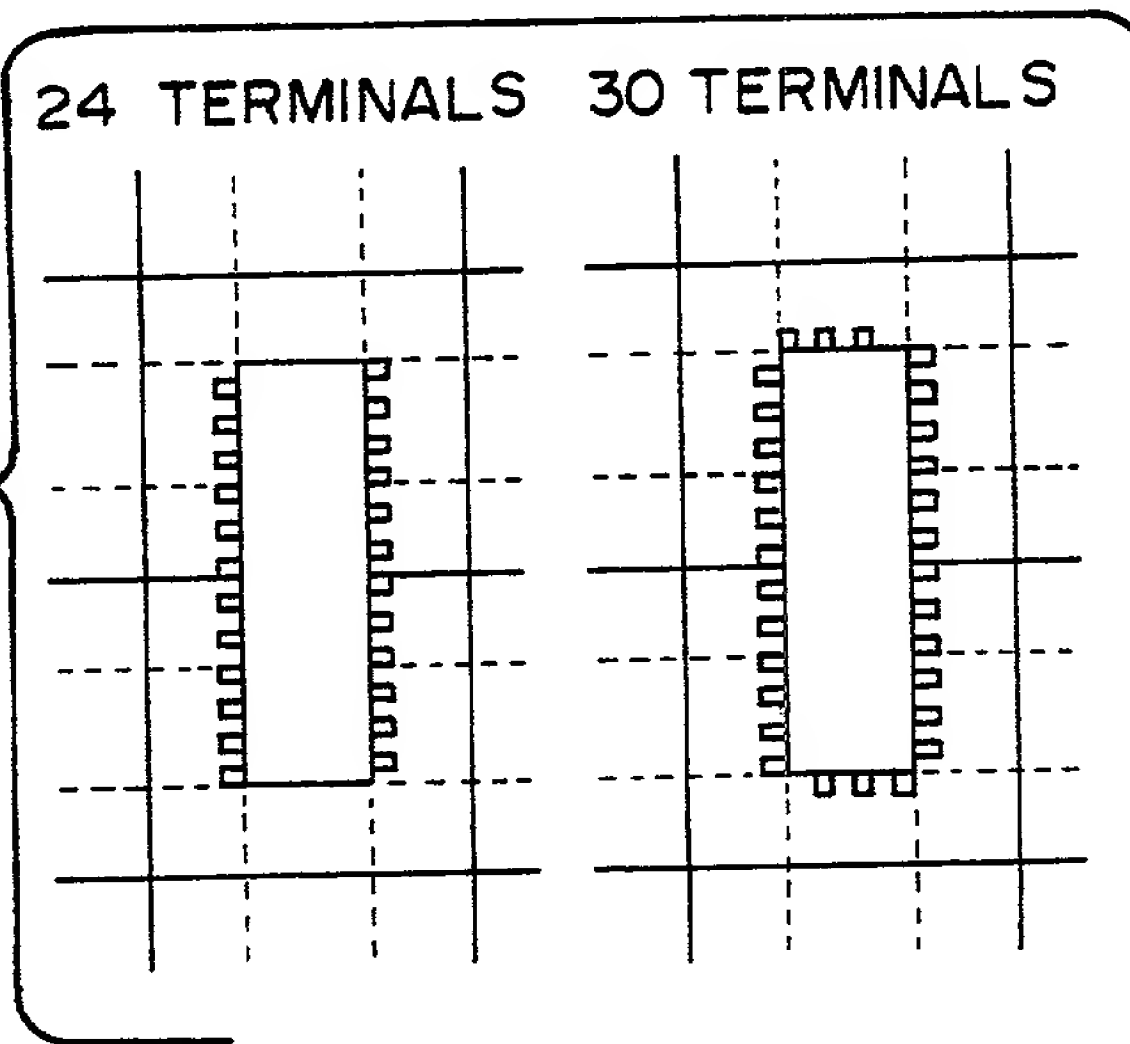


Fig.51(C)

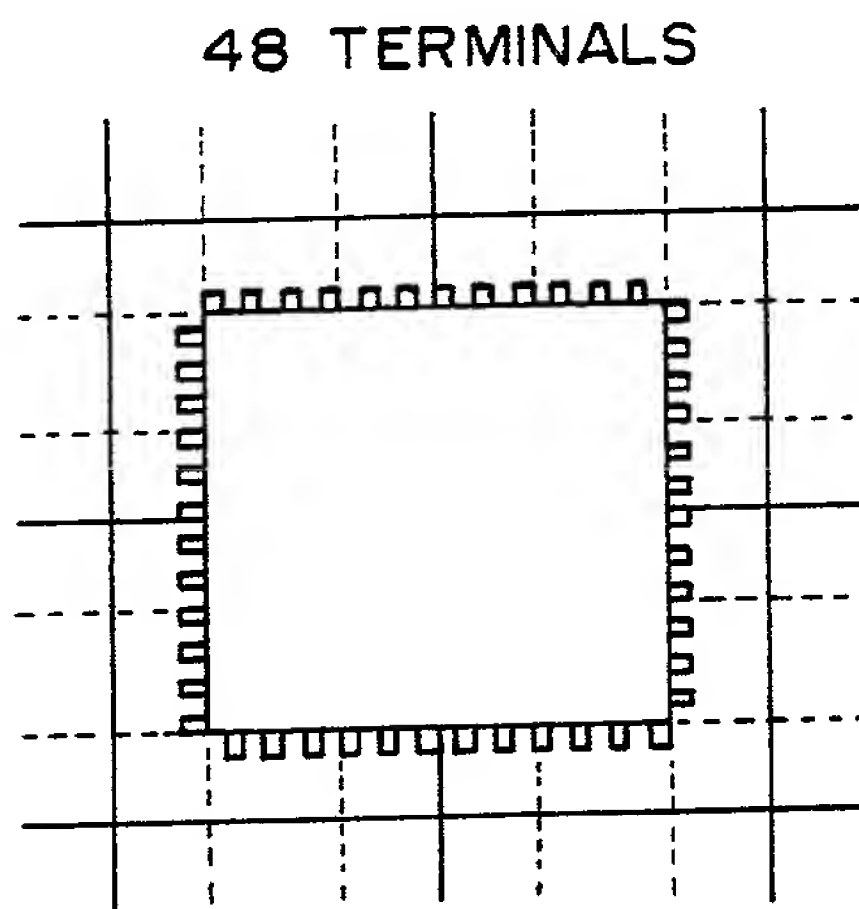


Fig.52(A)

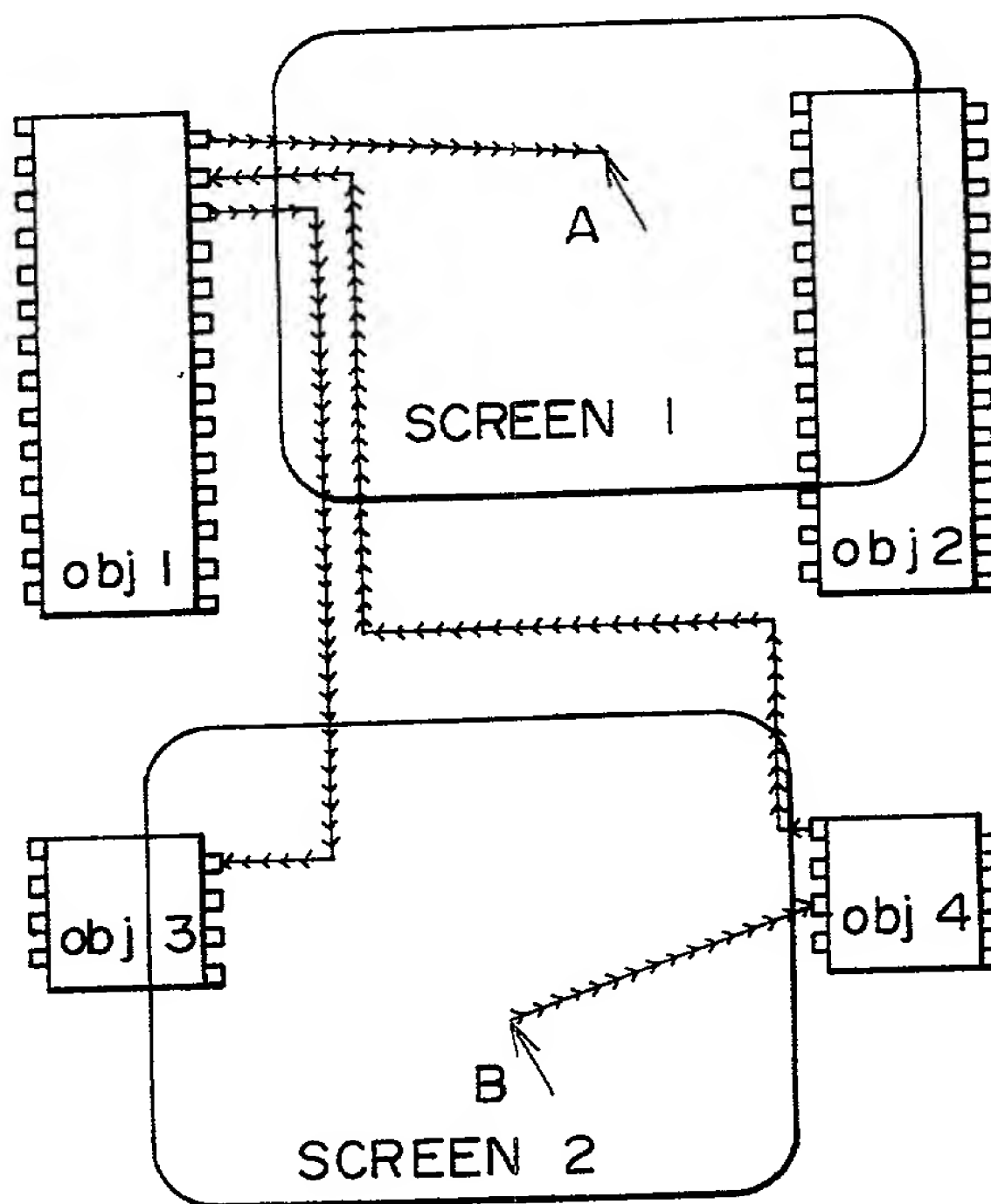


Fig.52(B)

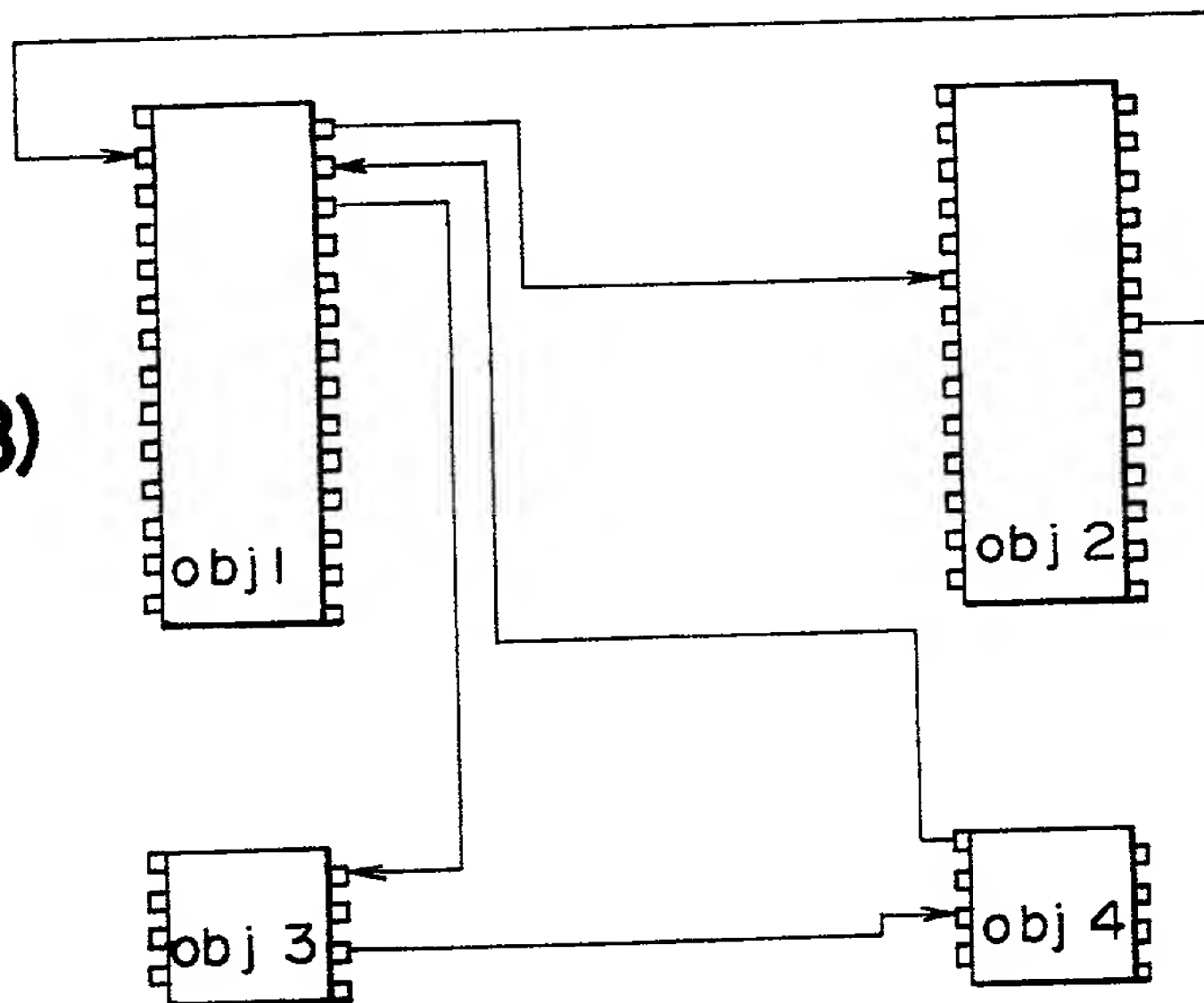


Fig. 53

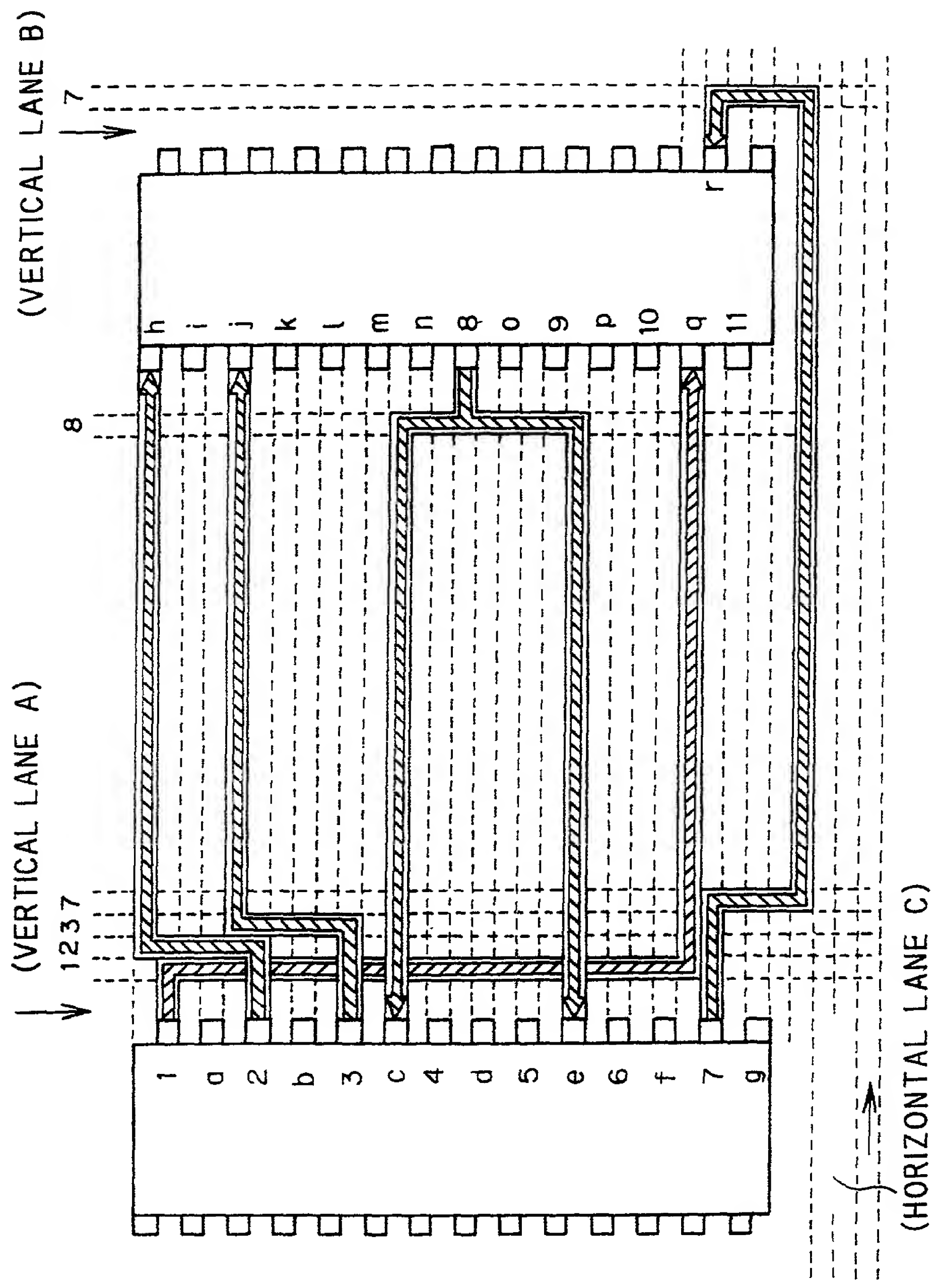


Fig. 54

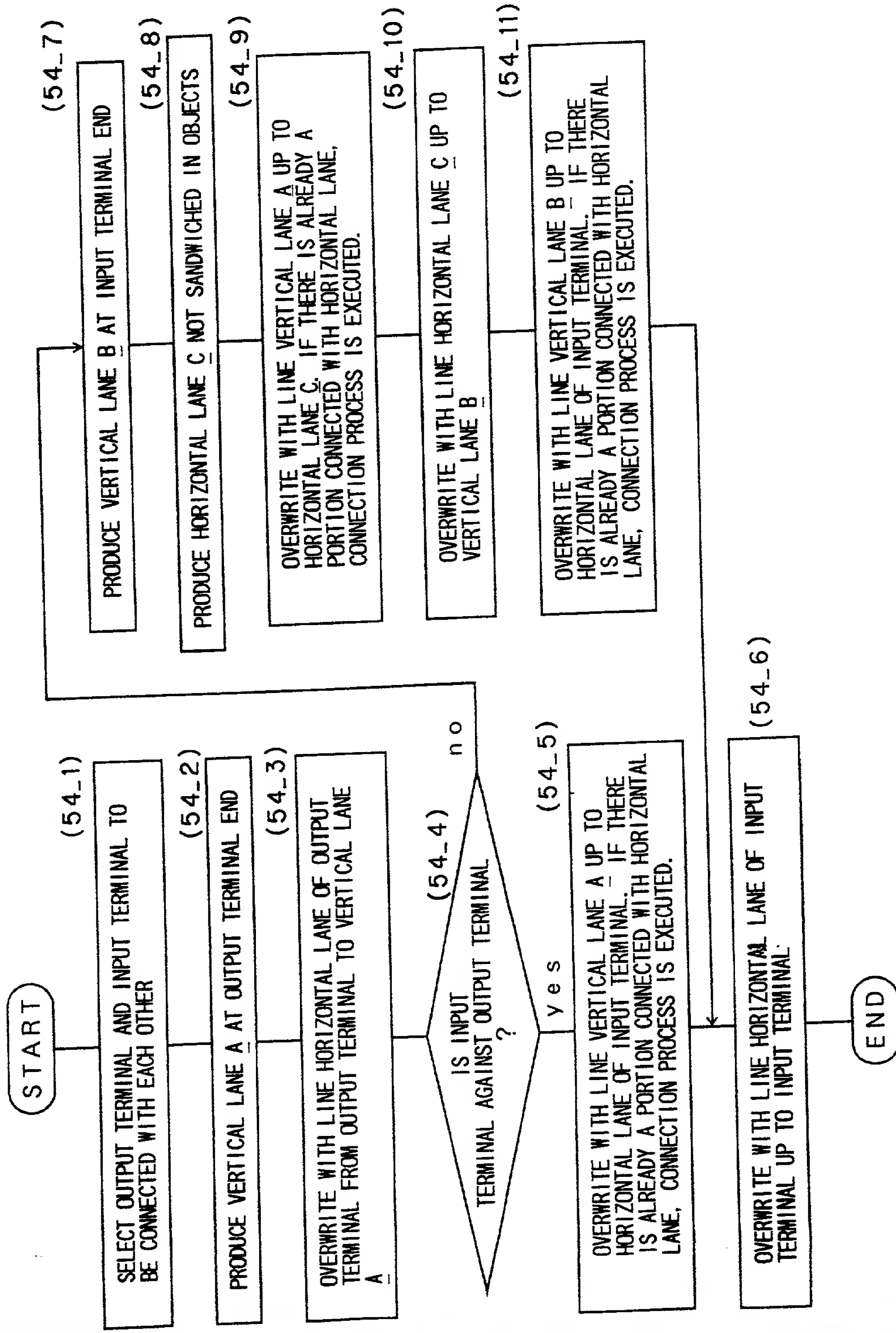


Fig. 55

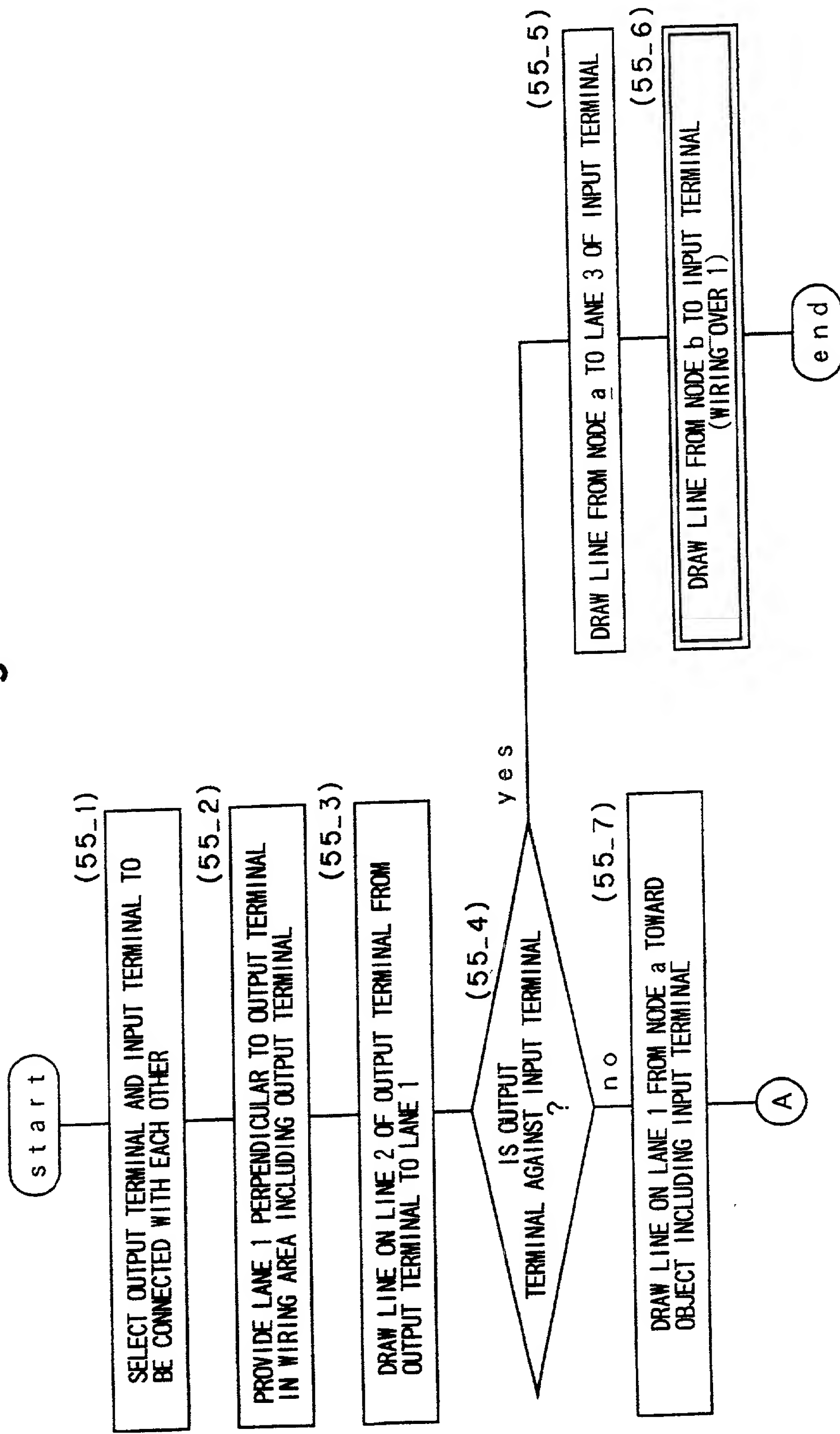


Fig. 56

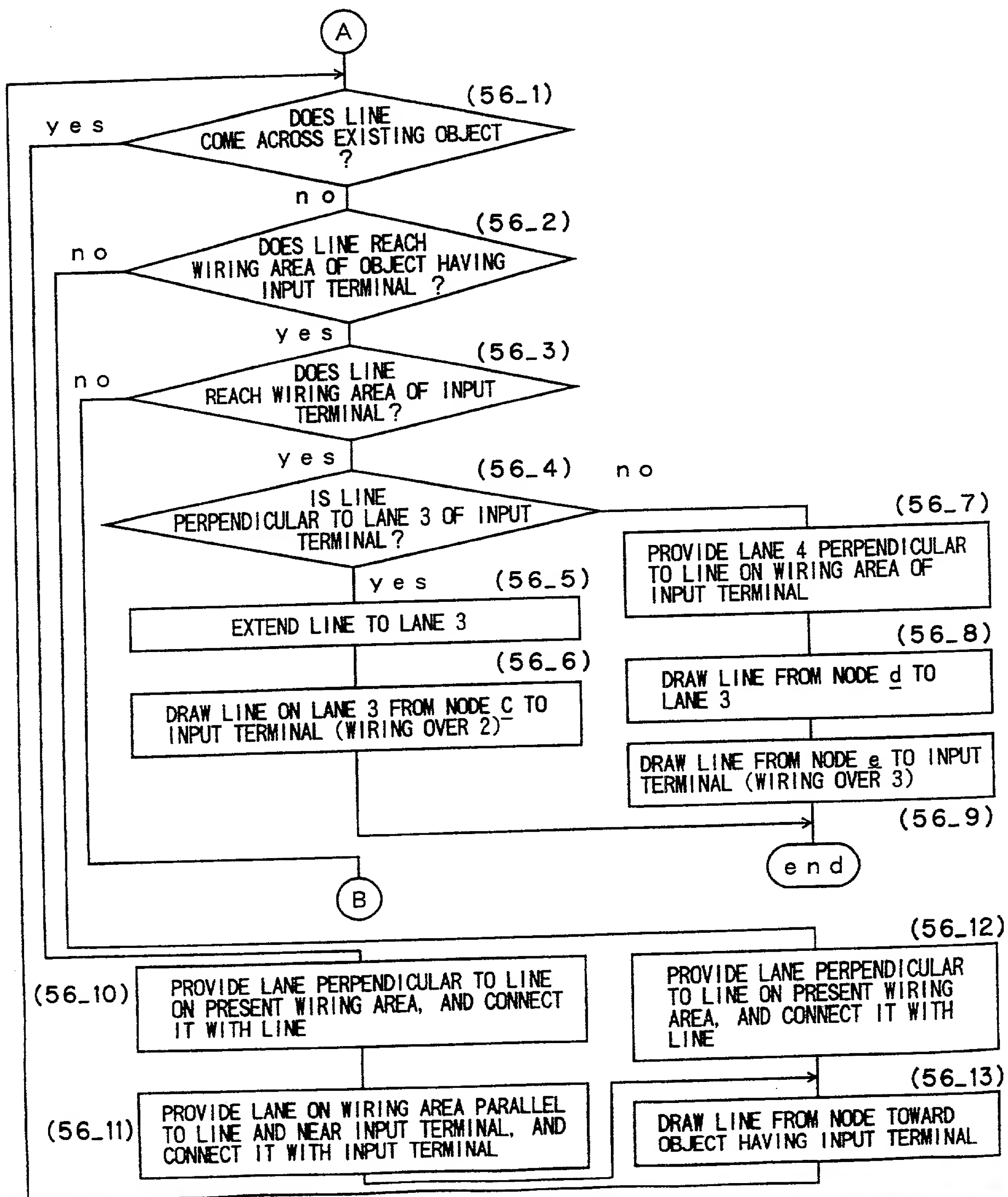


Fig. 57

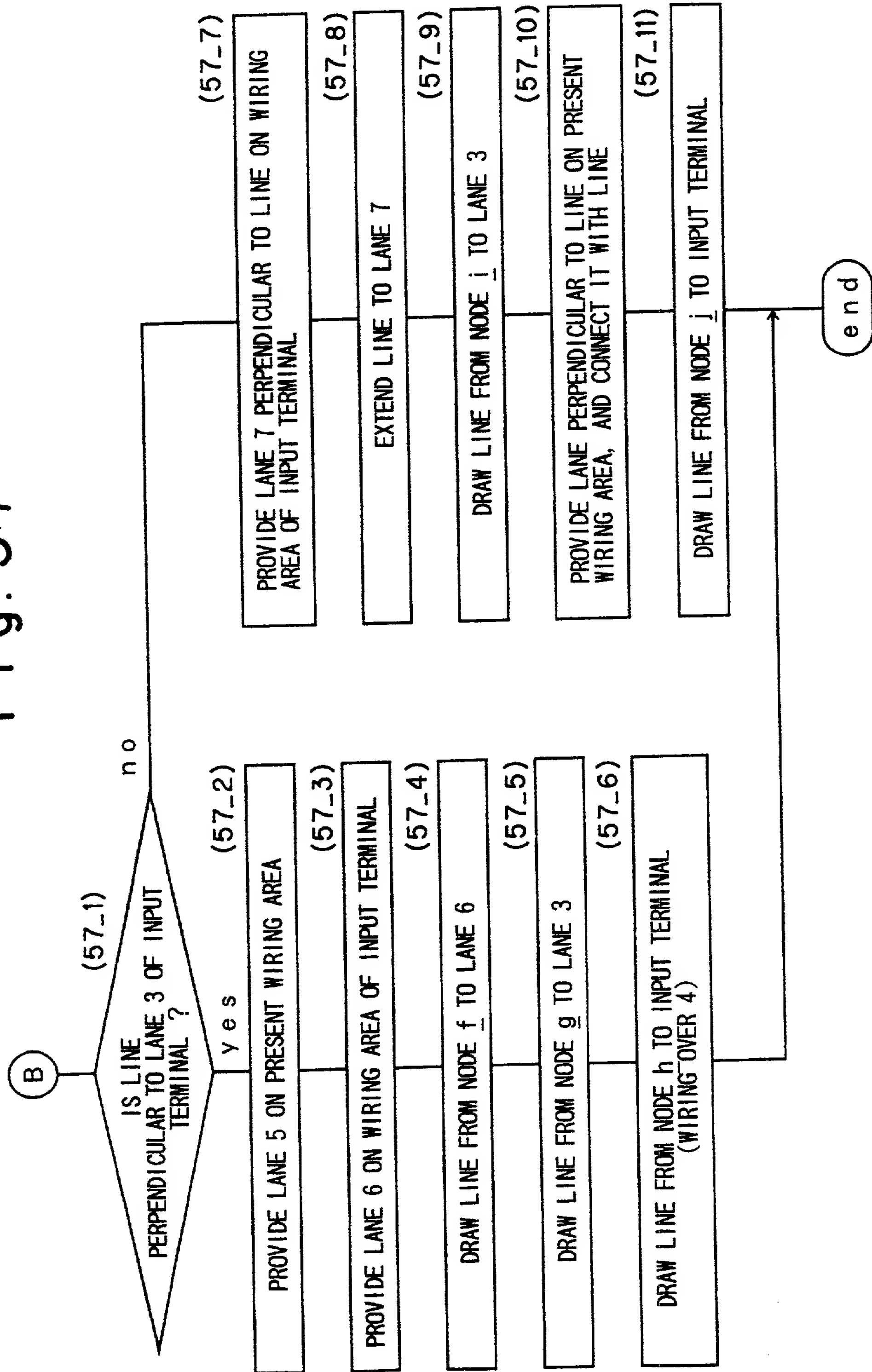


Fig. 58

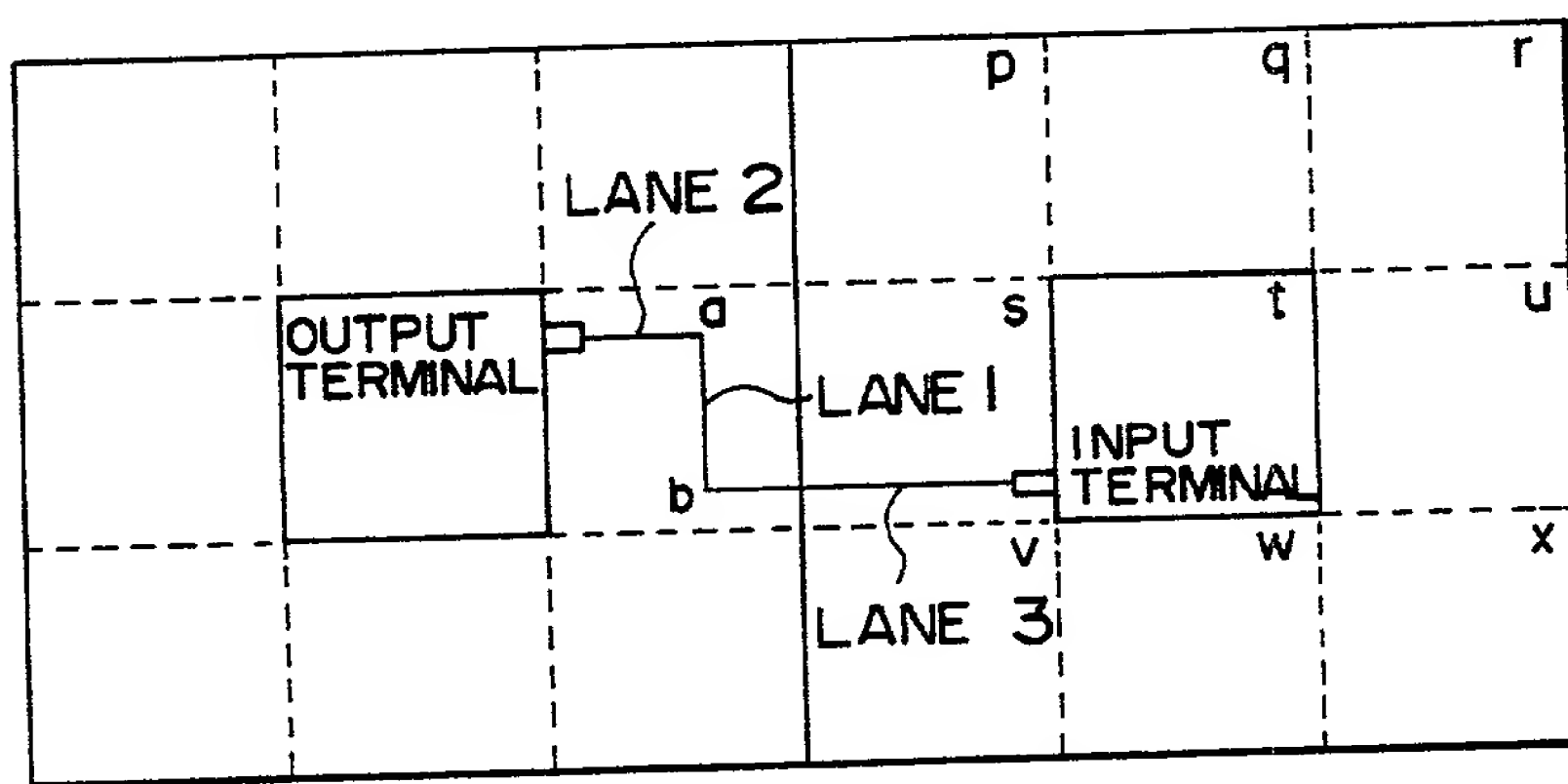


Fig. 59

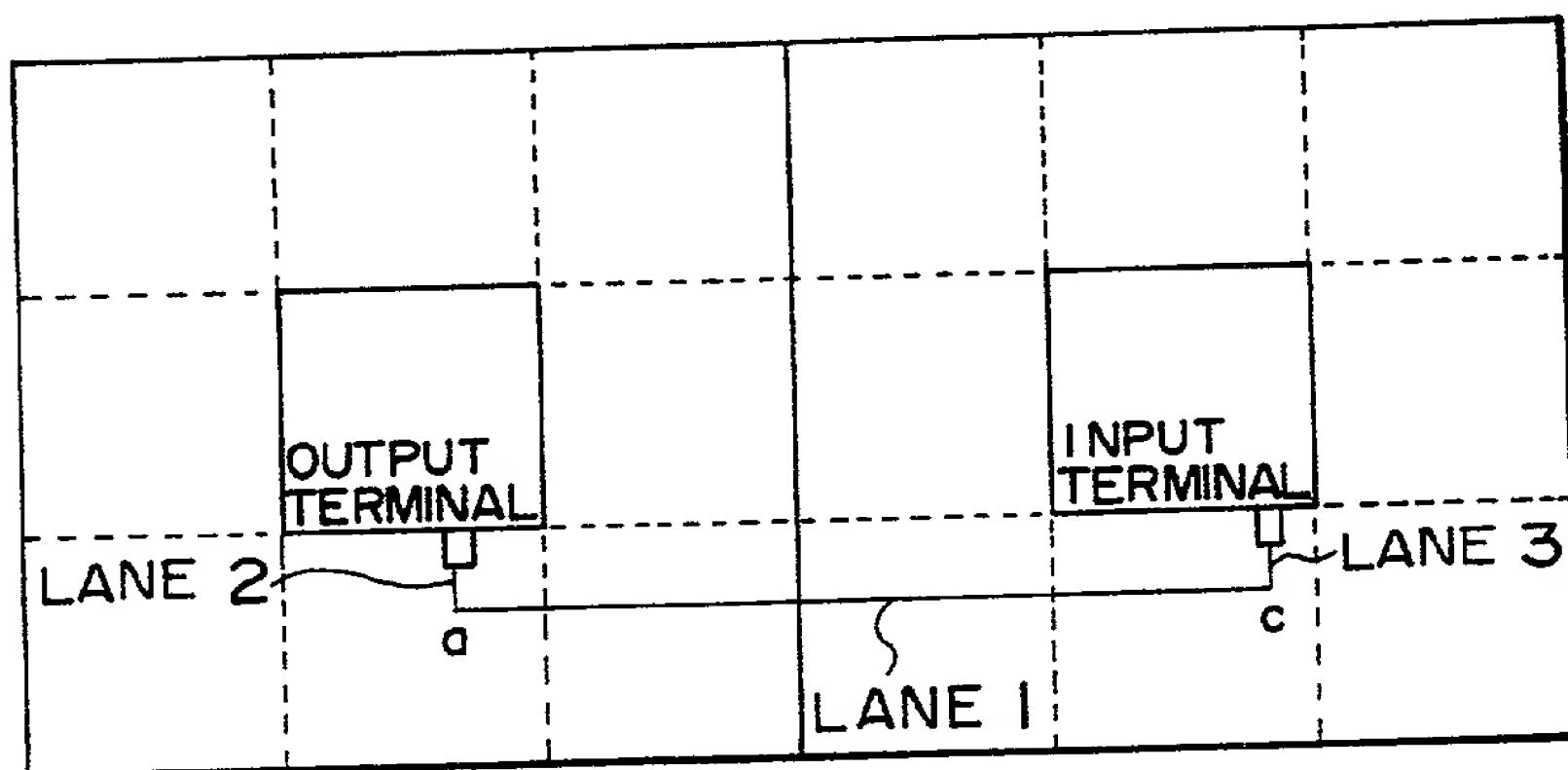


Fig. 60

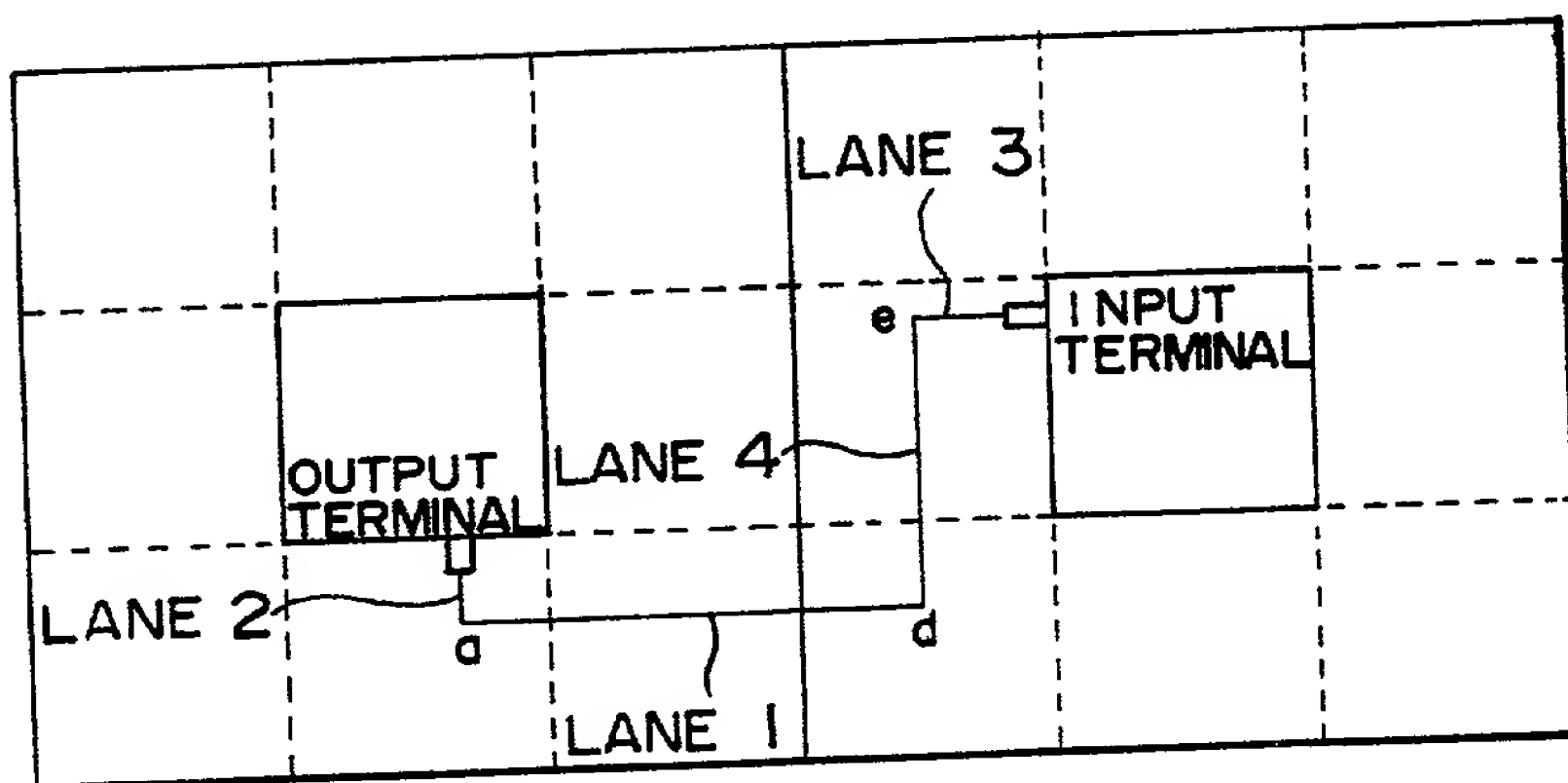


Fig. 61

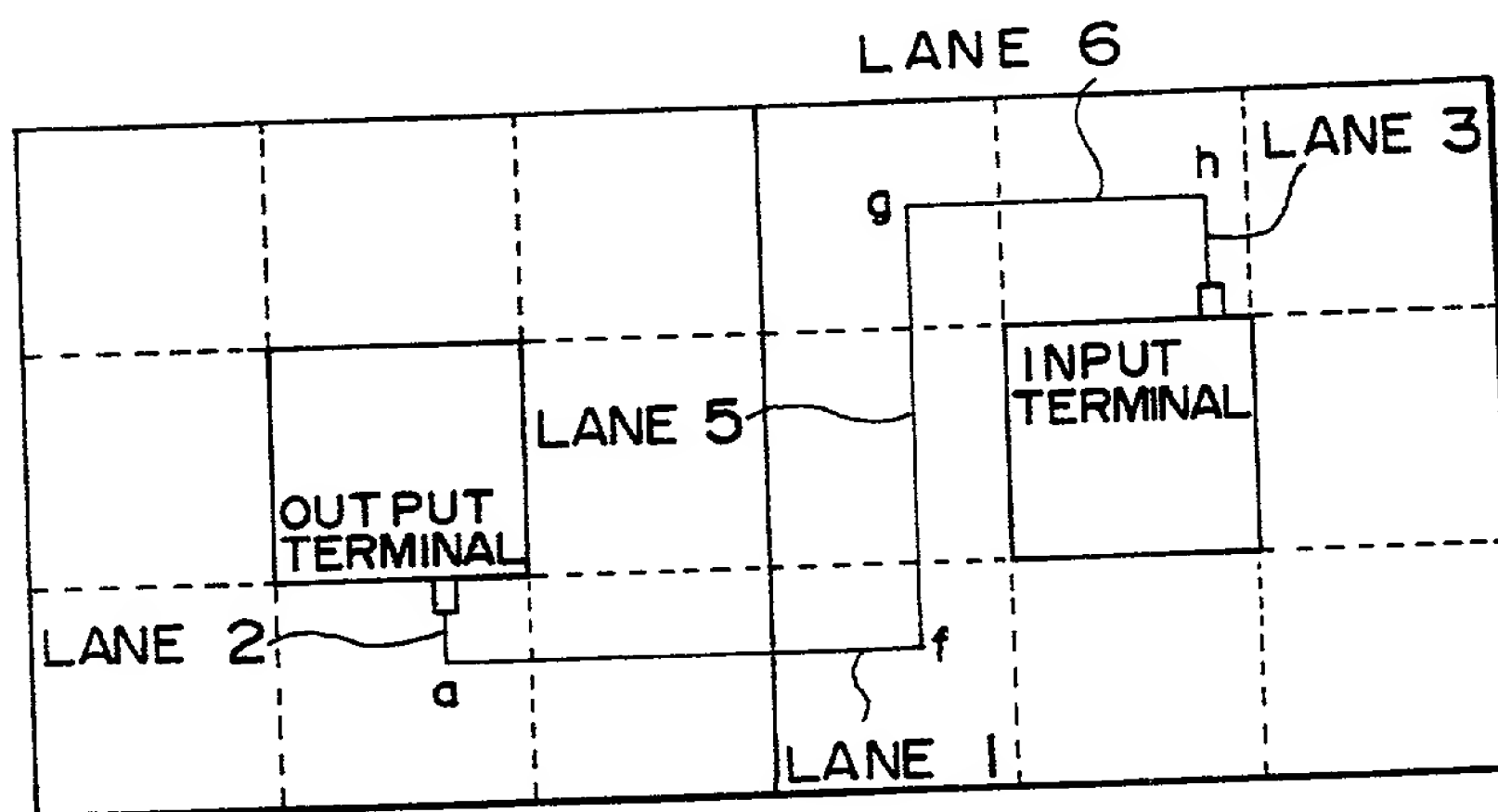


Fig. 62

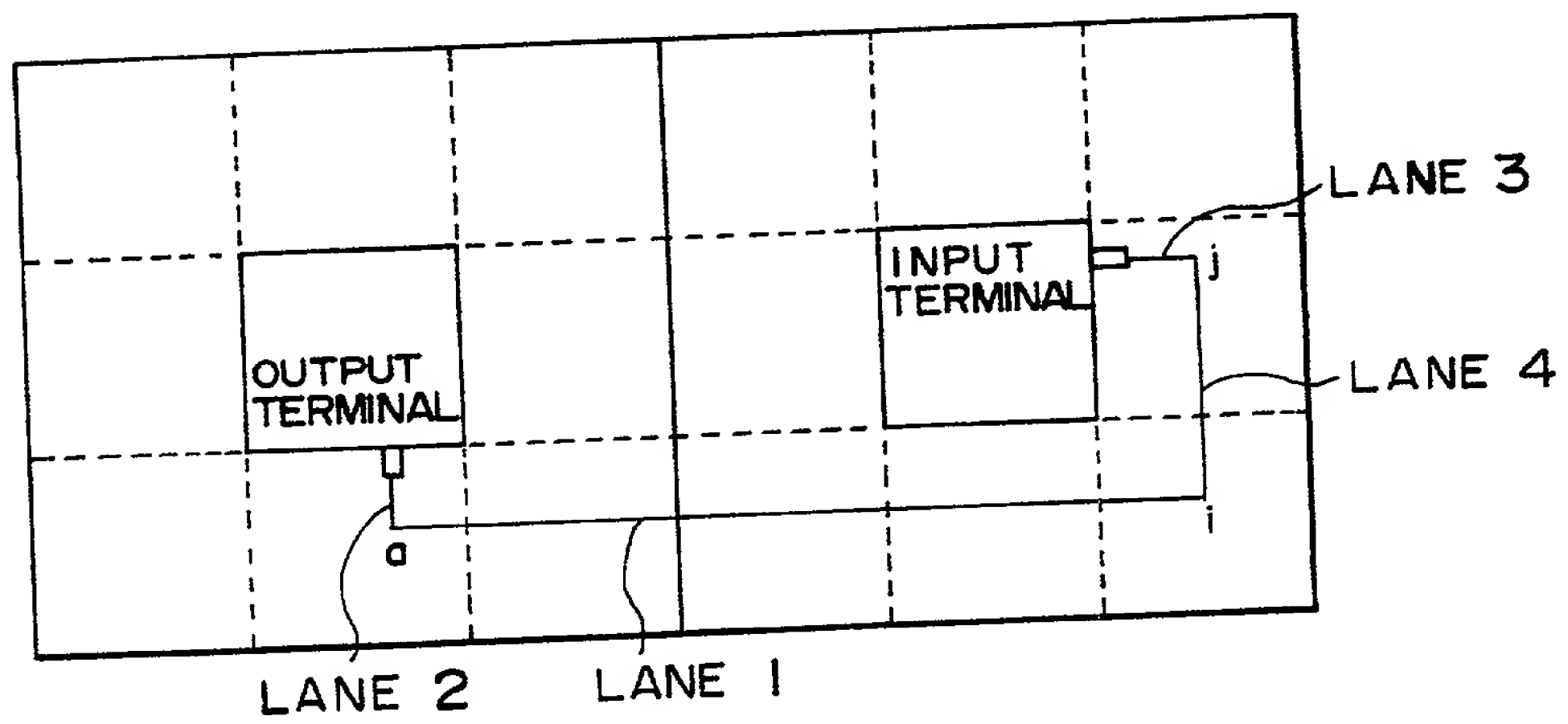


Fig. 63(C)

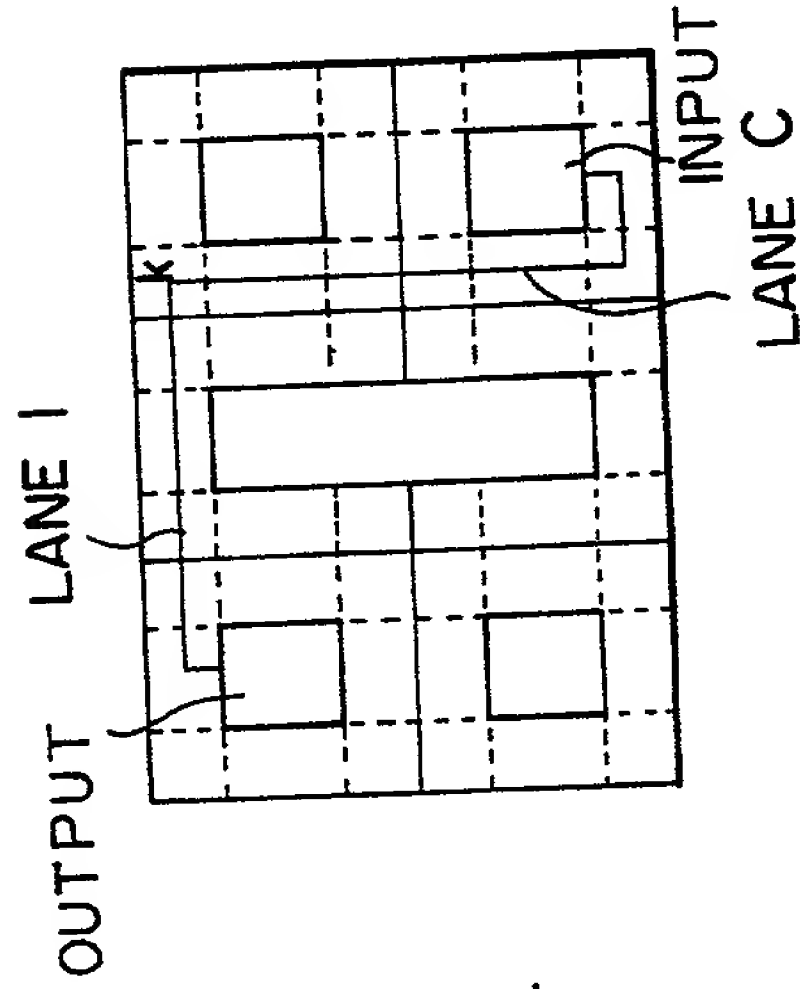


Fig. 63(D)

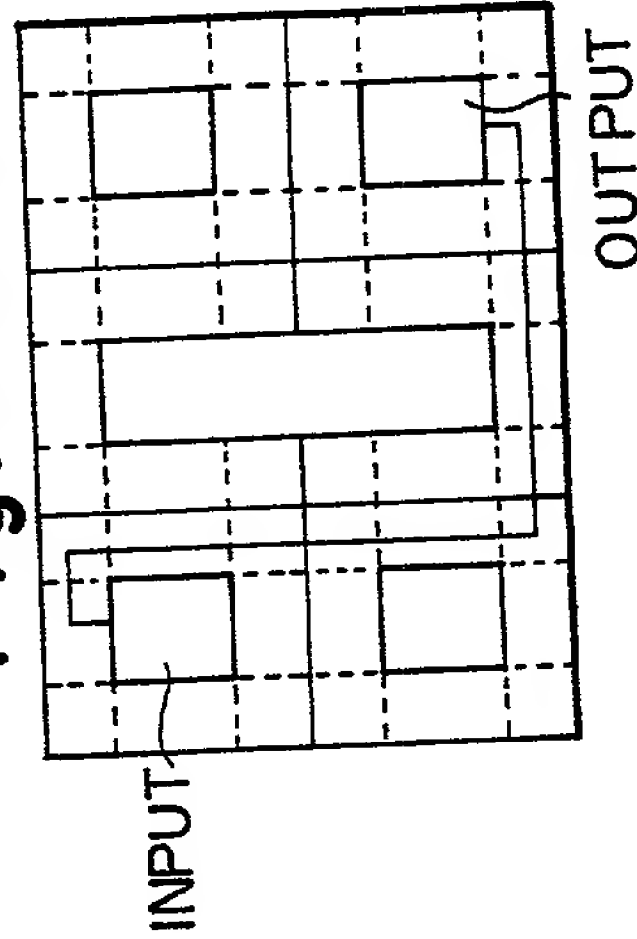


Fig. 63(A)

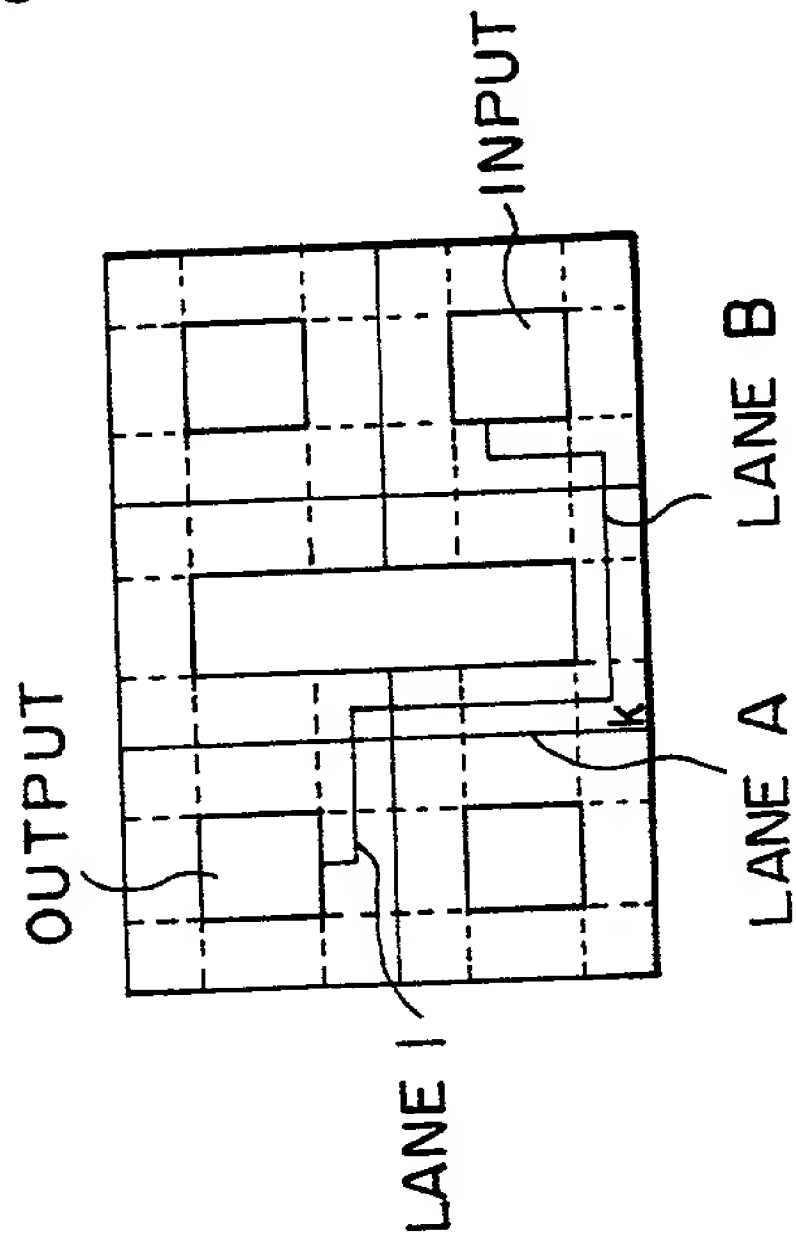


Fig. 63(B)

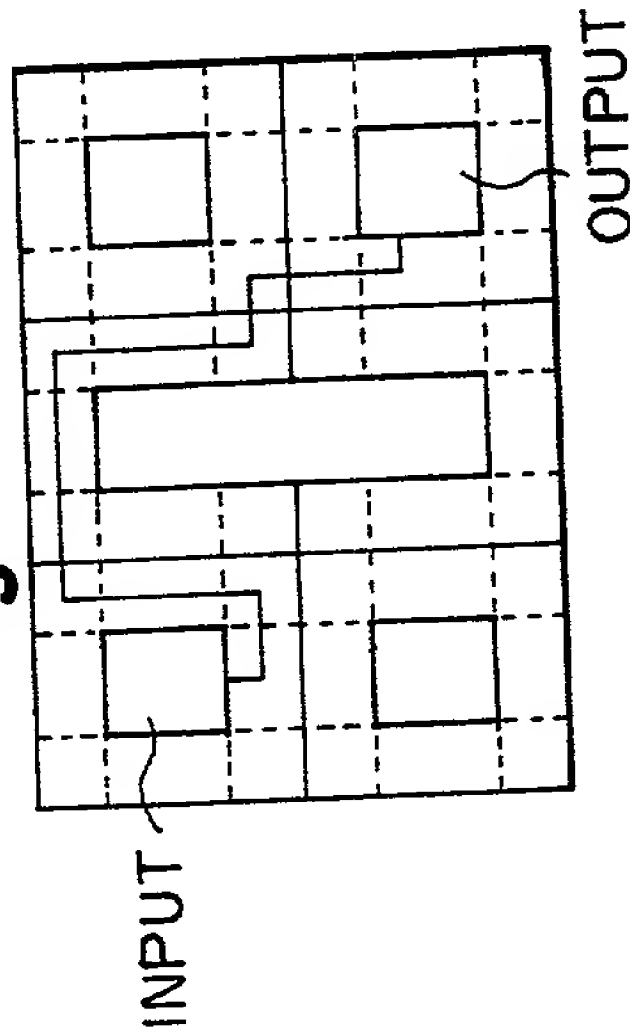


Fig. 64

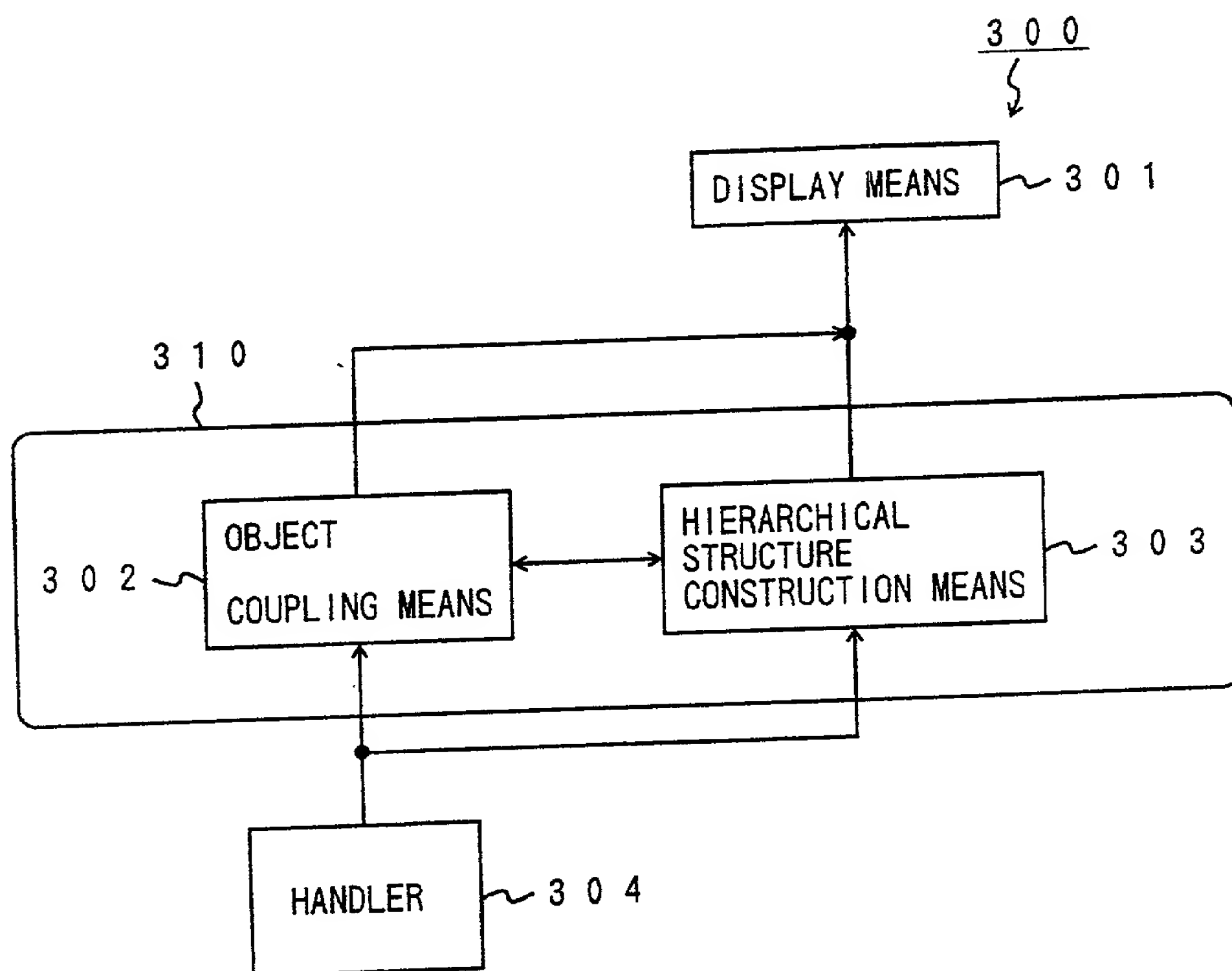


Fig. 65

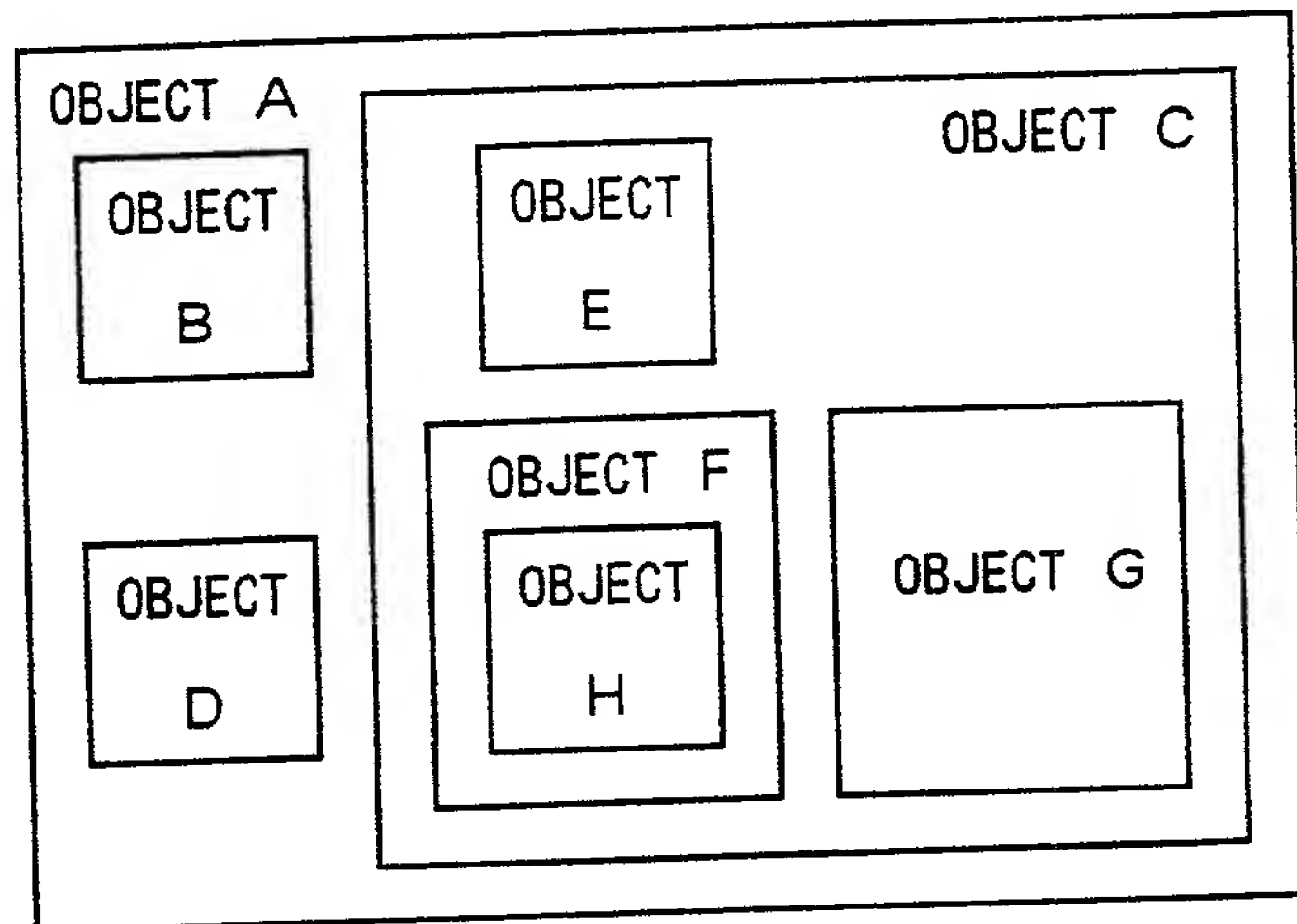


Fig. 66

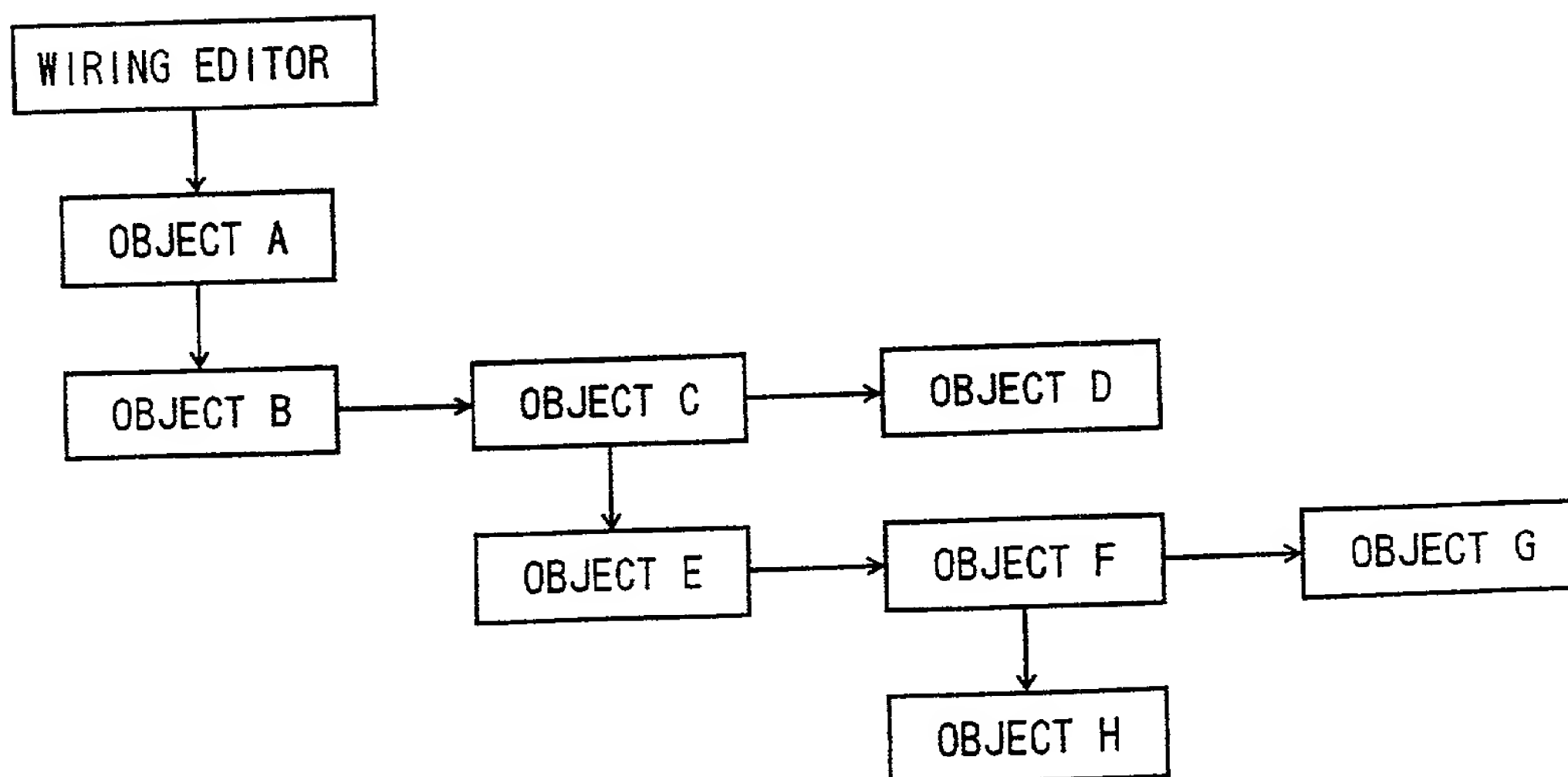


Fig. 67

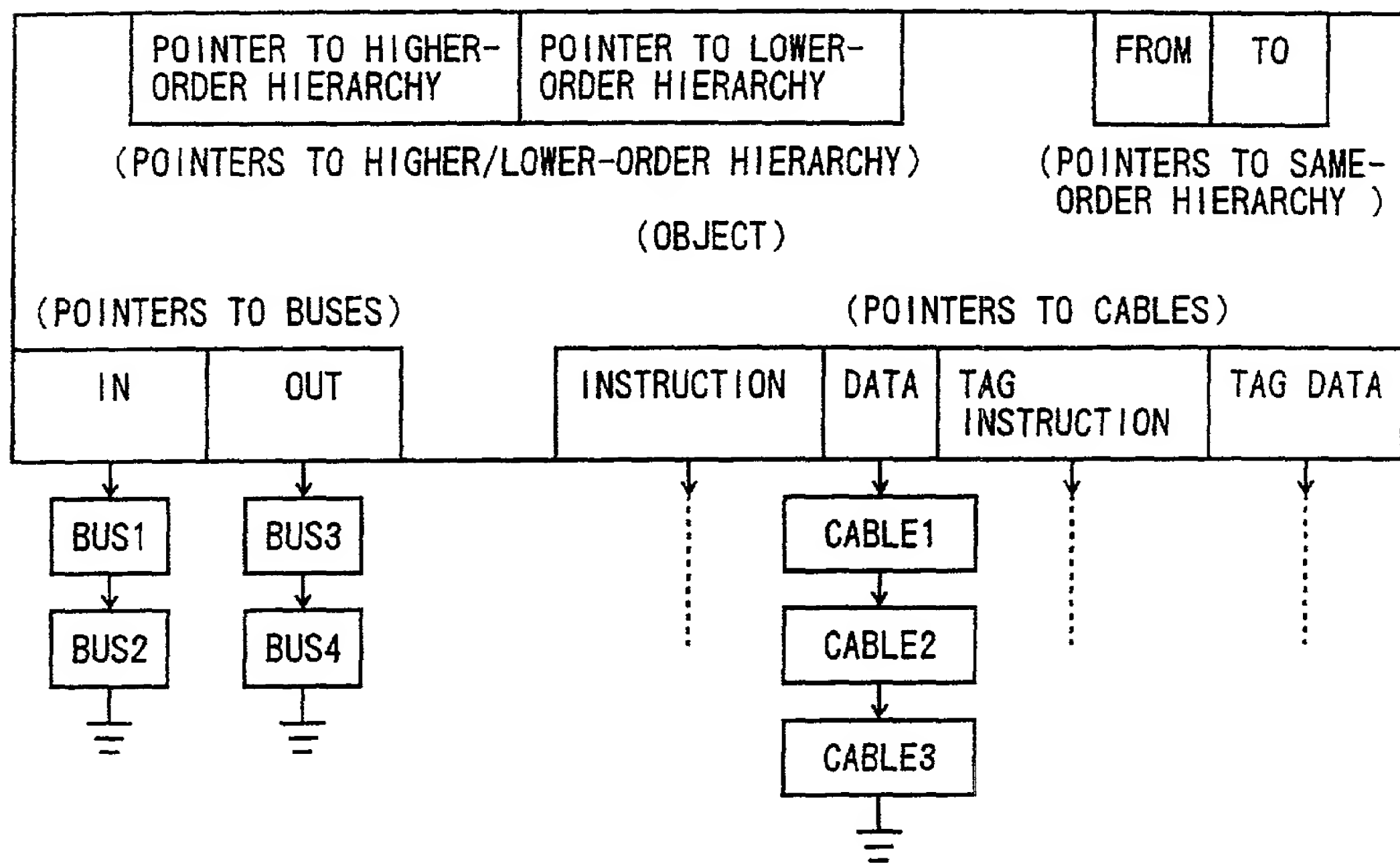


Fig. 68

(BUS)

POINTER TO SUBSTANTIAL OBJECT
POINTER TO BUS OF SUBSTANTIAL OBJECT
POINTER TO NEXT BUS
OTHER DATA

Fig. 69

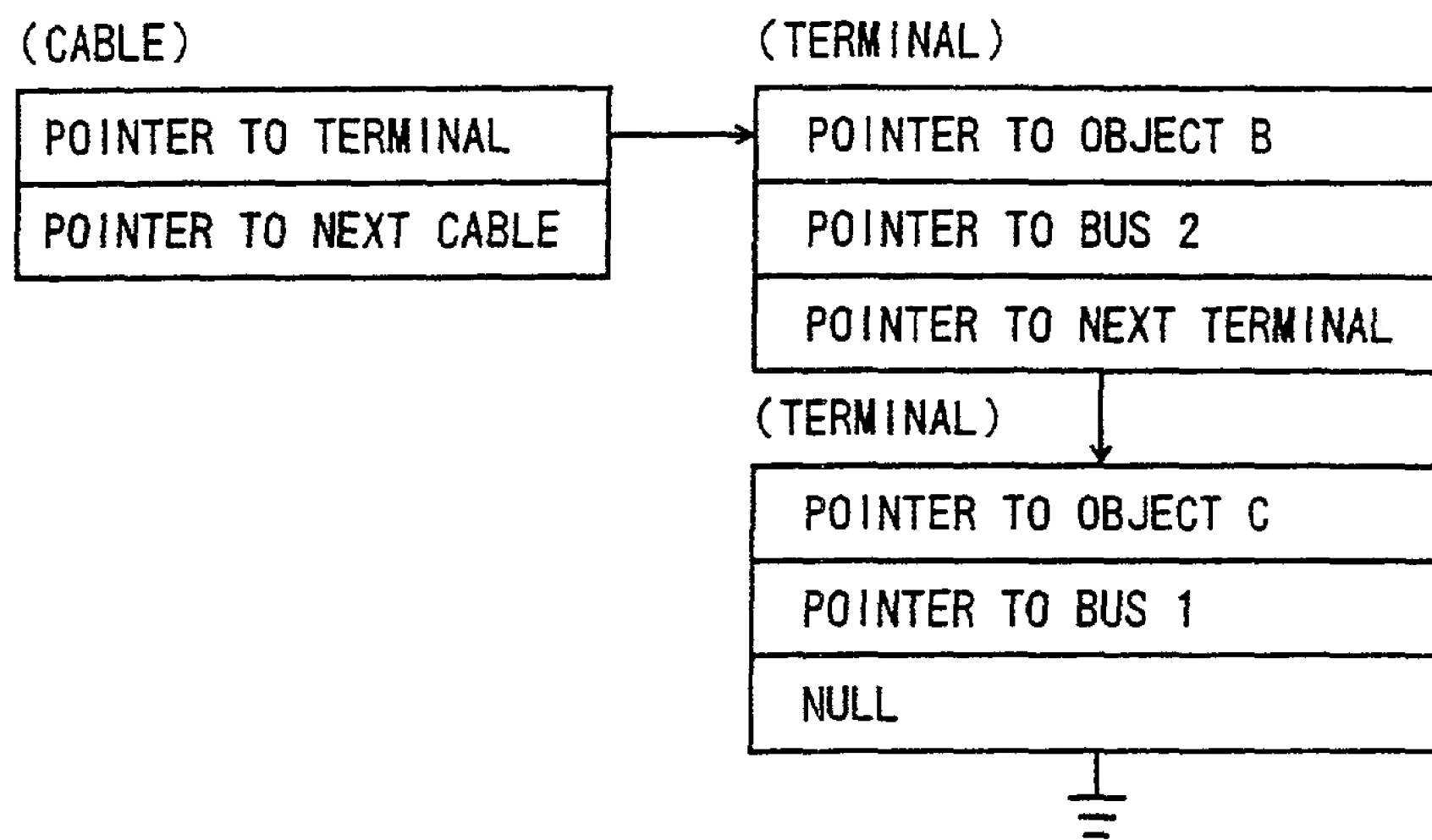


Fig. 70

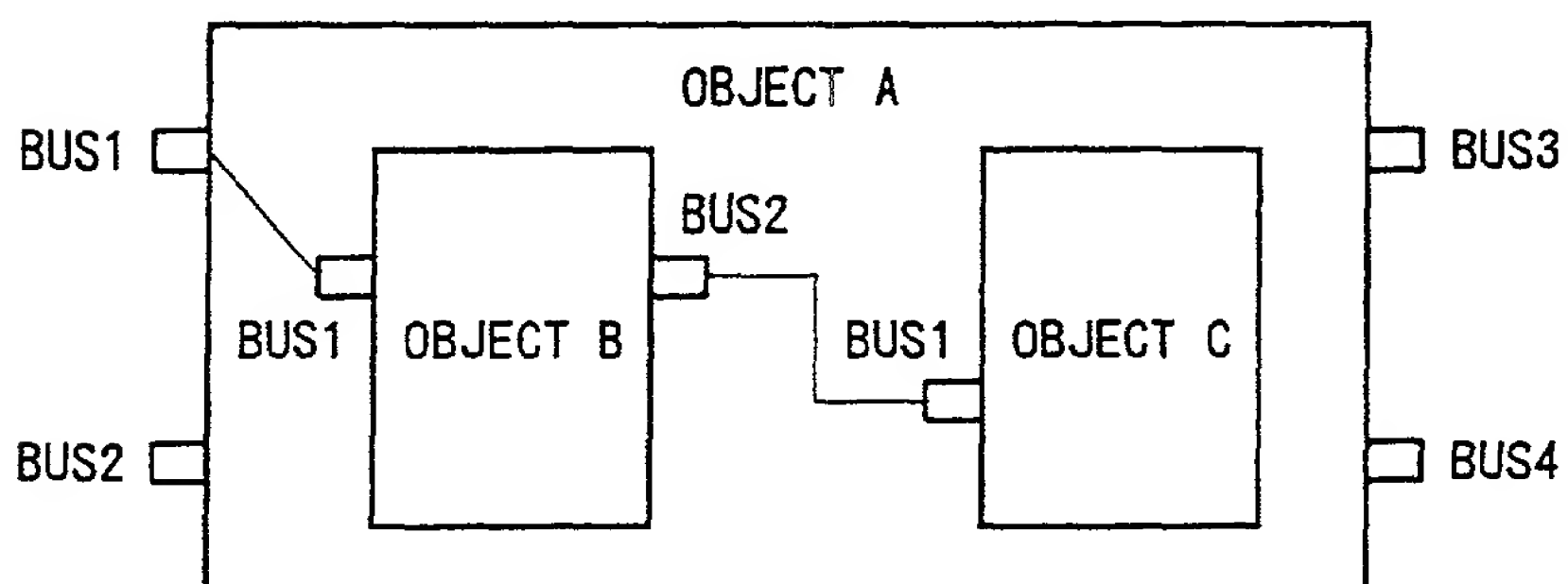


Fig. 71

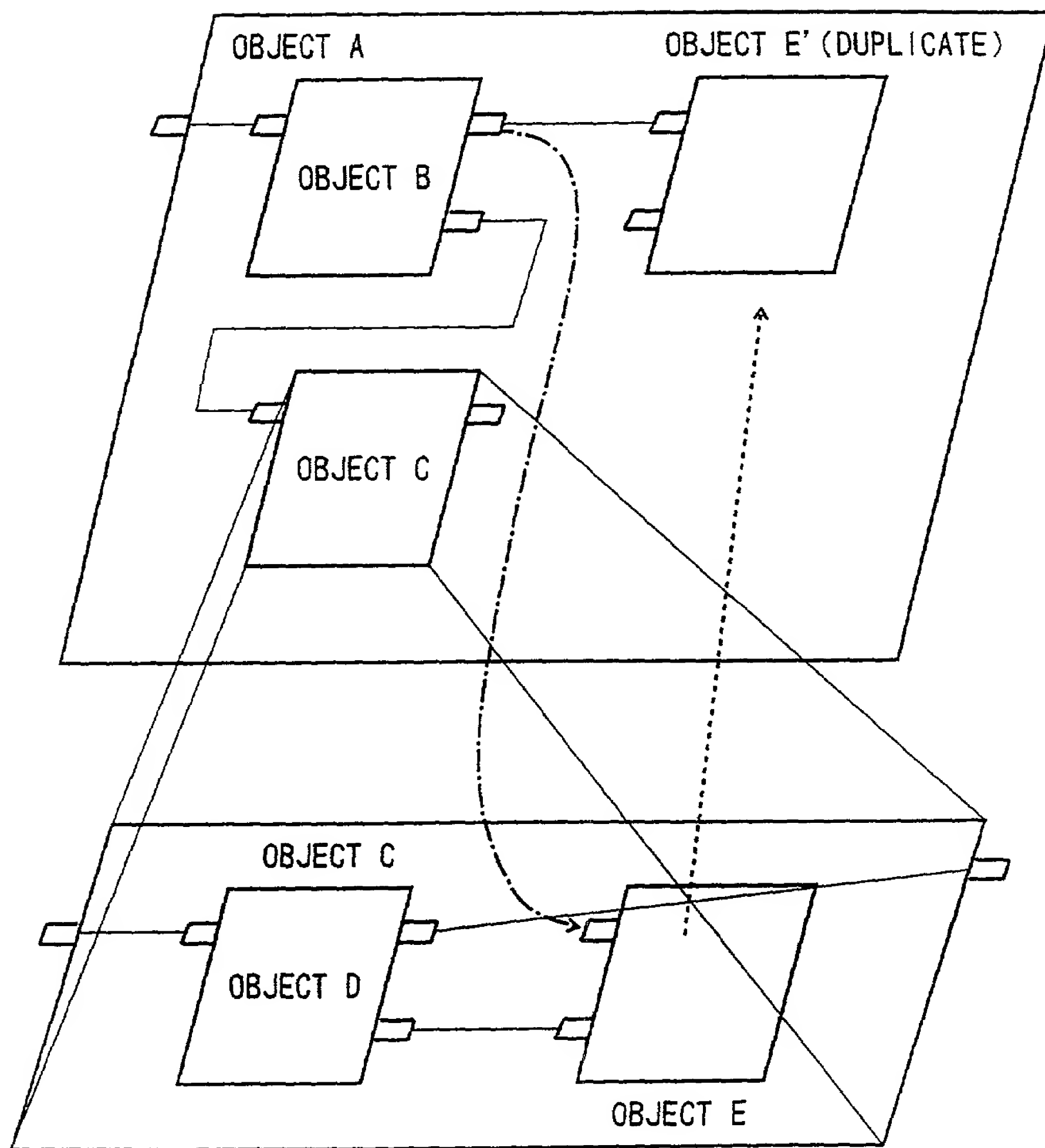


FIG. 71

Fig. 72

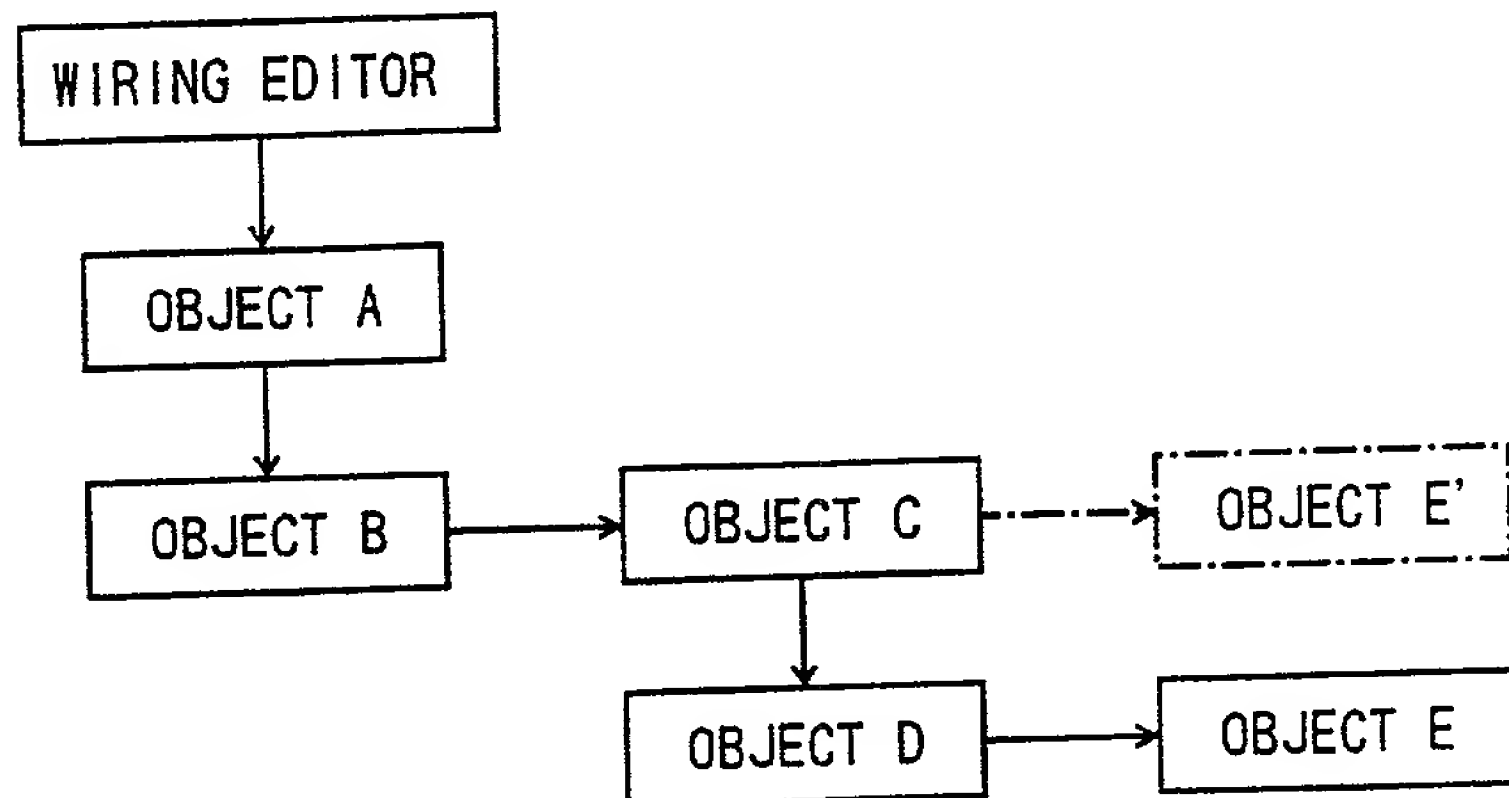


Fig. 73

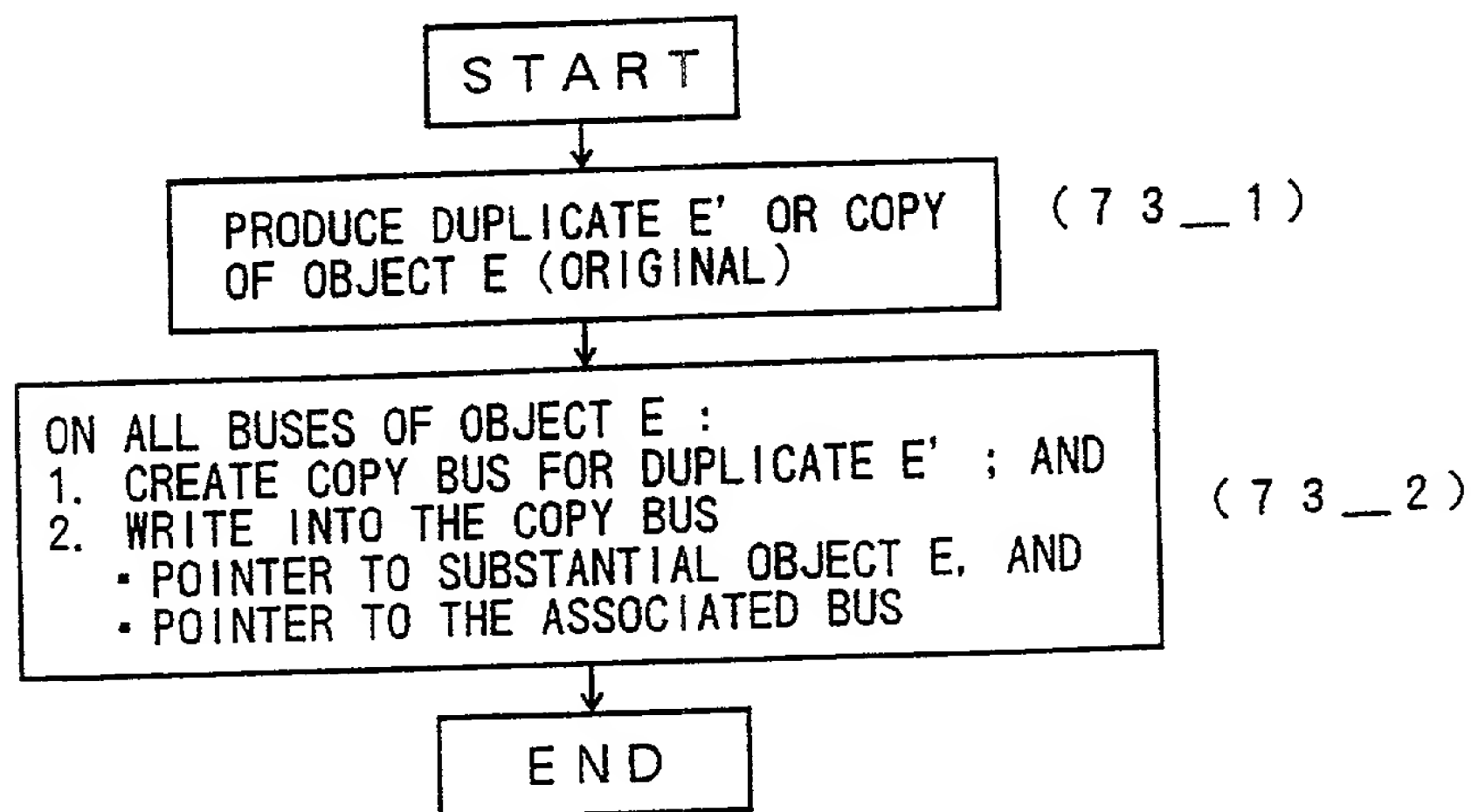


Fig. 74

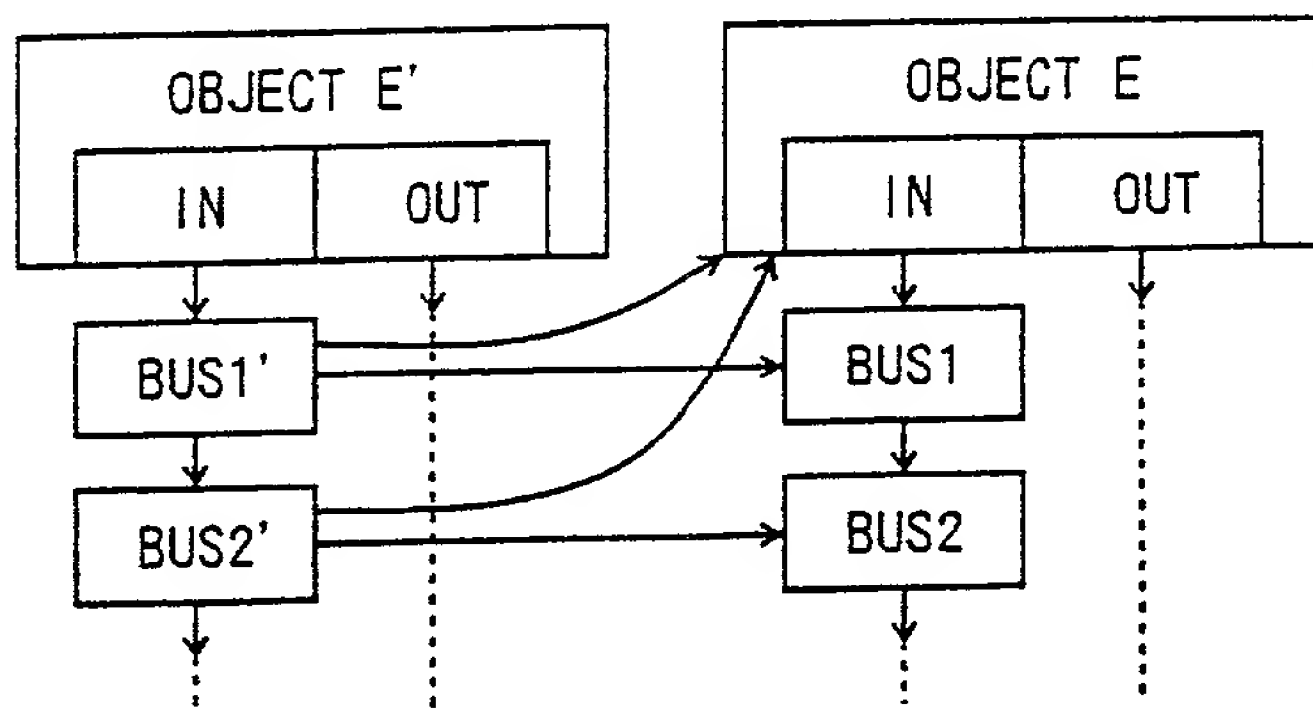


Fig. 75

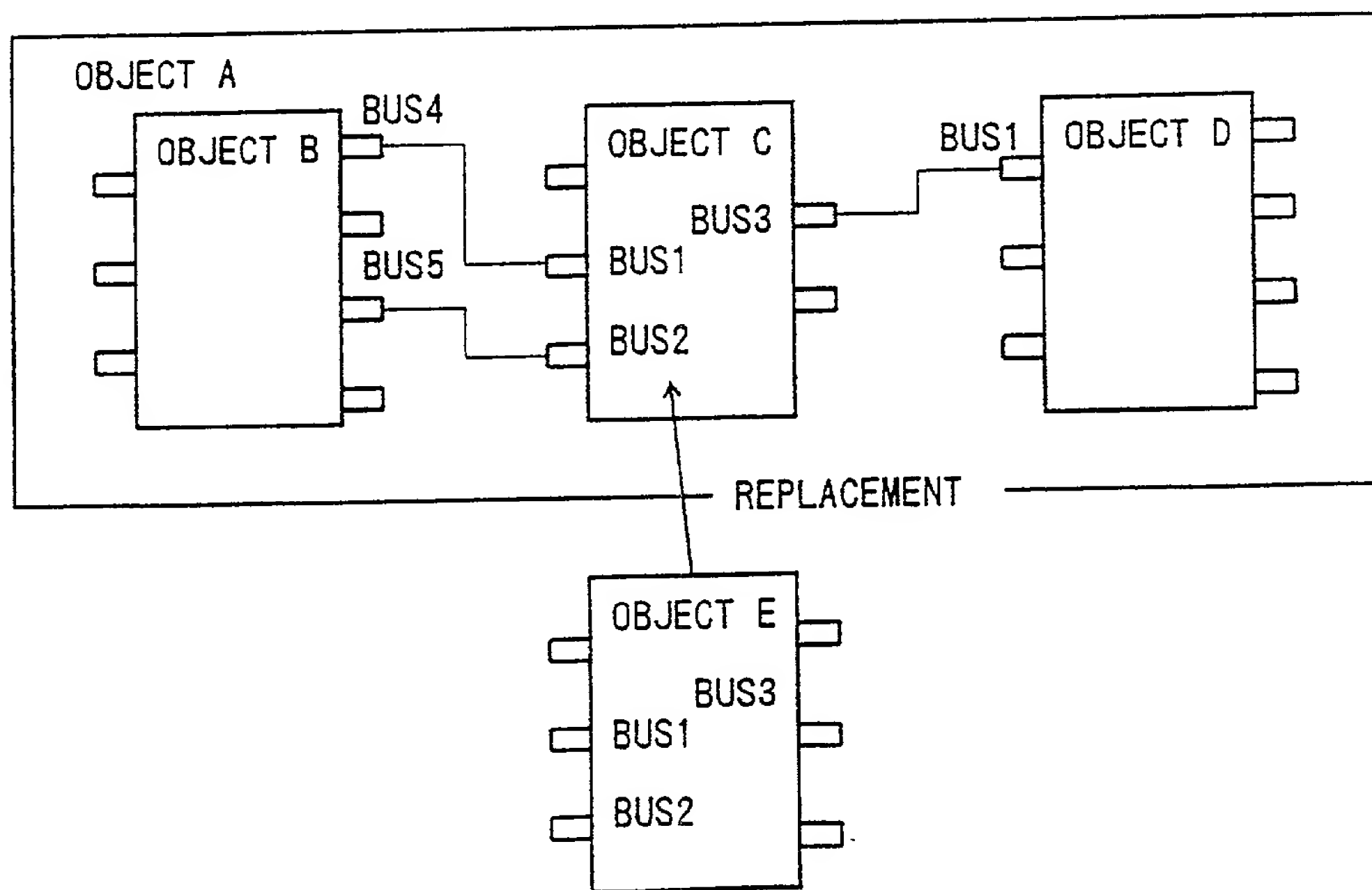


Fig. 76

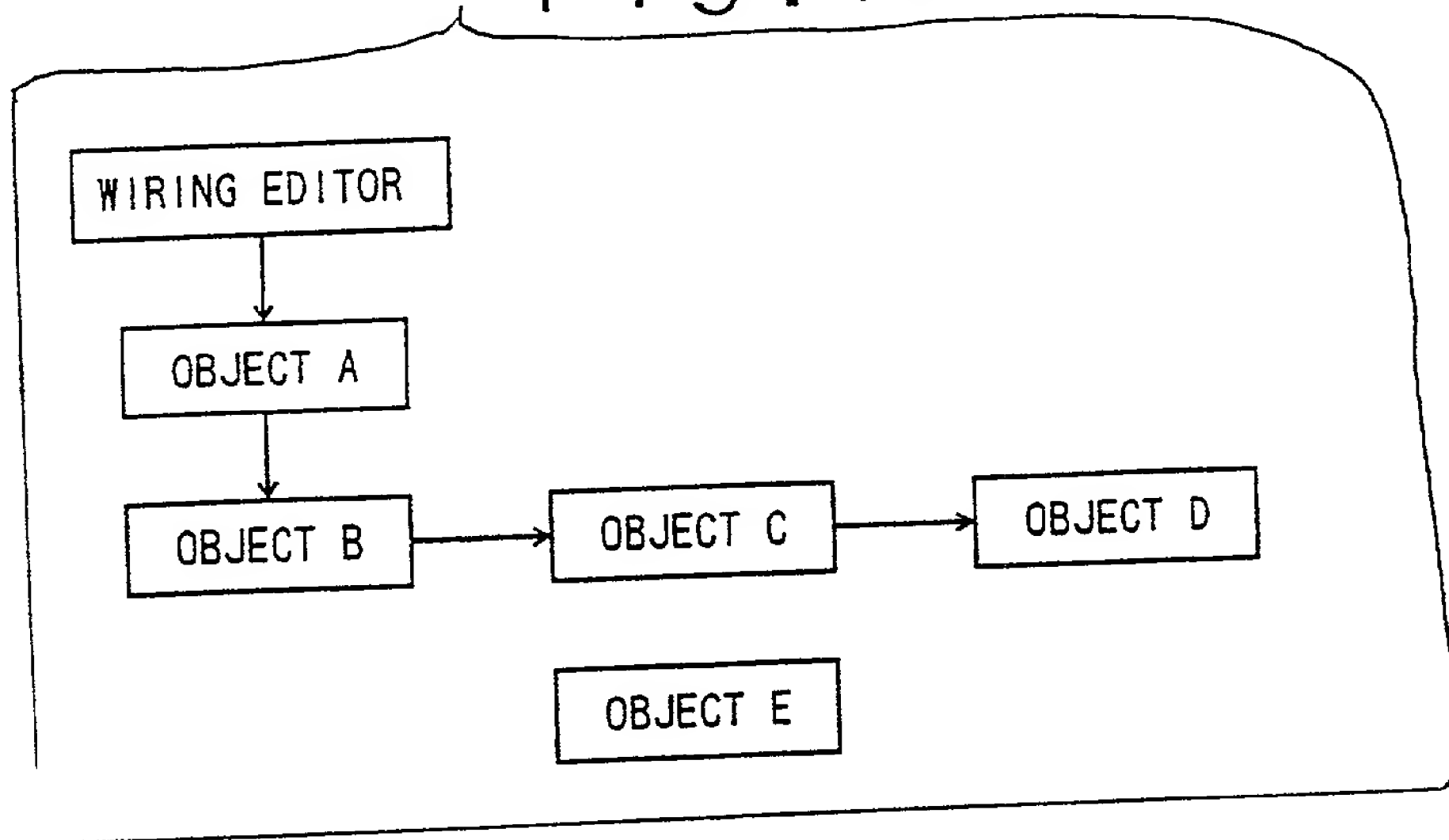


Fig. 77

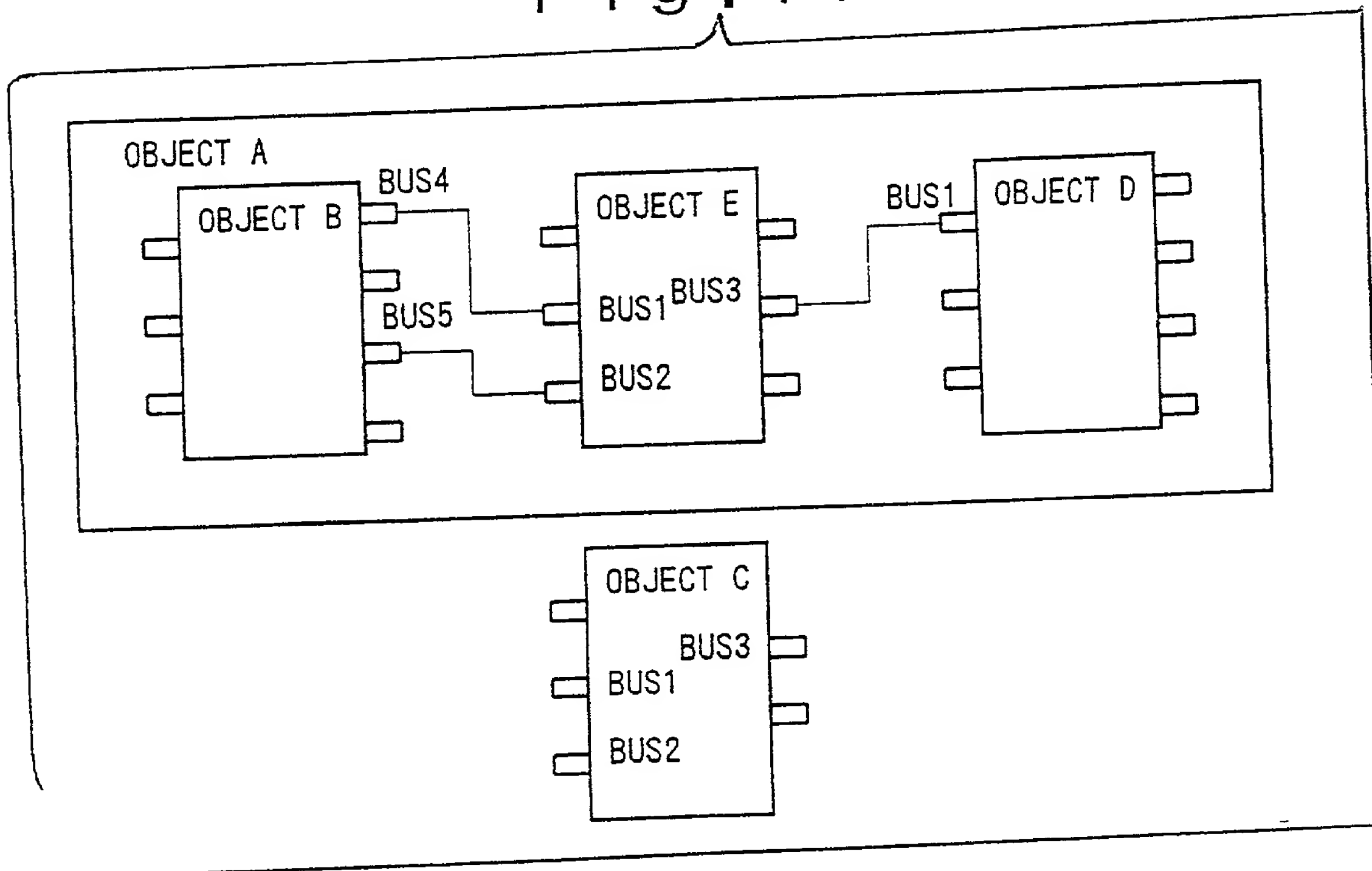


Fig. 78

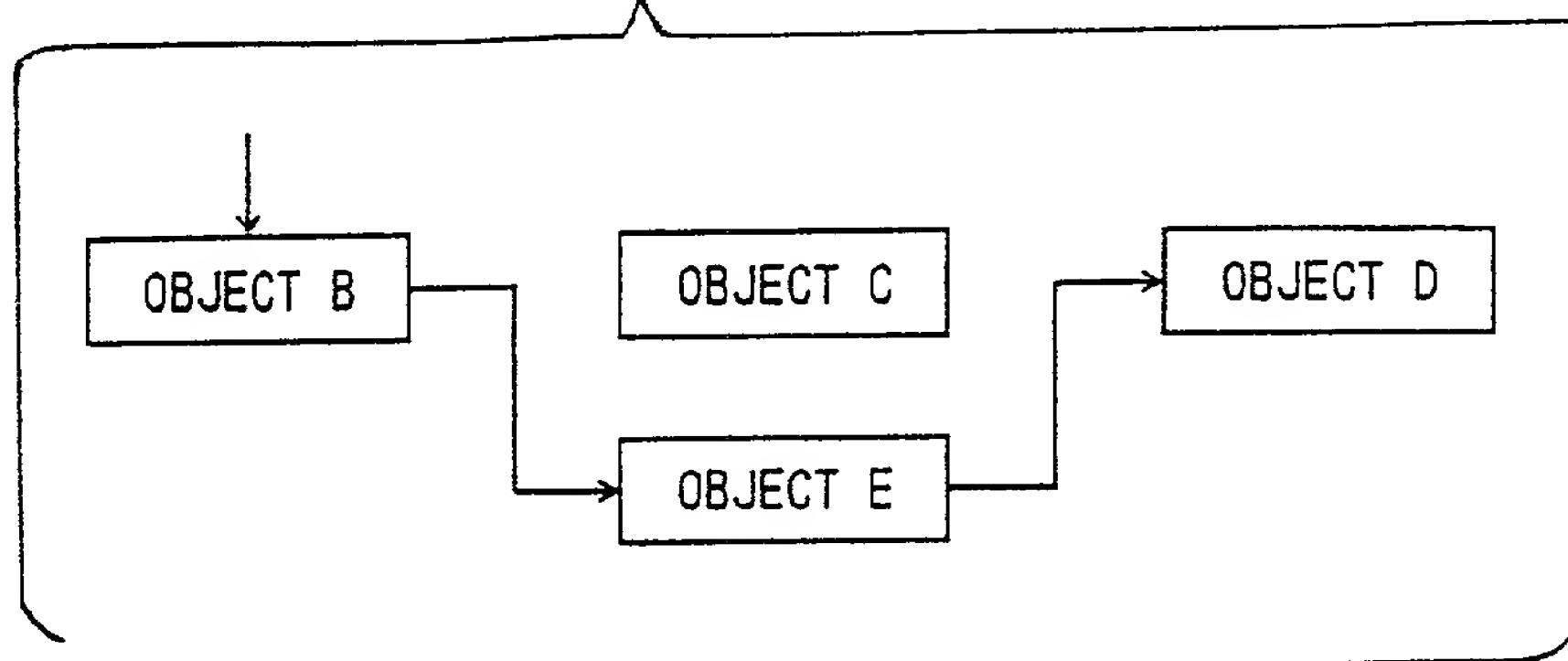


Fig. 79

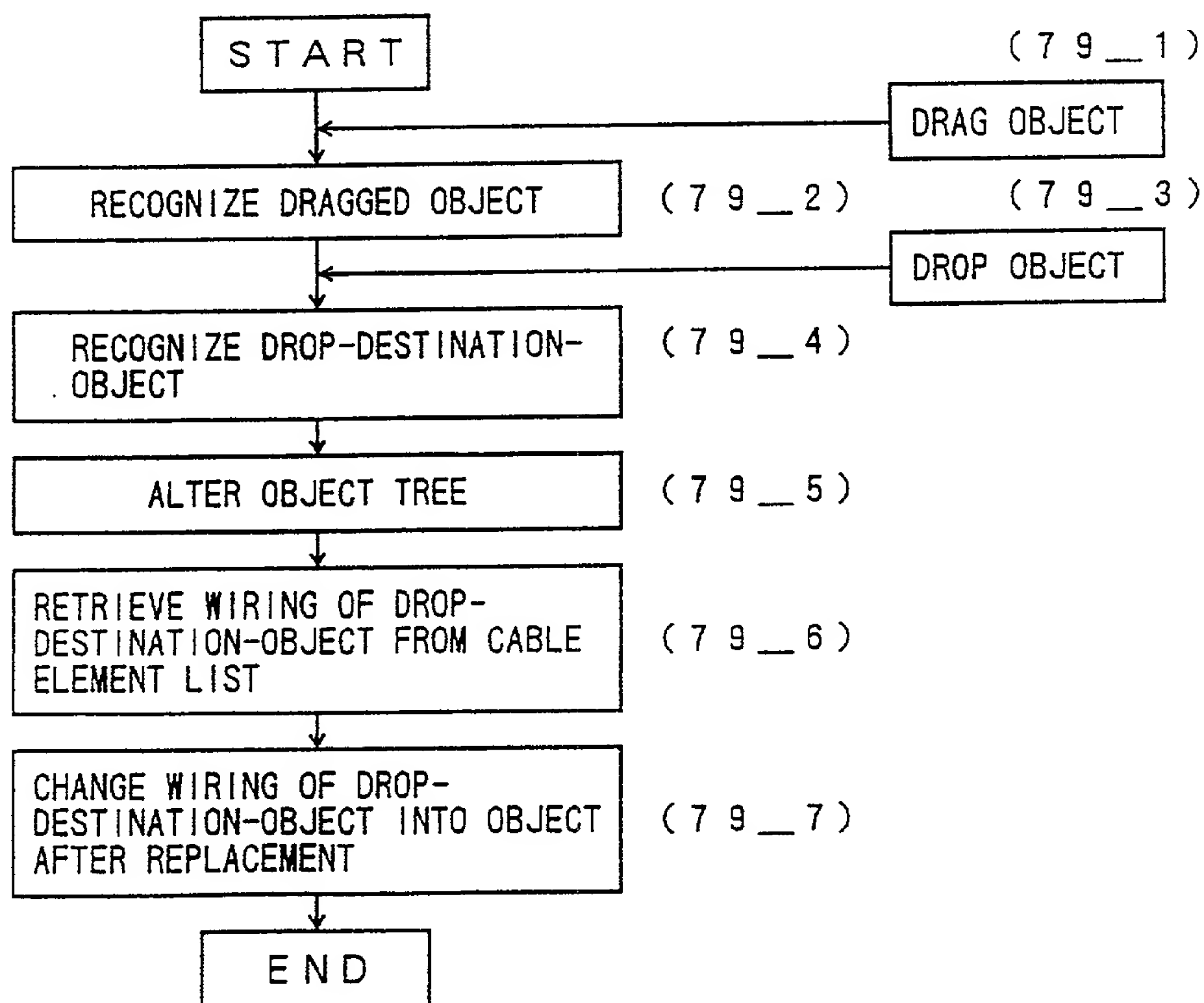


Fig. 80

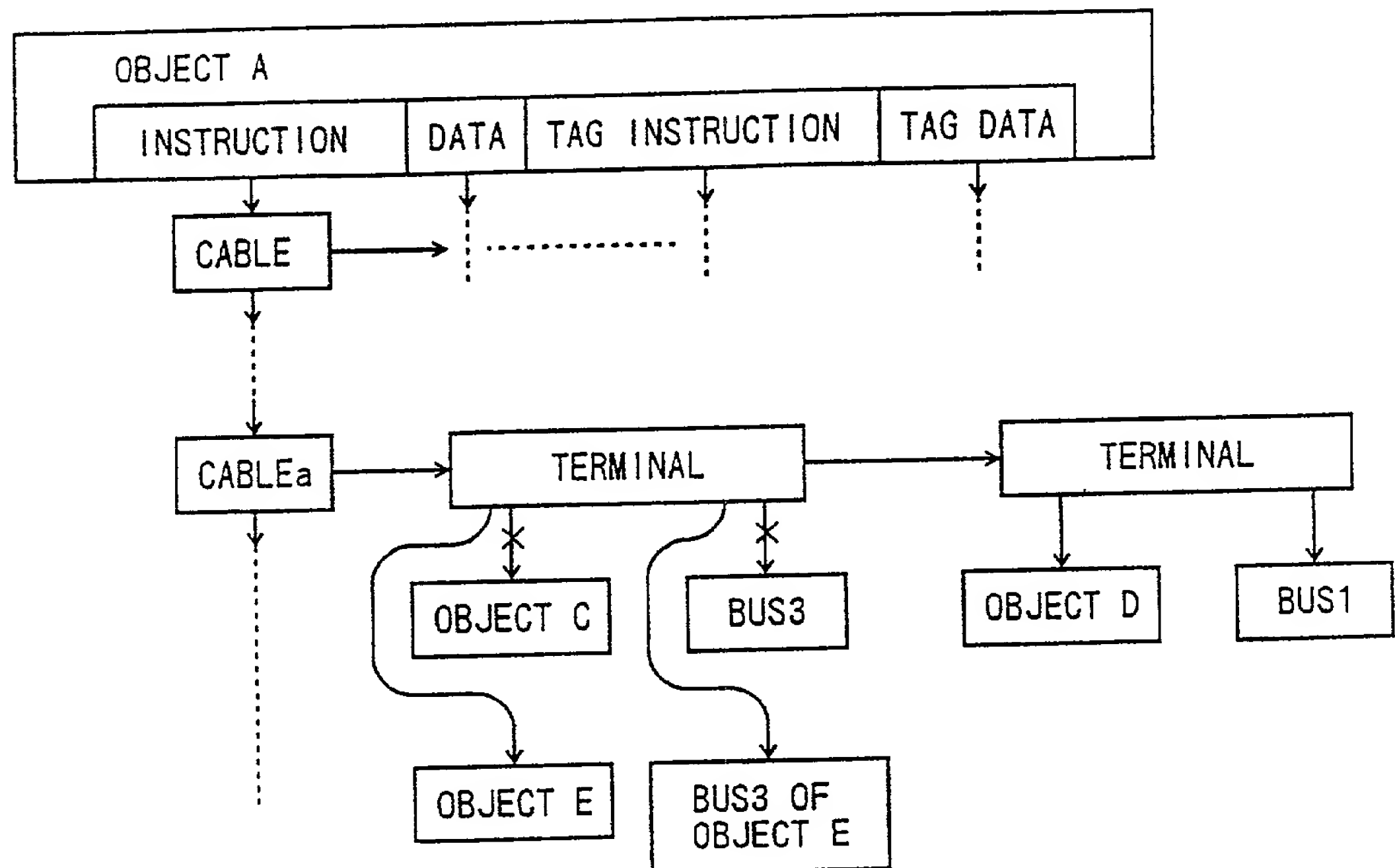
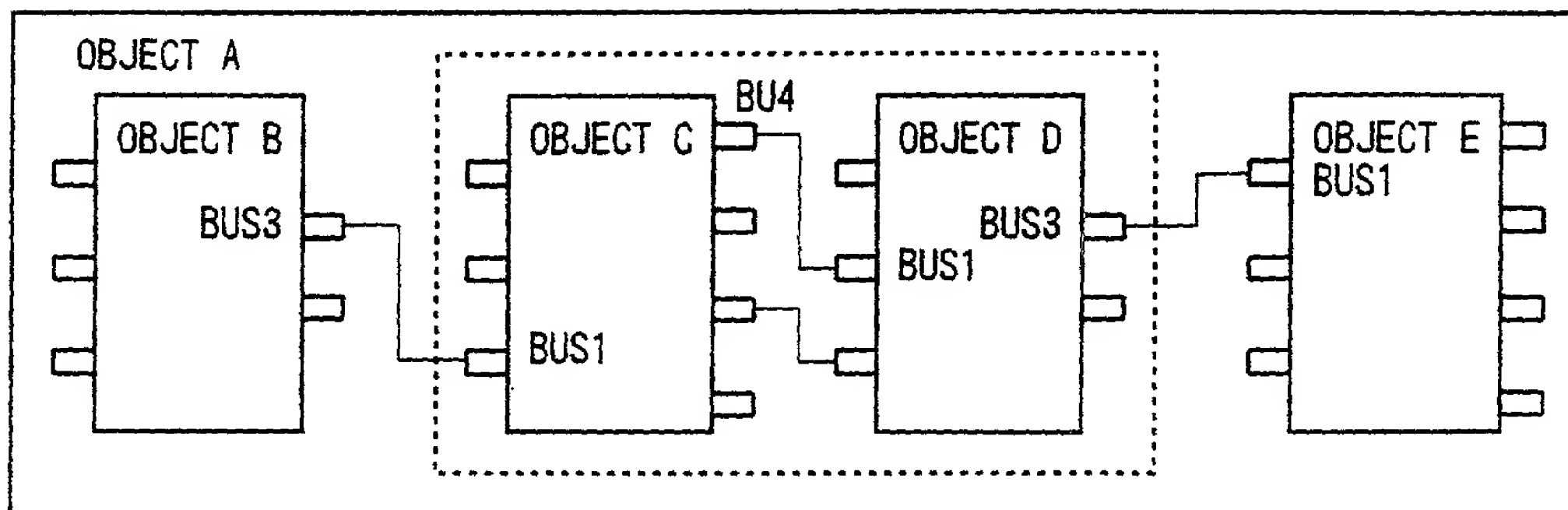


Fig. 81



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Fig. 82

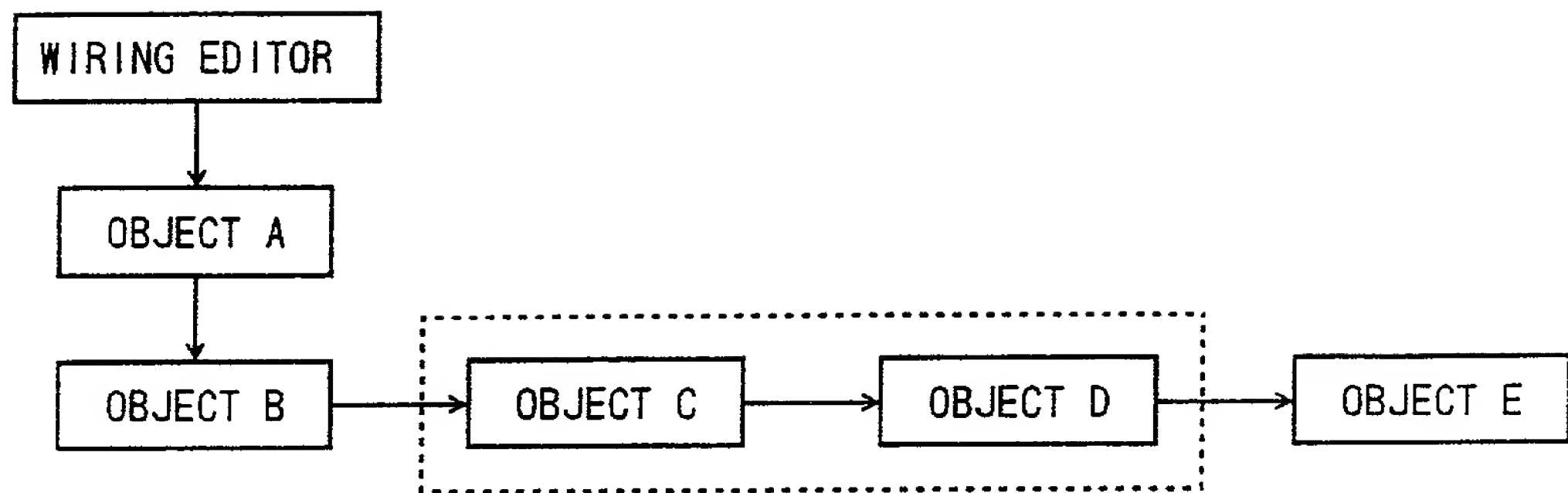


Fig. 83

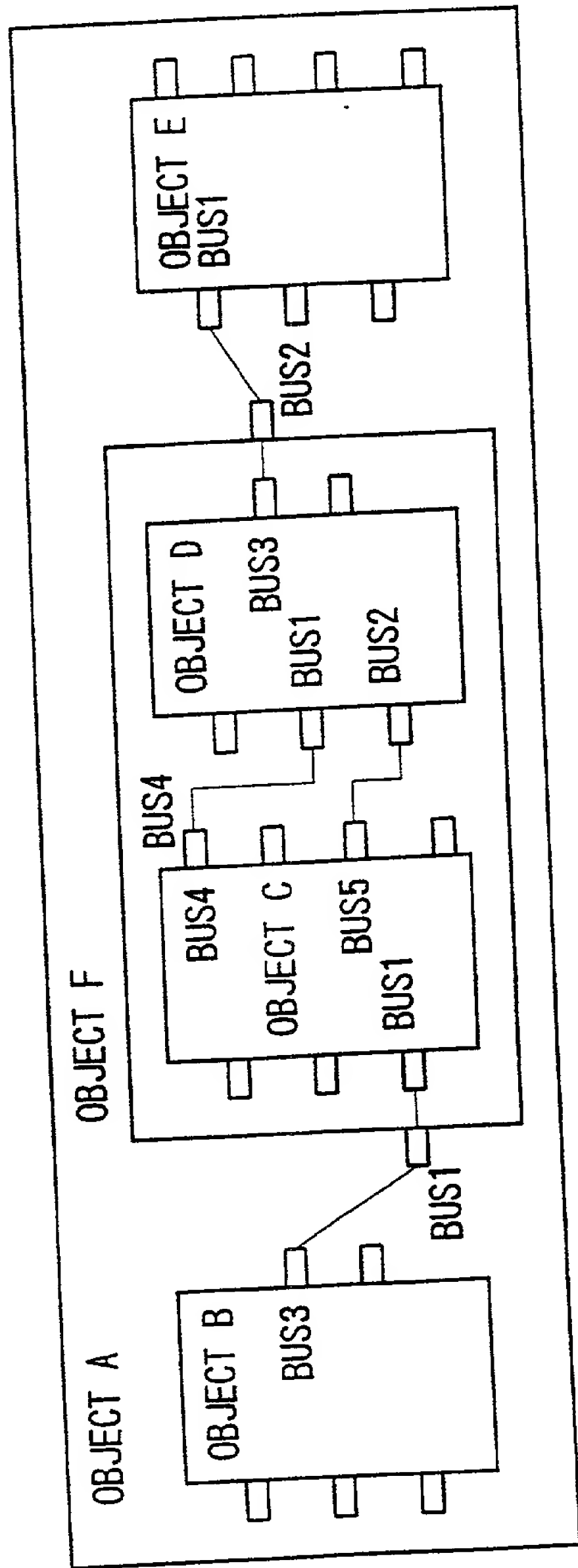


Fig. 84

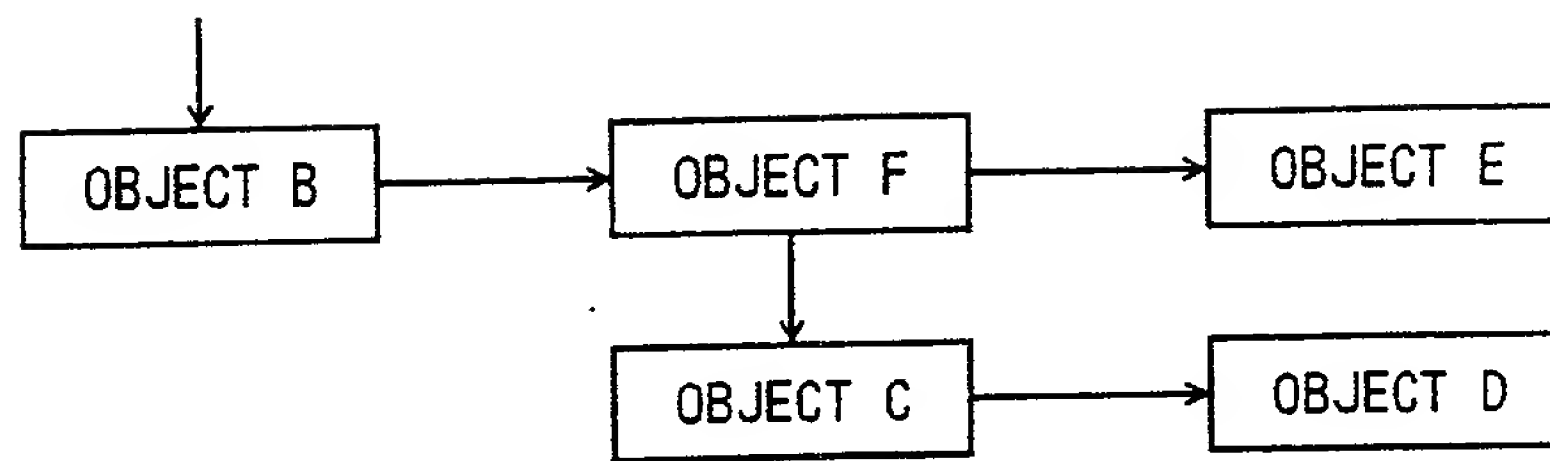
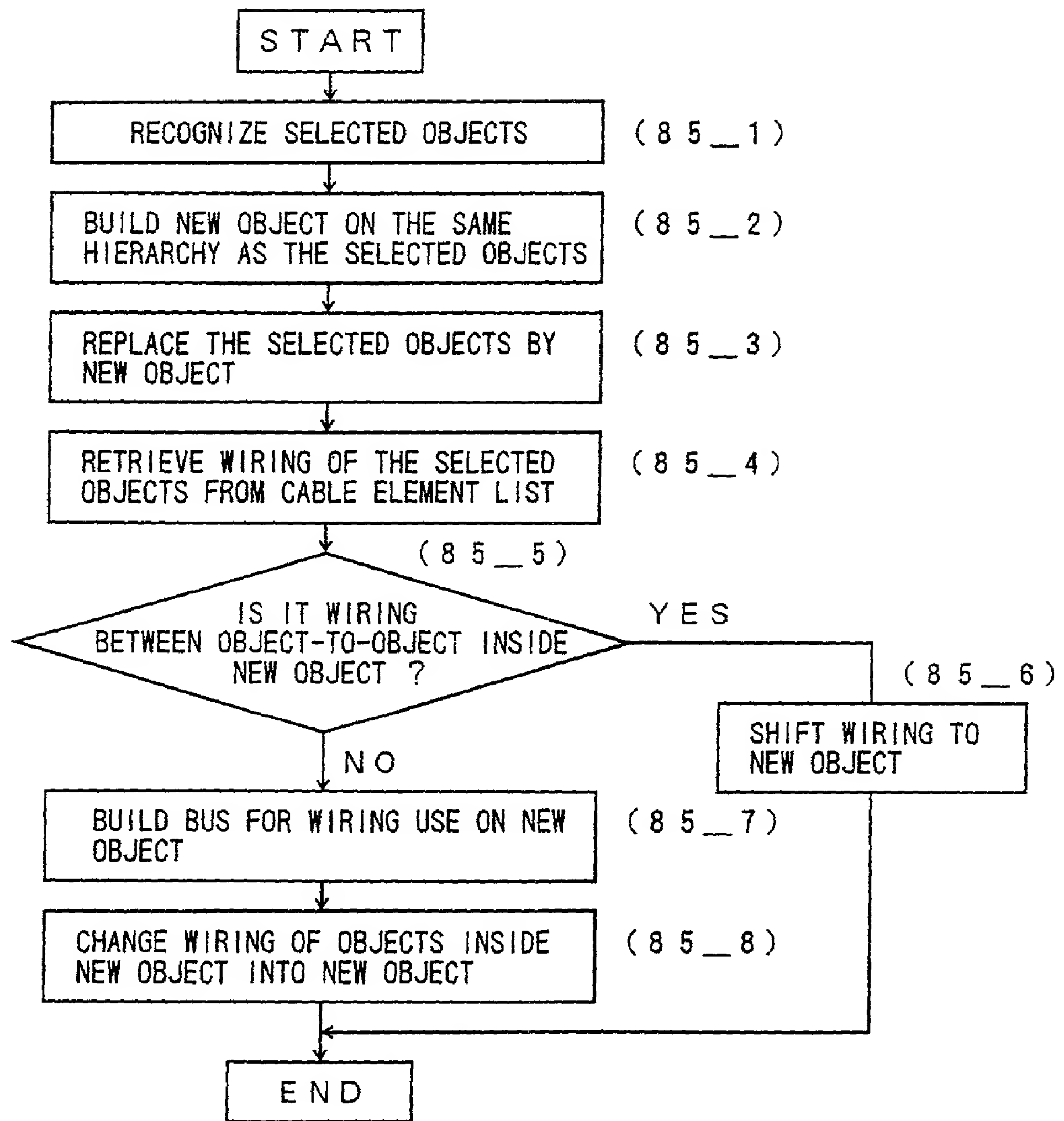


Fig. 85



TOP SECRET

Fig. 86

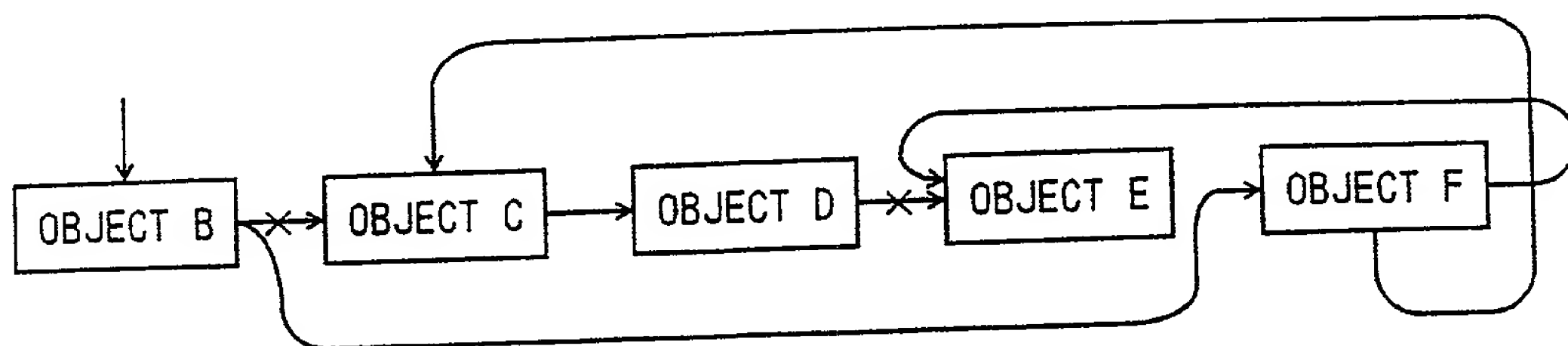


Fig. 87

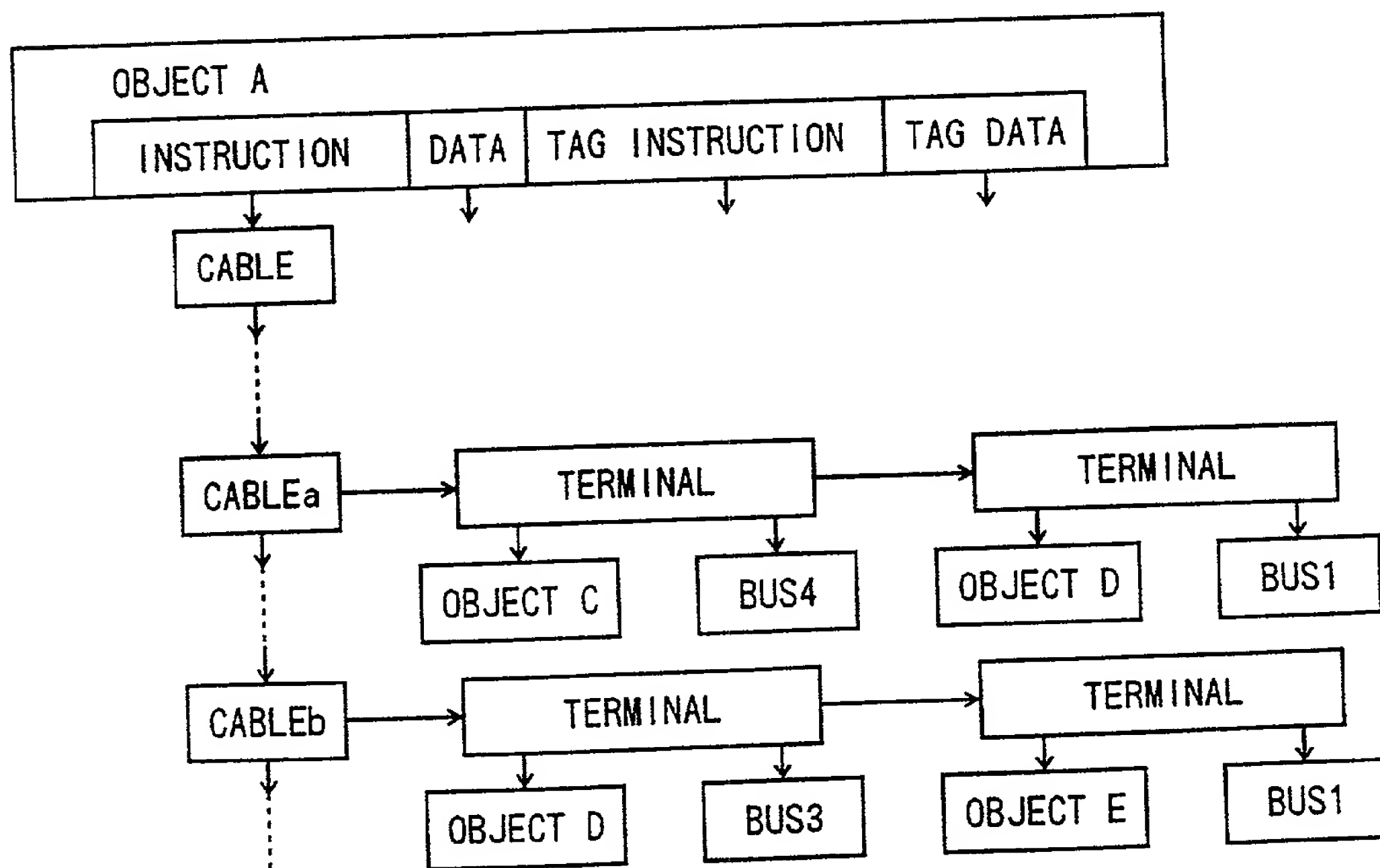


Fig. 88

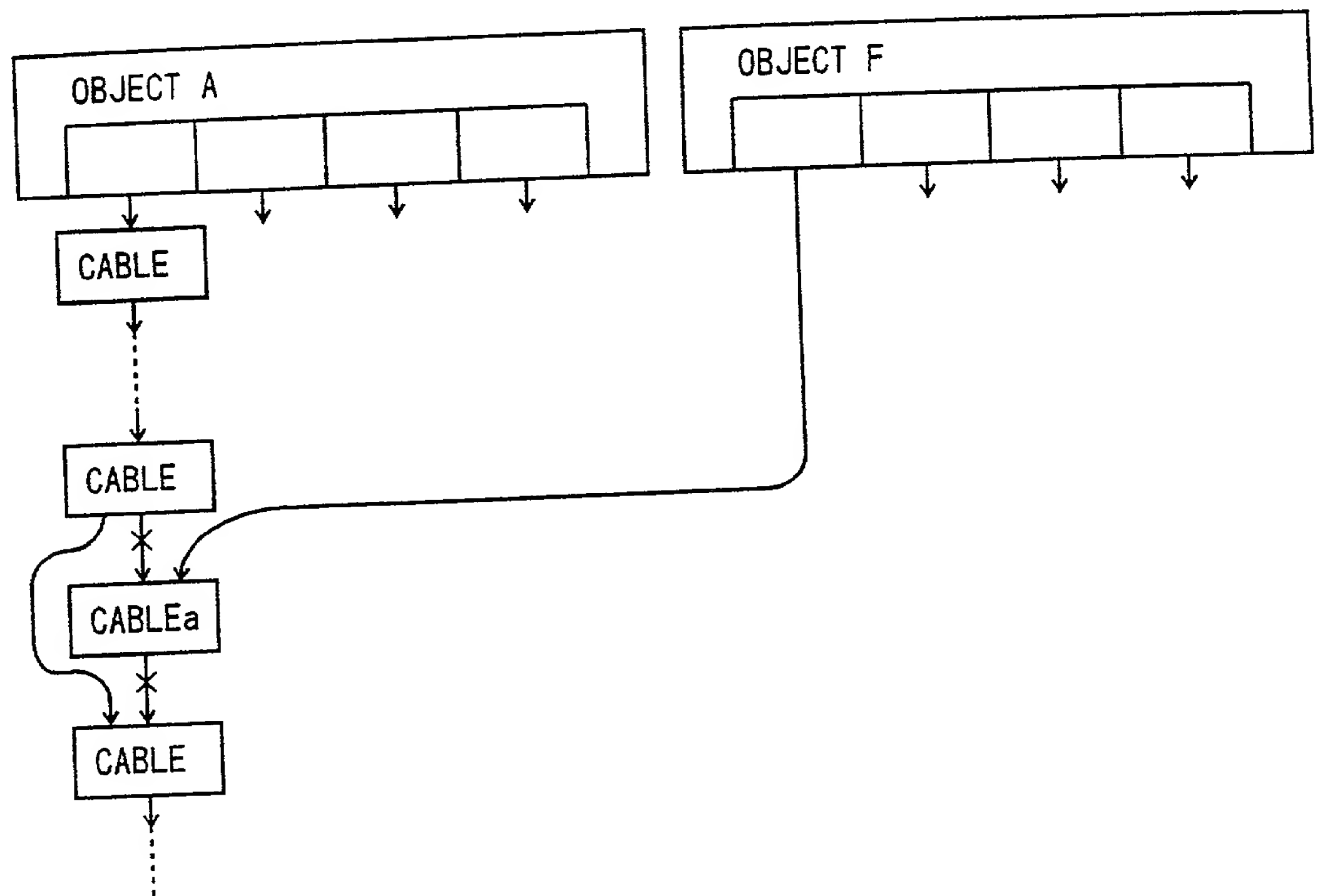


Fig. 89

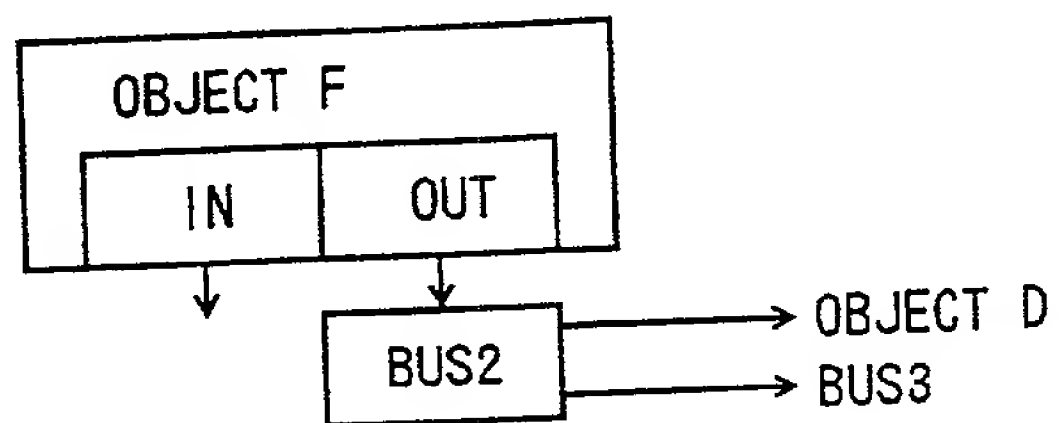


Fig. 90

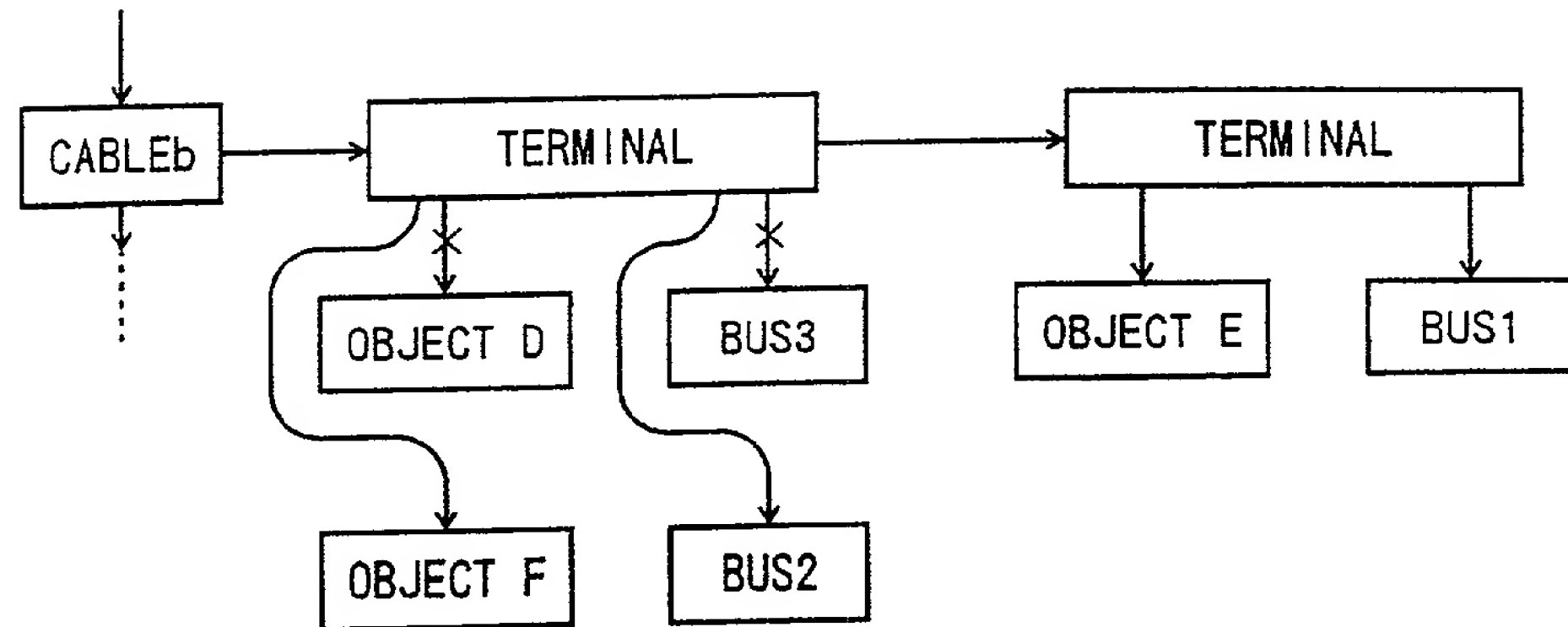


Fig. 91

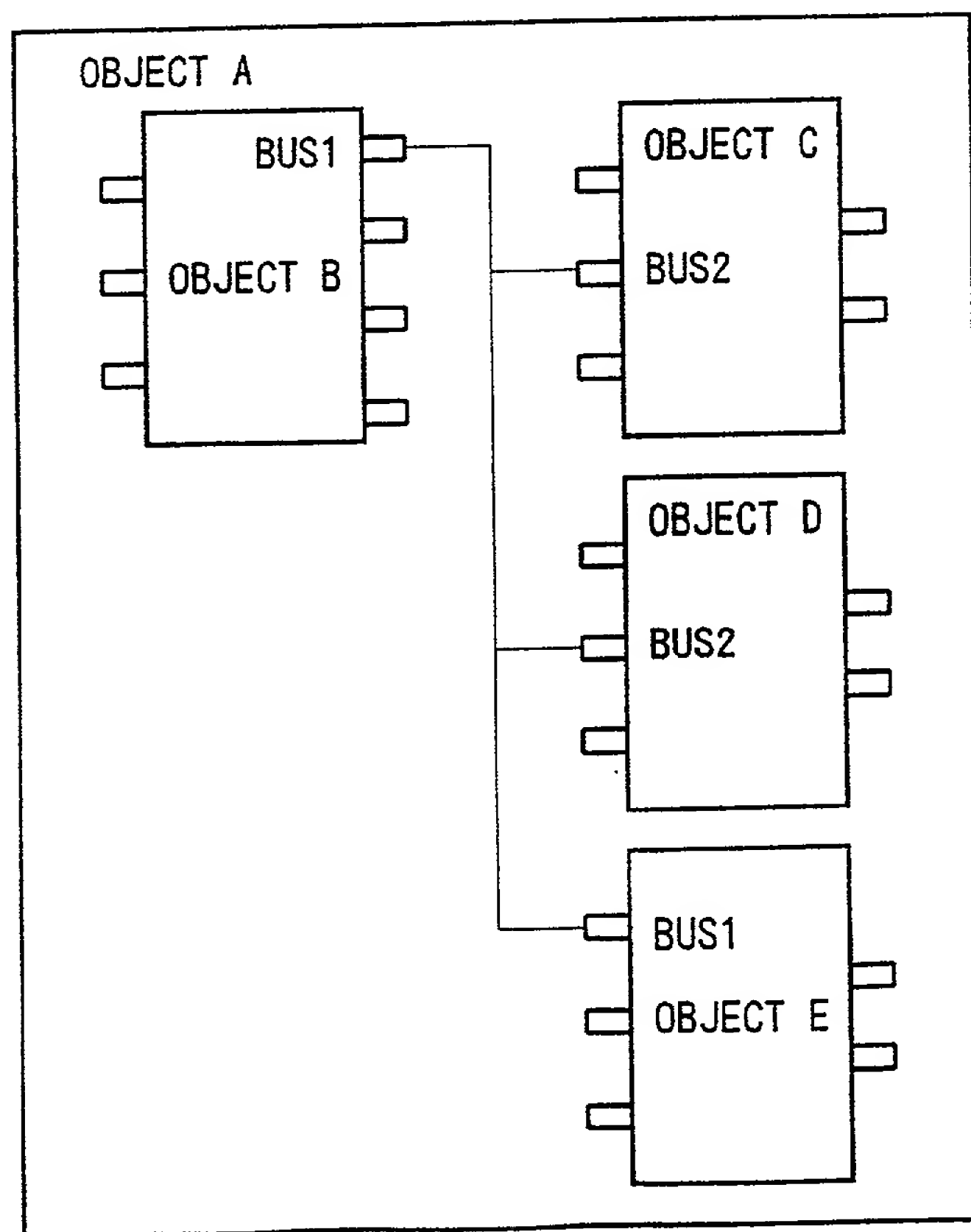
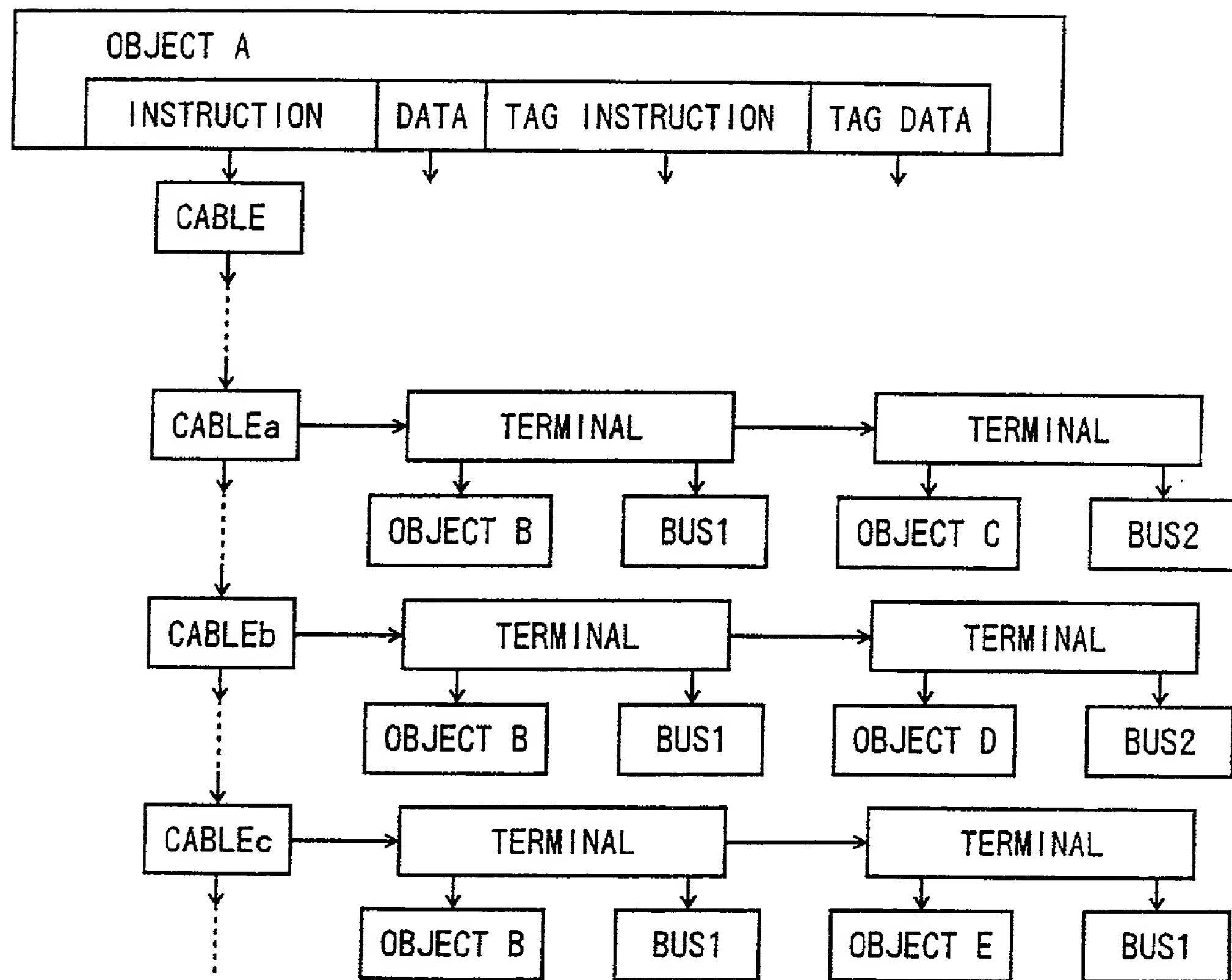


Fig. 92



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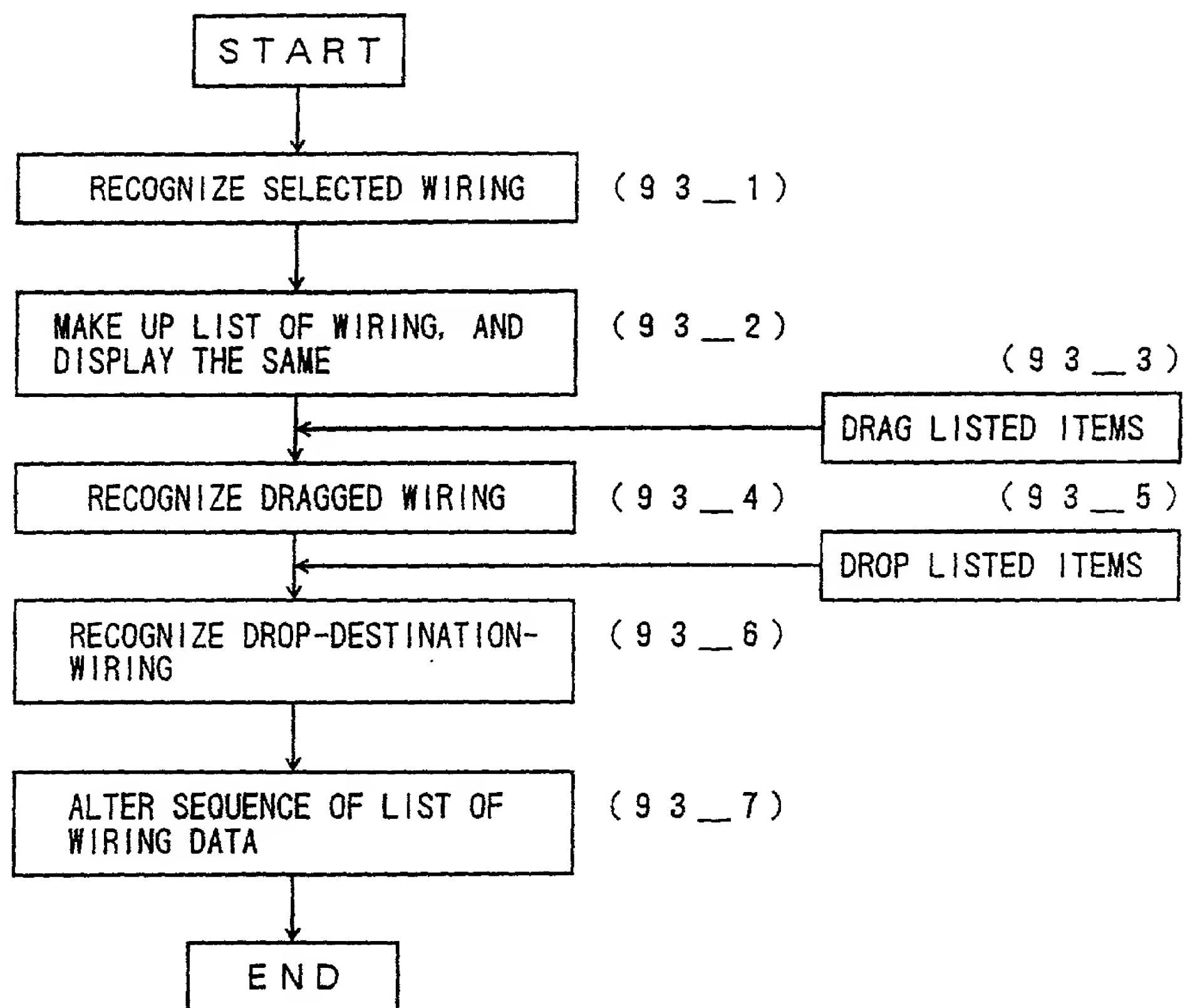


Fig. 94

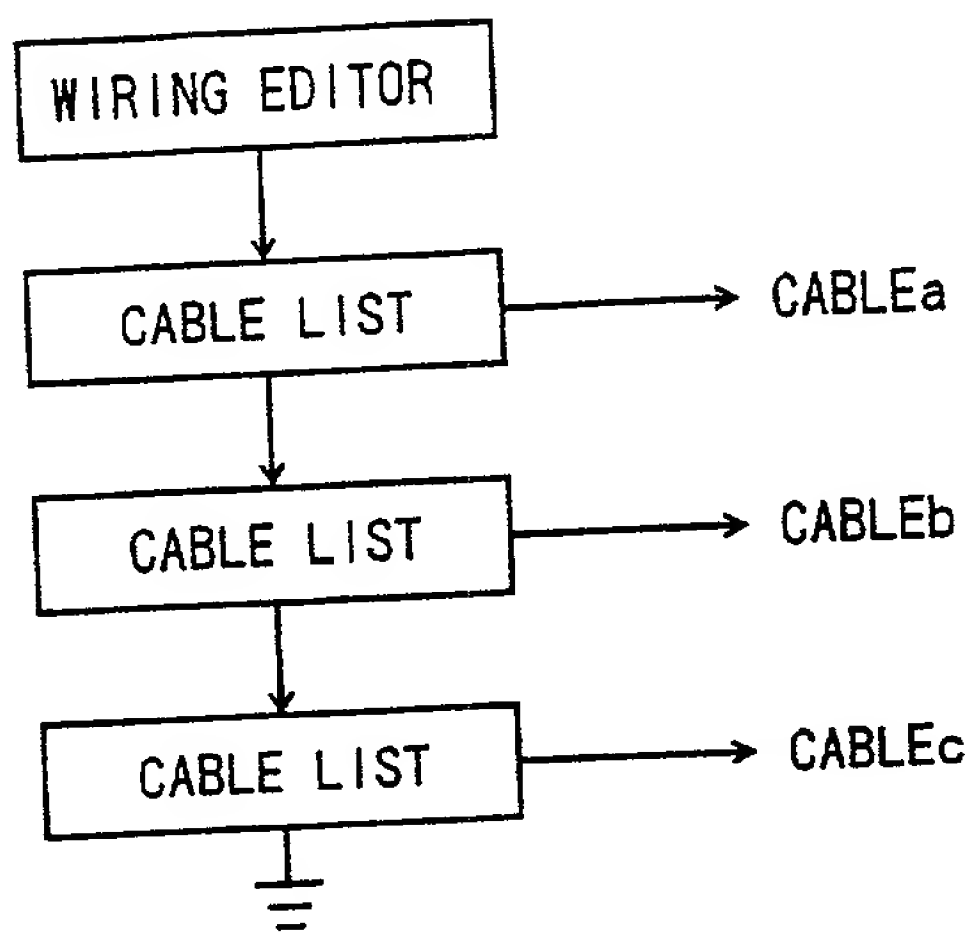


Fig. 95

OBJECT B : BUS1	OBJECT C : BUS2
OBJECT B : BUS1	OBJECT D : BUS2
OBJECT B : BUS1	OBJECT E : BUS1

Fig. 96

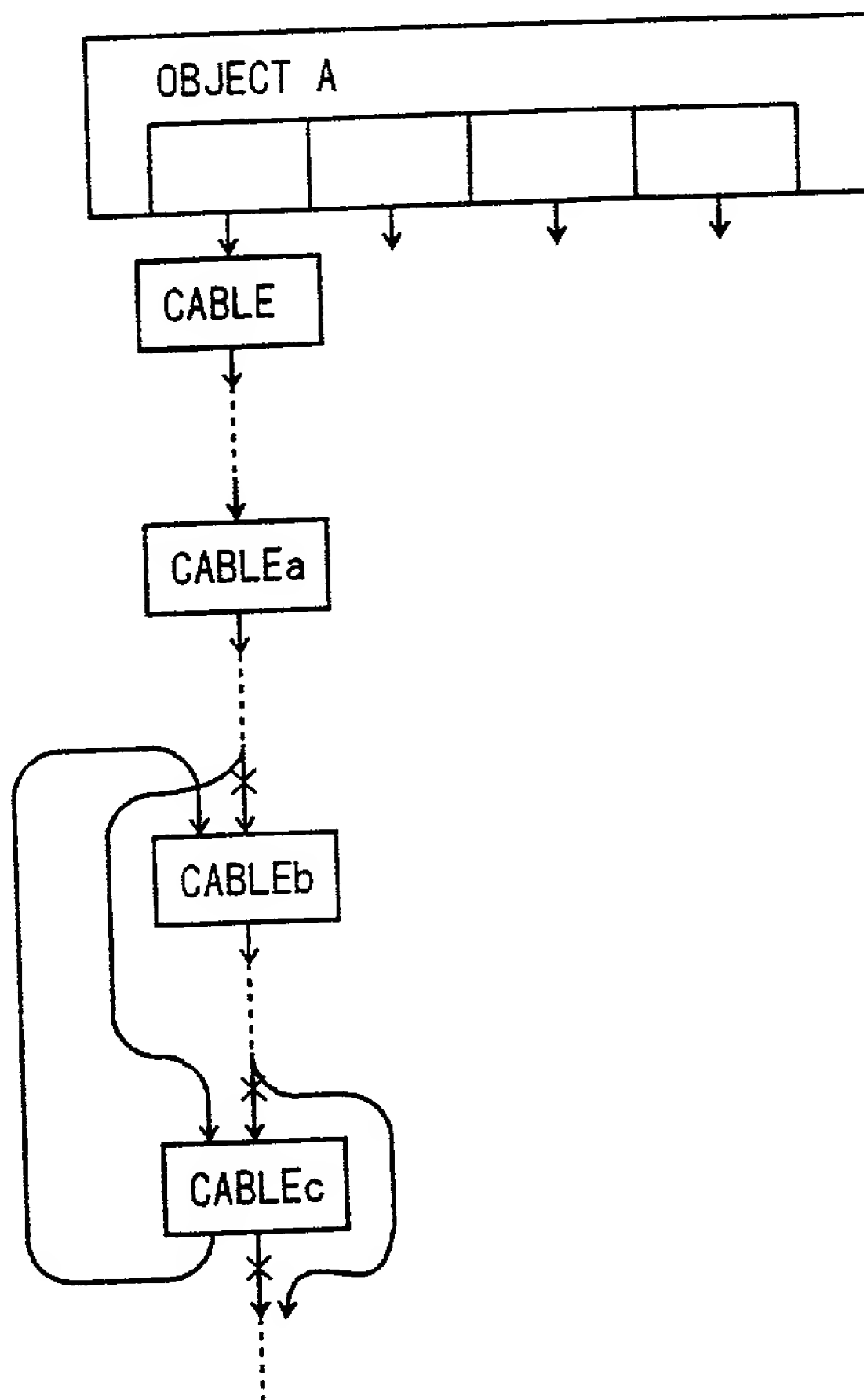


Fig. 97

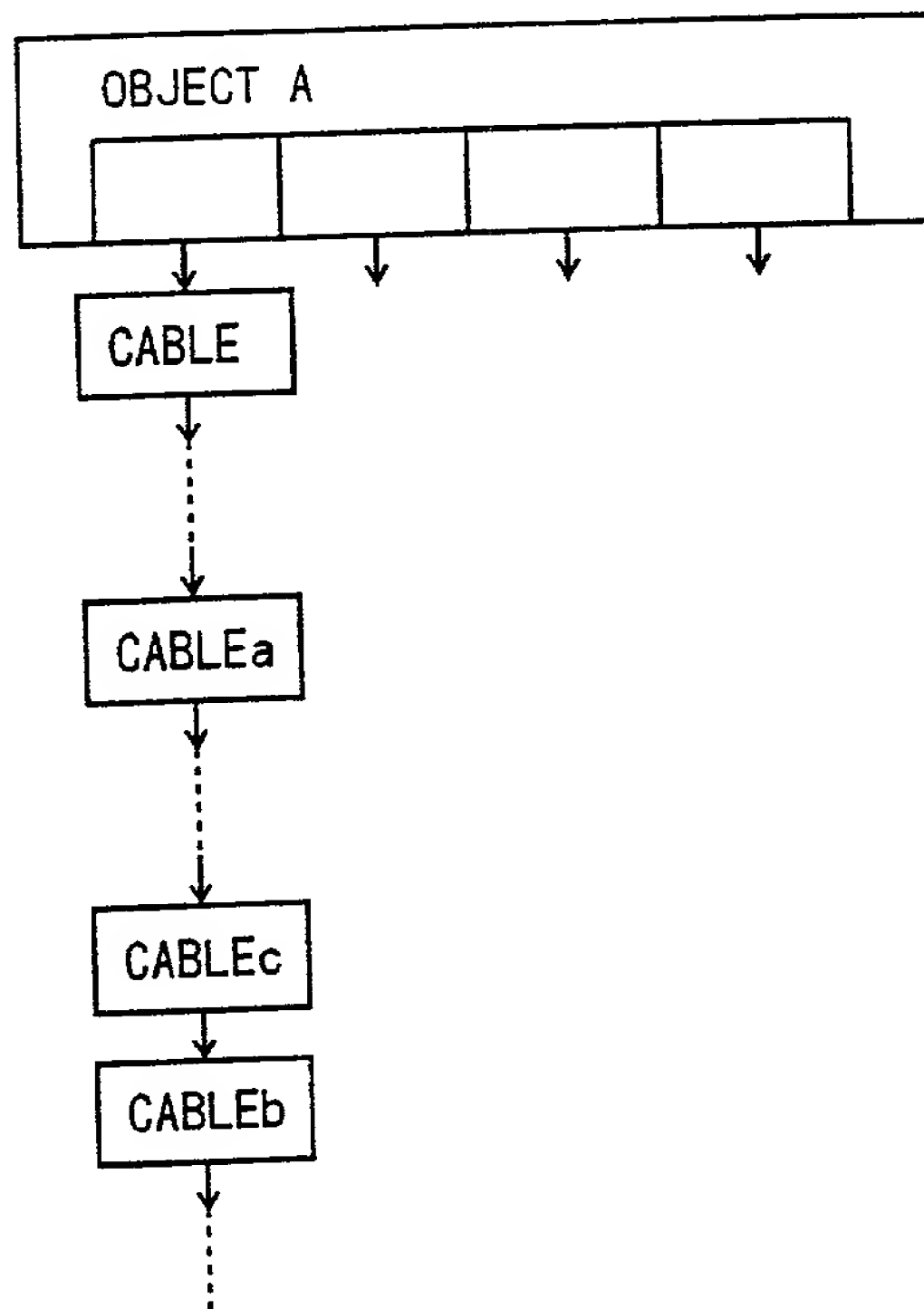


Fig. 98

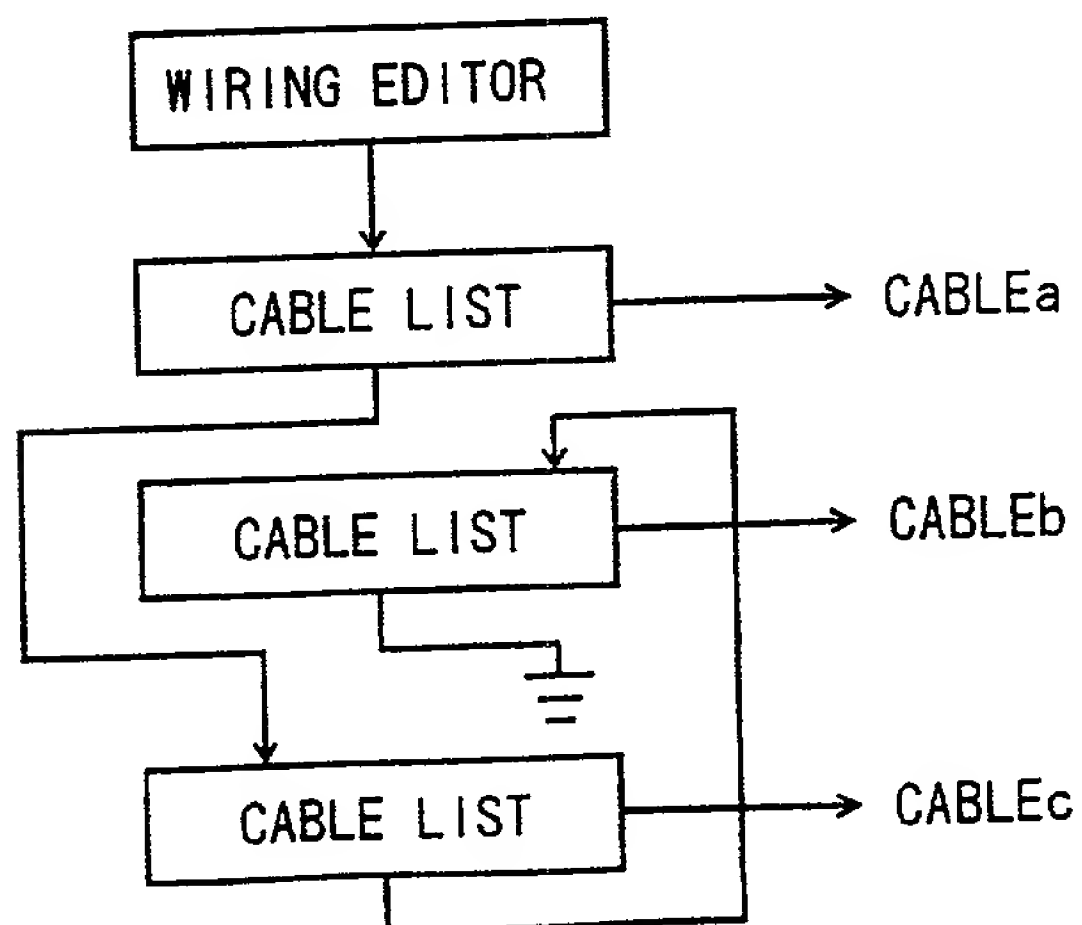


Fig. 99

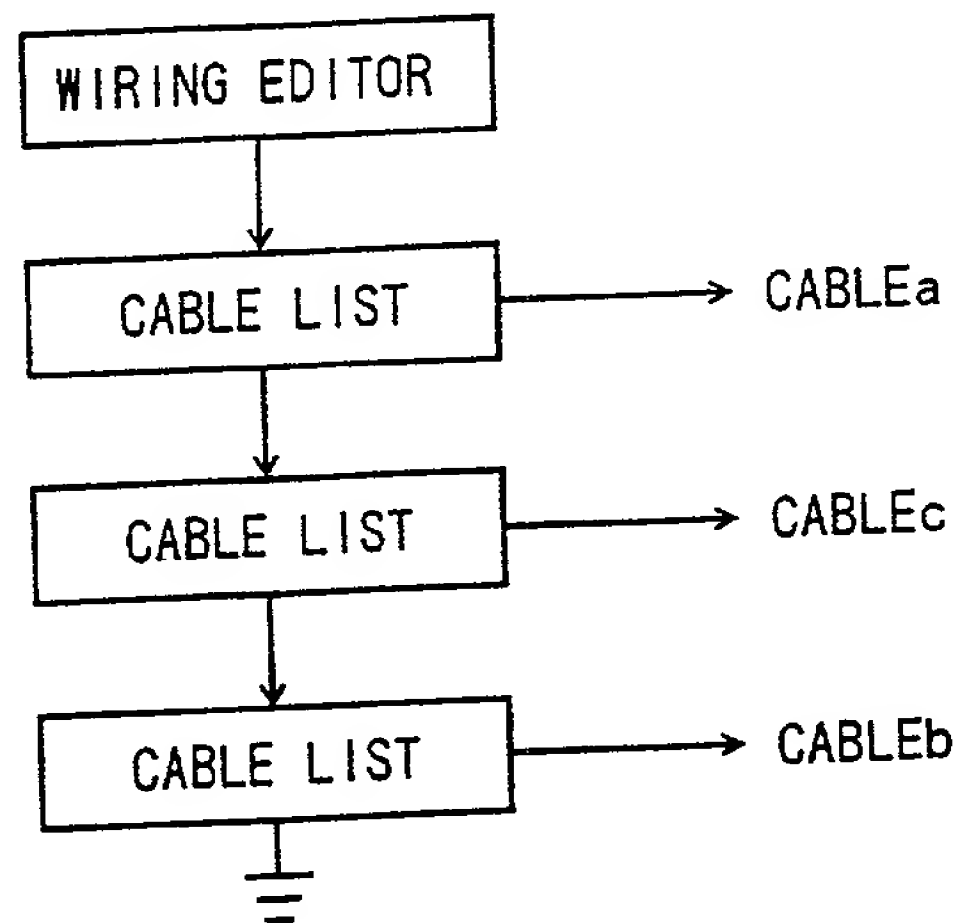


Fig. 100

OBJECT B : BUS1	OBJECT C : BUS2
OBJECT B : BUS1	OBJECT E : BUS1
OBJECT B : BUS1	OBJECT D : BUS2

Fig. 101

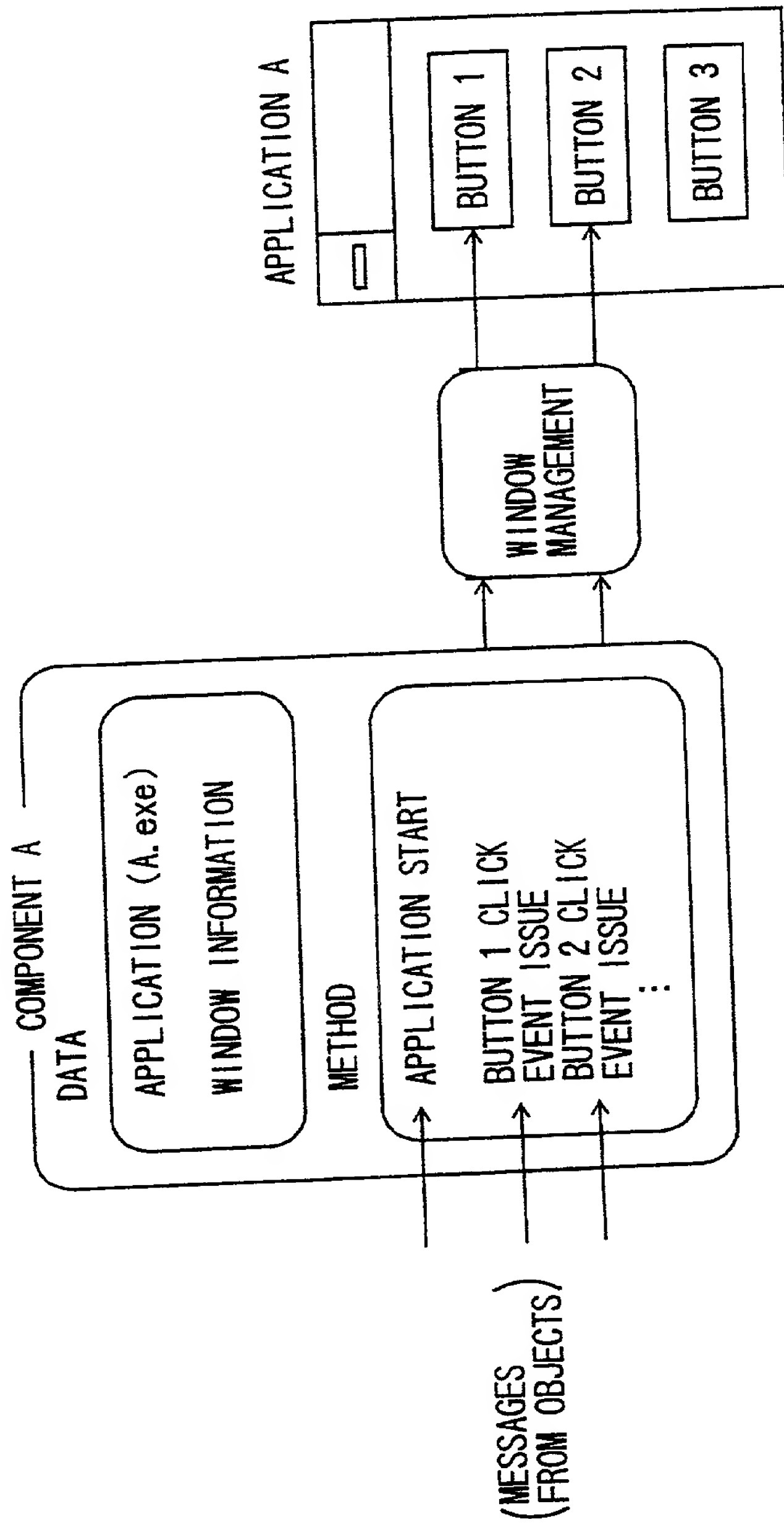


Fig. 102

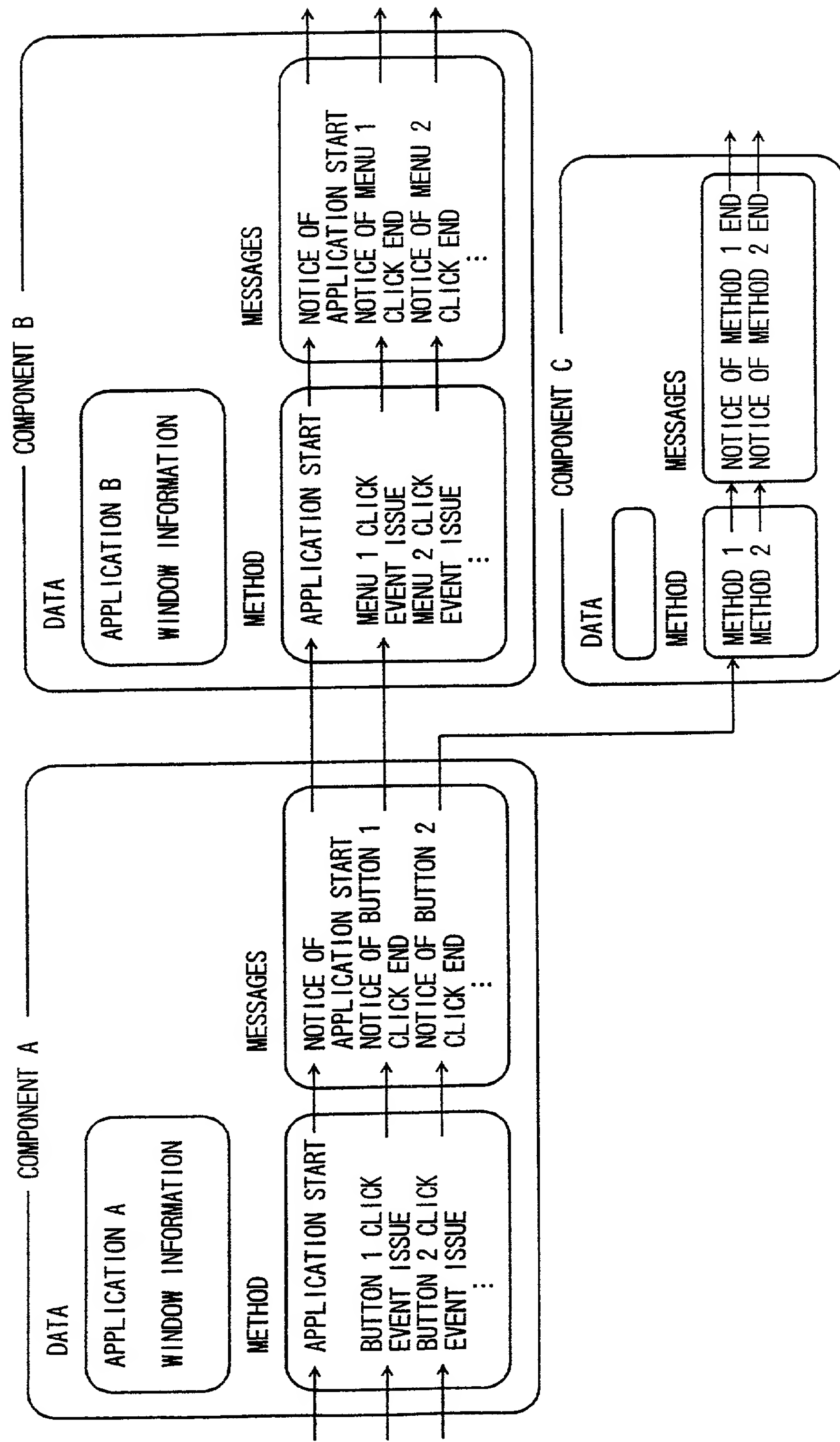


Fig. 103

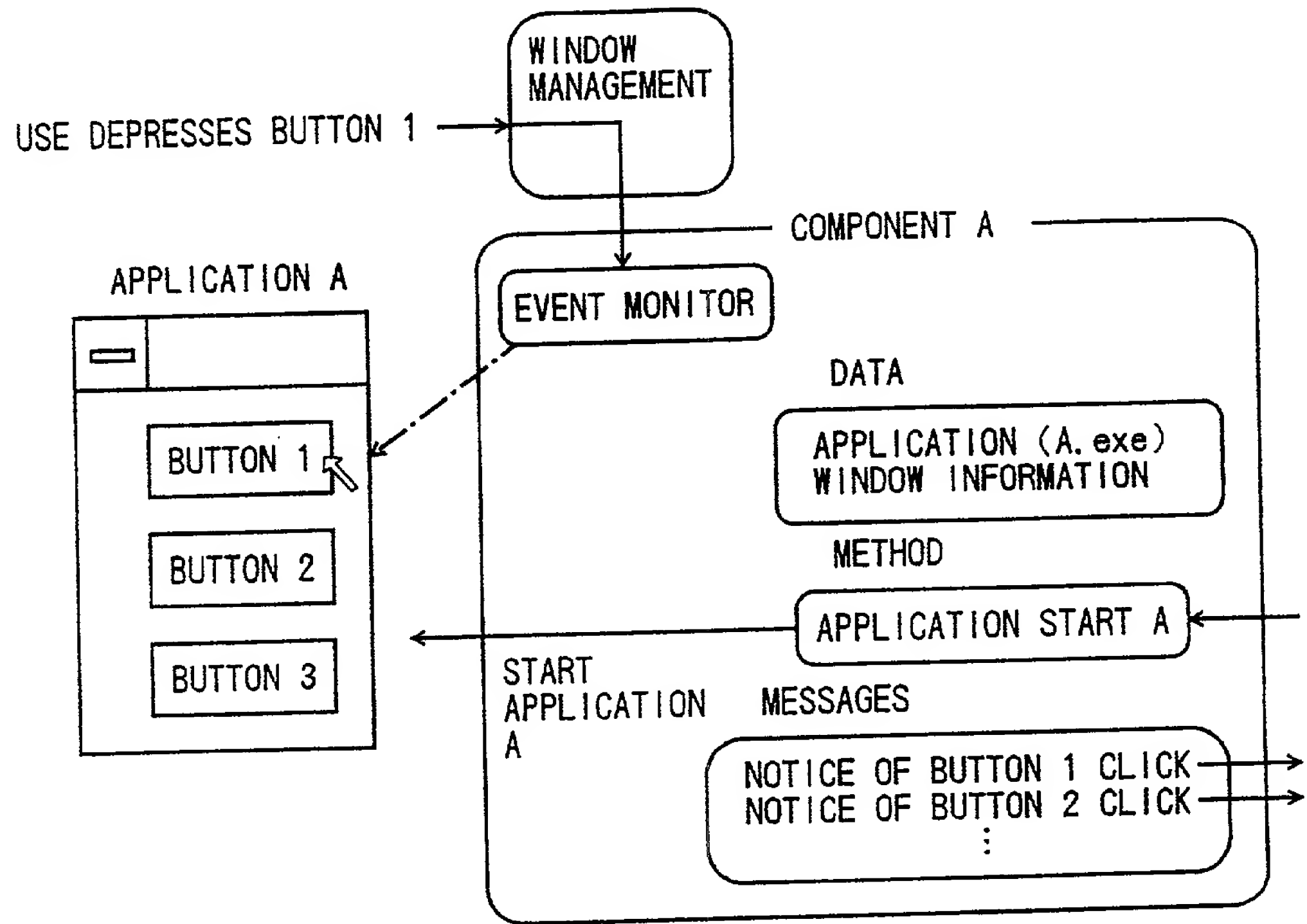


Fig. 104

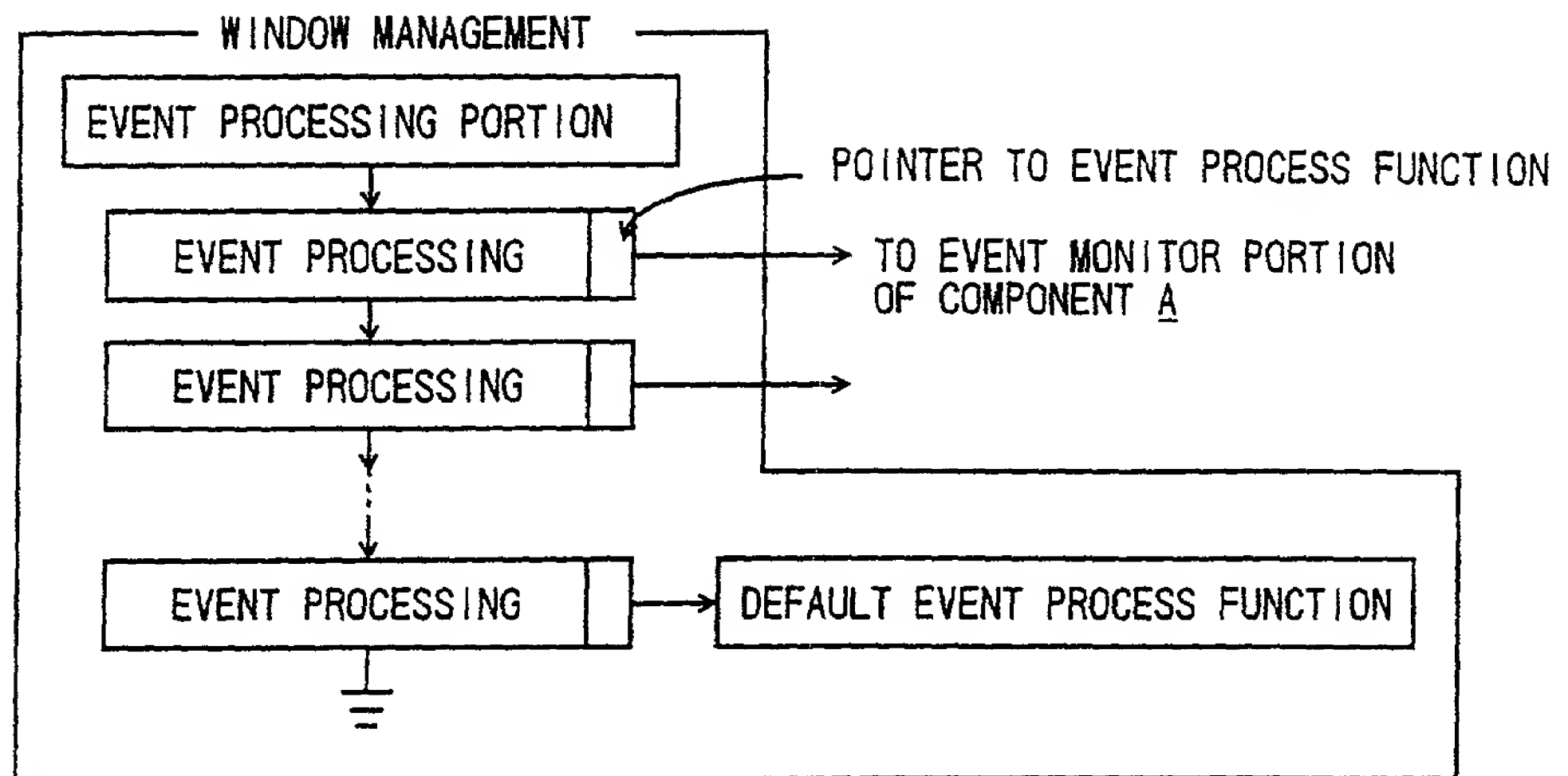


Fig. 105

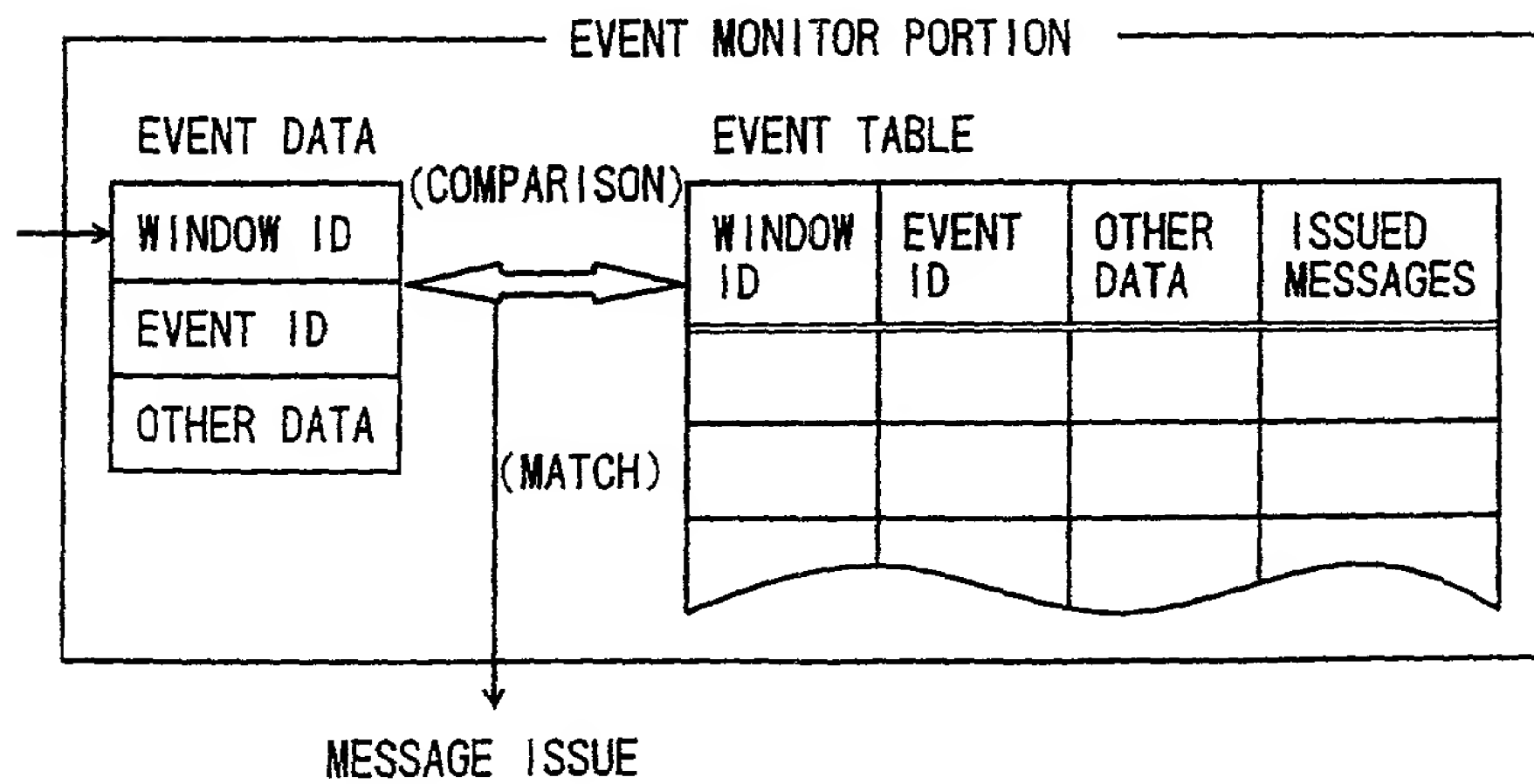


Fig. 106

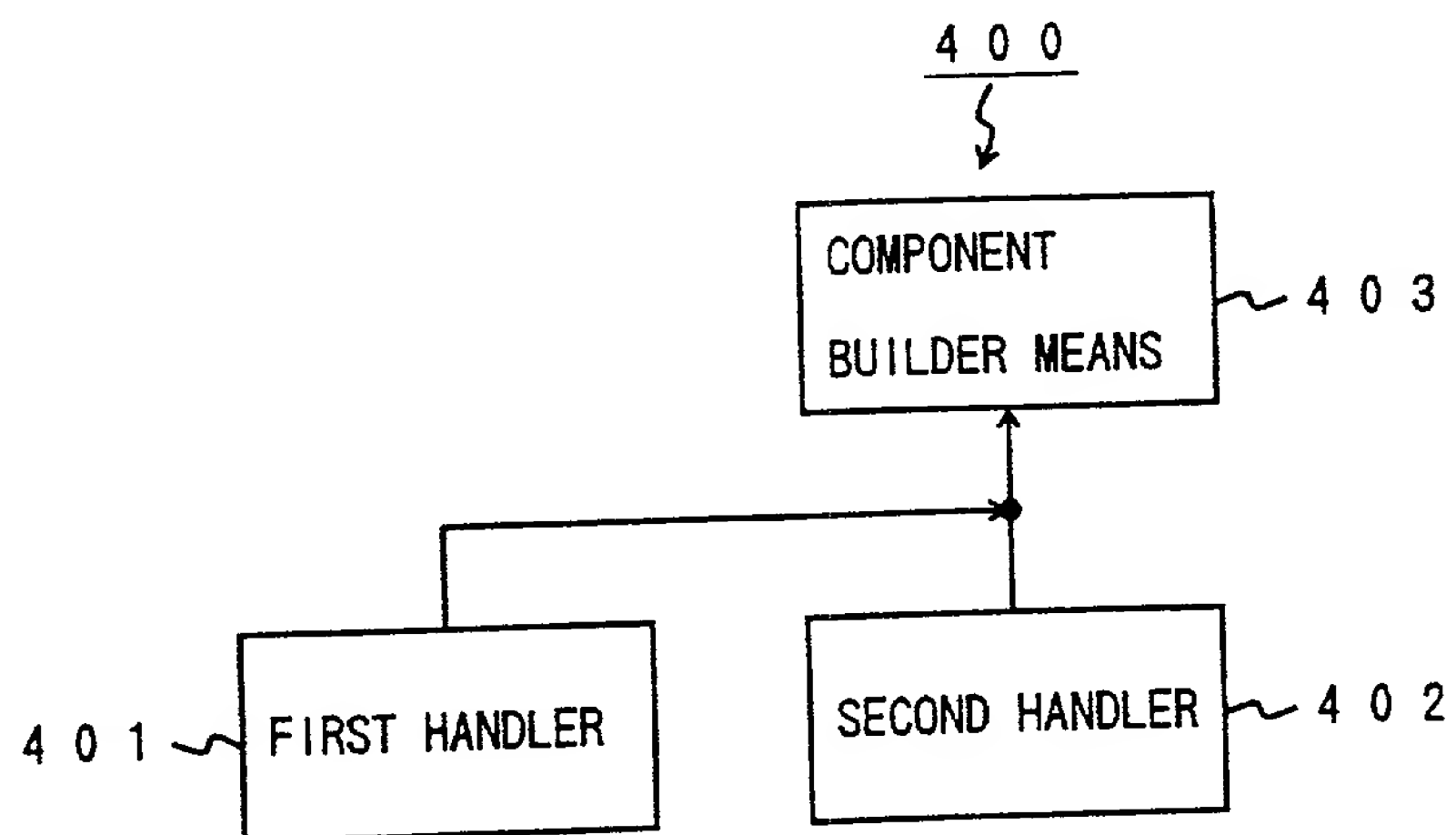


Fig. 107

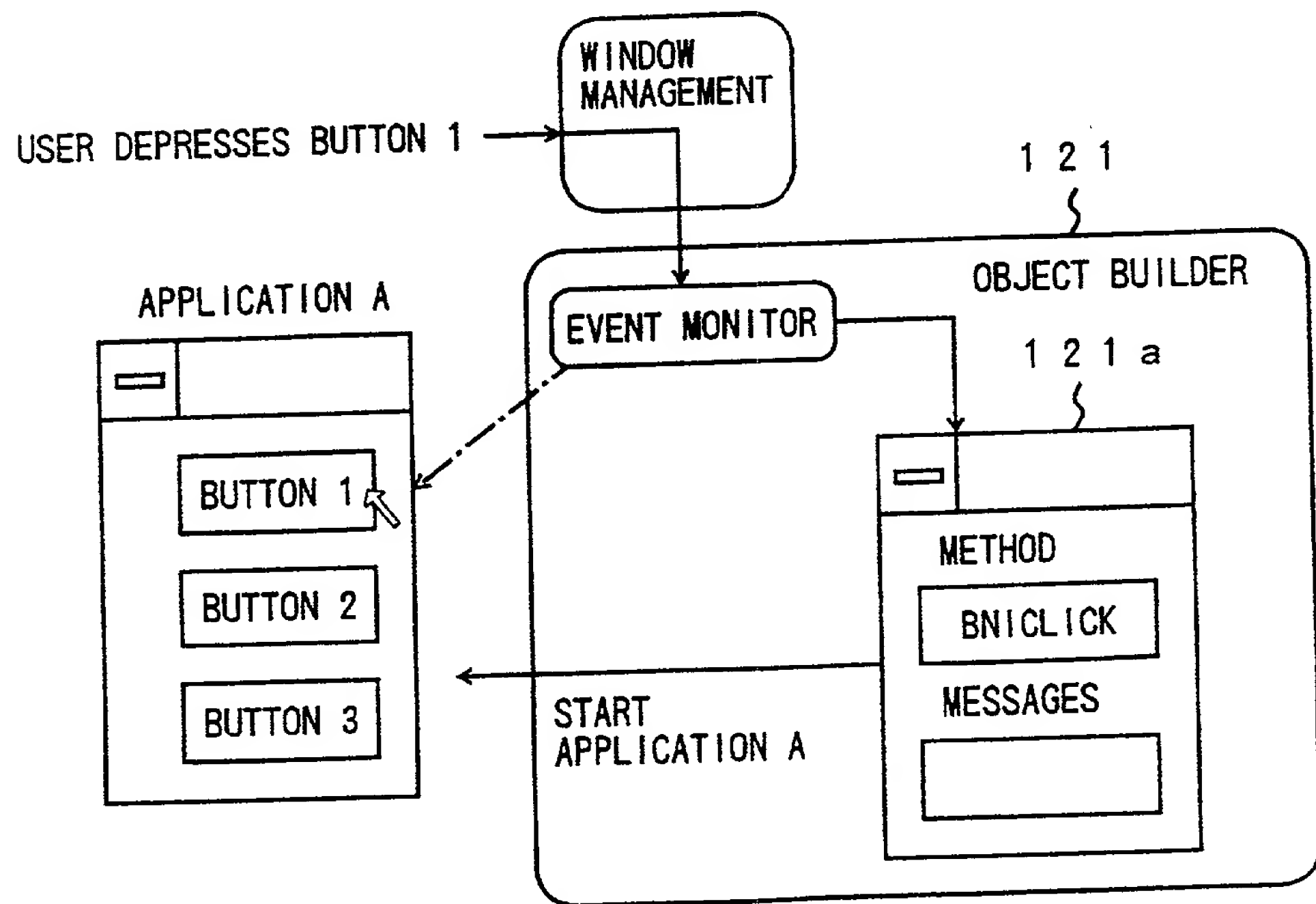


Fig. 108

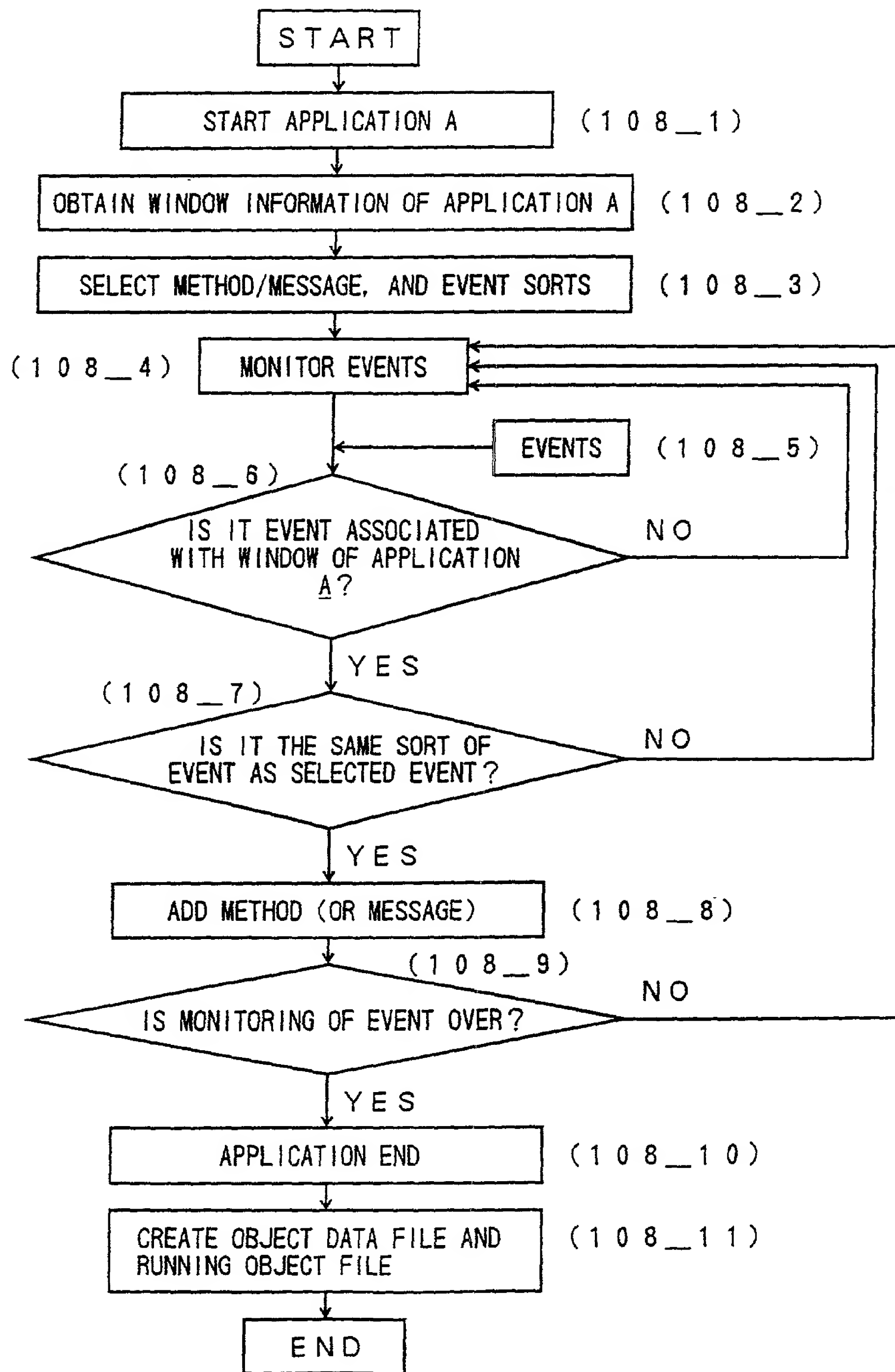
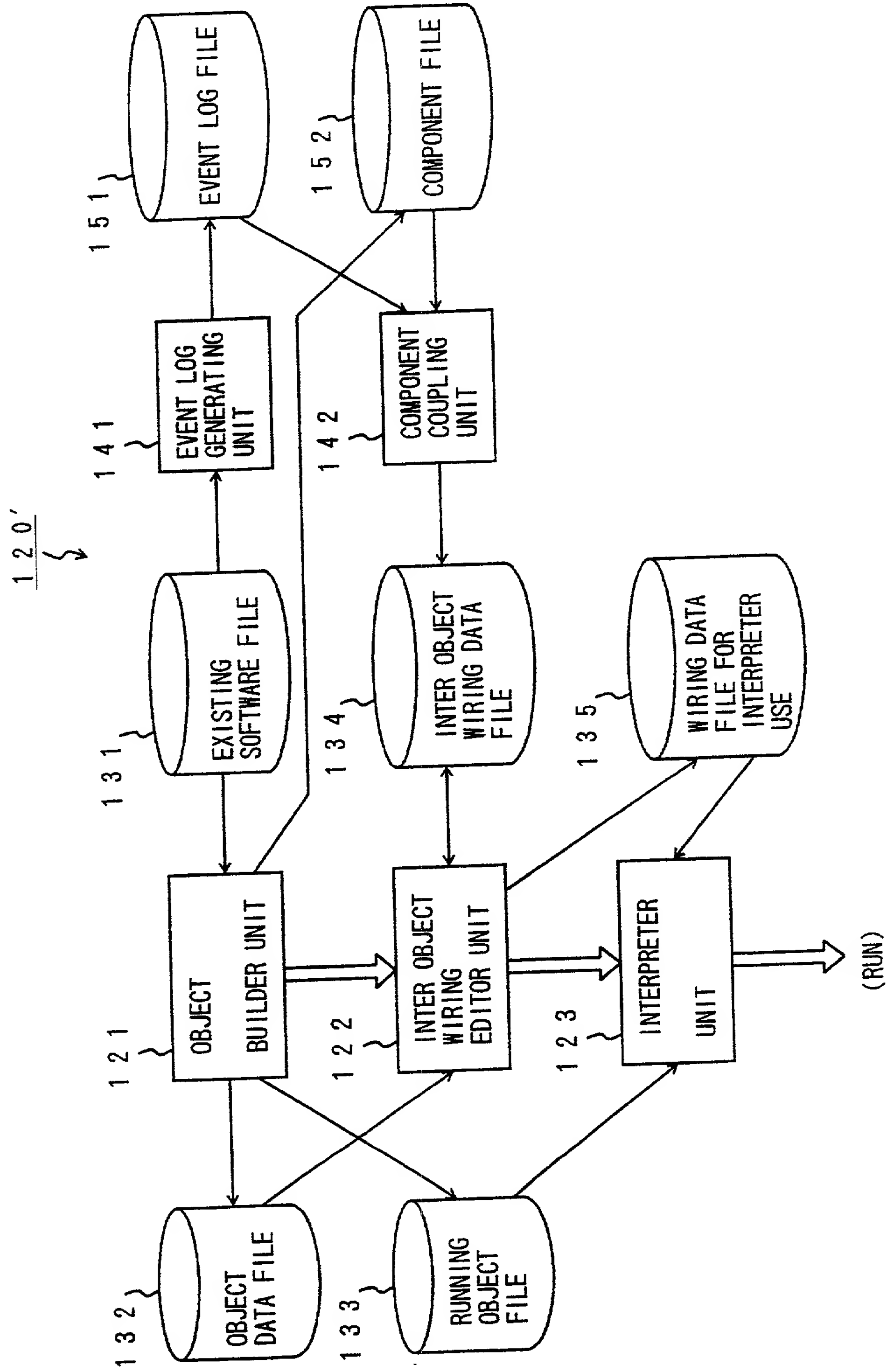


Fig. 109



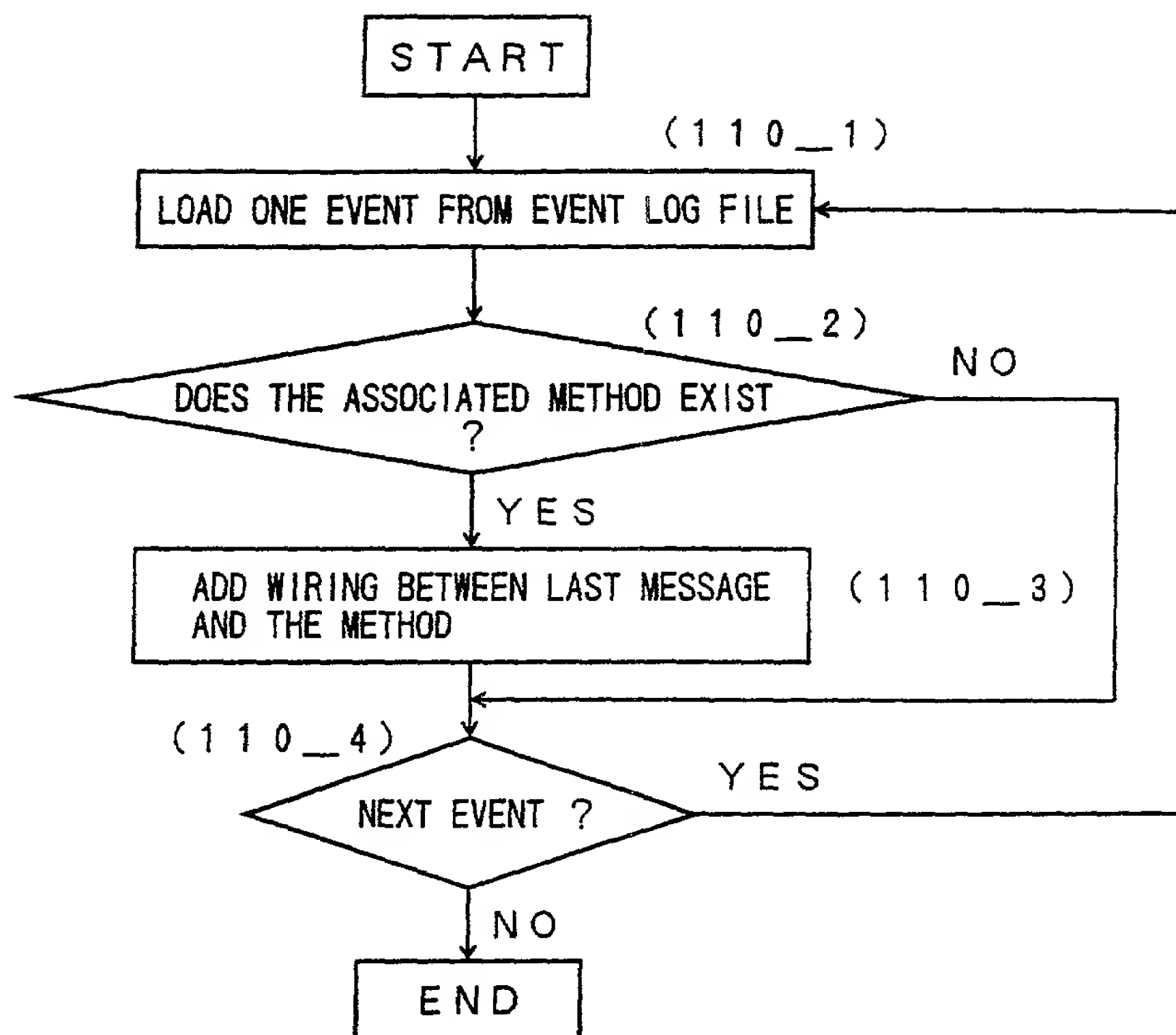
[illegible]

Fig. 111

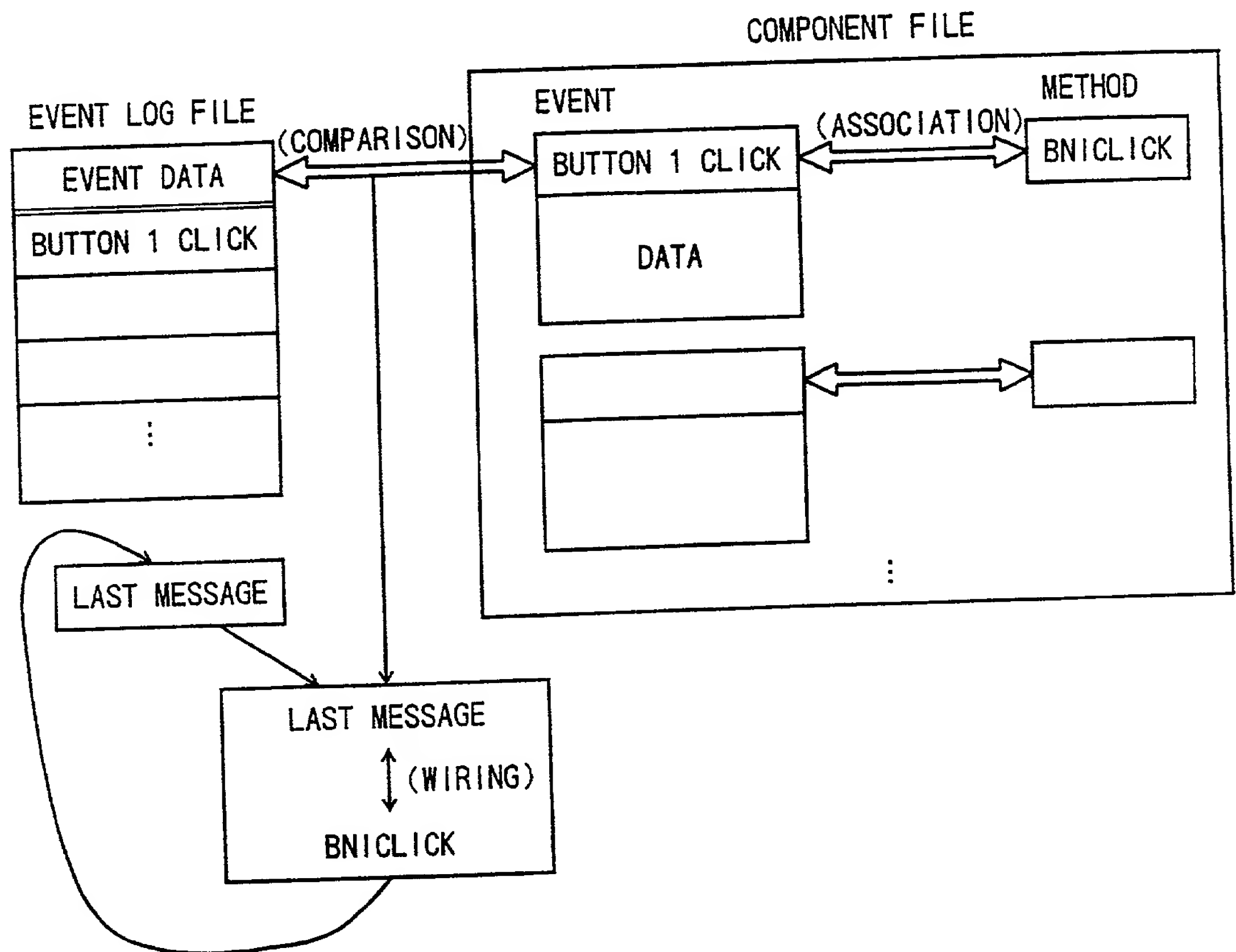


Fig. 112

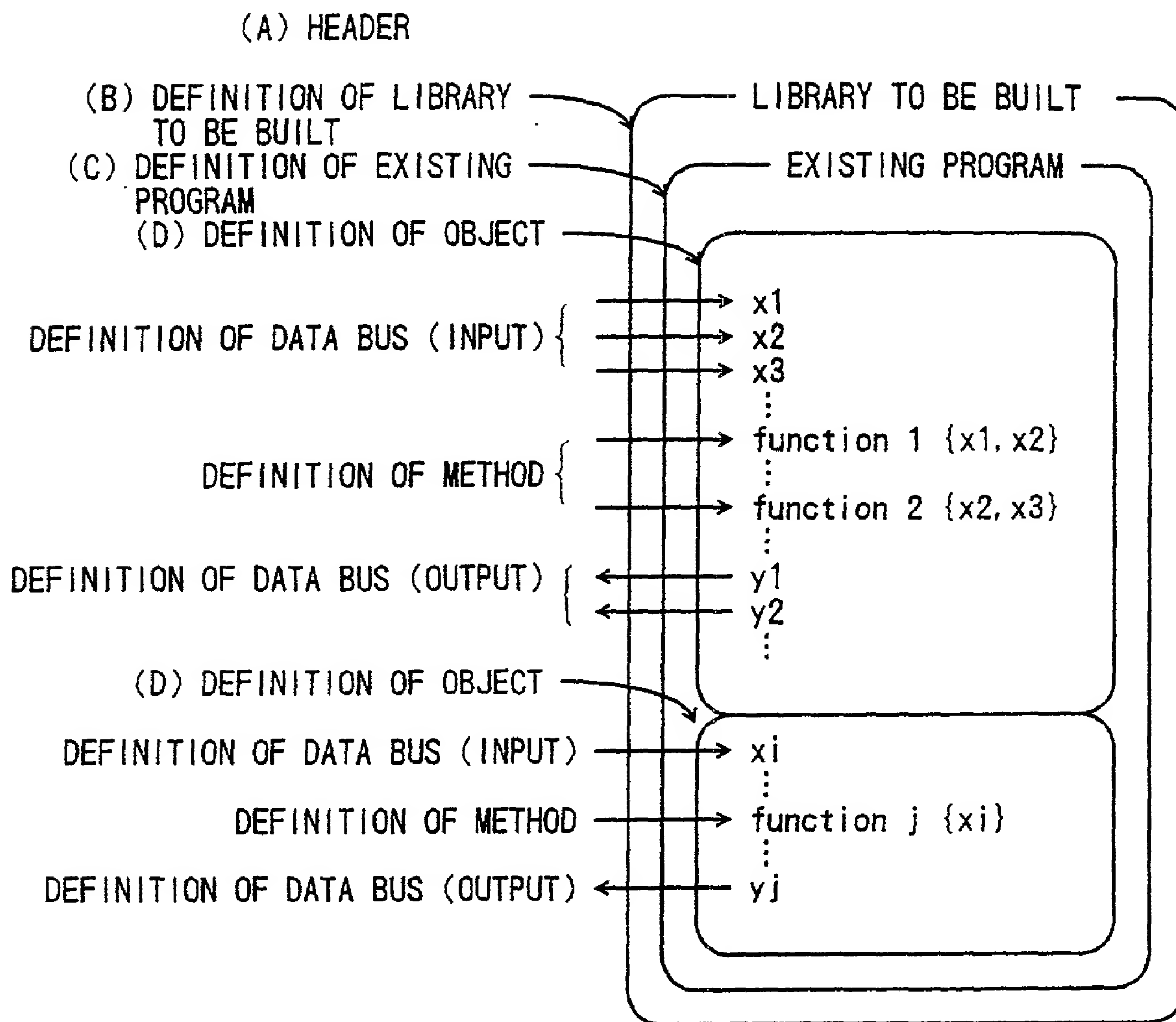


Fig. 113

	ITEMS	KEYWORDS	REMARKS
(A)	PROJECT PROJECT NAME PATH OF COMPILER SYSTEM PATH OF FIRSTSIGHT SYSTEM PATH OF USER AREA	LSIBuilderProject LSIBuilderProjectName MSVCRoot CoreRoot UserRoot	
(B)	DEFINITION OF ARCHIVES NAME OF ARCHIVES PATH OF LIB PATH OF DLL	Archives ArchivesName LibPath DllPath	
(C)	NAME OF LIBRARY TO BE BUILT COMPILE MODE DEFINITION OF #define AND typedef	LibName Debug Header	
(D)	DEFINITION OF LSI NAME OF LSI COLOR OF LSI DATA BUS NAME OF DATA CORRECTION PROCESS NAME OF DATA BUS TYPE OF VARIABLES DATA CORRECTION PROCESS DIRECT DEVELOPMENT INTO DefineConnector DISTINCTION BETWEEN INPUT AND OUTPUT COLOR OF BUS INSTRUCTION NAME OF INSTRUCTION BUS FUNCTION NAME OF ENTRY POINT MEANING OF RETURN VALUE INSTRUCTION PROCESS Cmd ? DIRECT DEVELOPMENT INTO Cmd OR Command COLOR OF BUS GLOBAL VARIABLES (GLOBAL VARIABLES INSIDE LSI) DEFINITION #define AND typedef INITIALIZATION PROCESS CONSTRUCTOR DESTRUCTOR	LSI LSIName Color DataBus ProcessName Name VariableType Process Inline IO Color InstBus Name ProcessName ReturnValue Process Cmd Inline Colo Variables Header Initialize Constructor Destructor	TREE COLORS OF RGB (0-255) input OR output zero OR nonzero OR NUMERAL CODE OF FUNCTION yes/no CODE OF FUNCTION CODE OF FUNCTION CODE OF FUNCTION

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Fig. 114

FILE	EDITING	DISPLAY	MAKE	HELP																																								
<div><div><ul style="list-style-type: none">○ project NAME○ information○ archives NAME○ information○ LSI NAME○ information○ instruction○ data○ LSI NAME○ information○ instruction○ data○ archives NAME○ information○ LSI NAME○ information○ instruction○ data○ LSI NAME○ information○ instruction○ data</div><div><div>LSI INFORMATION</div><table><thead><tr><th>LSI NAME</th><th>LSI COLOR</th><th>define...</th><th>...</th></tr></thead><tbody><tr><td></td><td></td><td></td><td></td></tr></tbody></table><div>INSTRUCTION BUS TABLE:</div><table><thead><tr><th>BUS NAME</th><th>ENTRY ...</th><th>RETURN VALUE...</th><th>...</th></tr></thead><tbody><tr><td>...</td><td></td><td></td><td></td></tr><tr><td>...</td><td></td><td></td><td></td></tr><tr><td></td><td></td><td></td><td></td></tr></tbody></table><div>DATA BUS TABLE:</div><table><thead><tr><th>BUS NAME</th><th>TYPE OF VARIABLE</th><th>INPUT & OUTPUT...</th><th>...</th></tr></thead><tbody><tr><td>...</td><td></td><td></td><td></td></tr><tr><td>...</td><td></td><td></td><td></td></tr><tr><td></td><td></td><td></td><td></td></tr></tbody></table></div></div>					LSI NAME	LSI COLOR	define...	...					BUS NAME	ENTRY ...	RETURN VALUE...								BUS NAME	TYPE OF VARIABLE	INPUT & OUTPUT...							
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